

Micro Commercial Components

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Features

- Low Current Leakage
- Metalurgically Bonded Construction
- Low Cost
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0

um Ratings Maxi

- Operating Temperature: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Maximum Thermal Resistance; 50 °C/W Junction To Lead

MCC Catalog Number	Device Marking	Maximum Recurrent Peak Reverse Voltage	Maximum RMS Voltage	Maximum DC Blocking Voltage
RL101	RL101	50V	35V	50V
RL102	RL102	100V	70V	100V
RL103	RL103	200V	140V	200V
RL104	RL104	400V	280V	400V
RL105	RL105	600V	420V	600V
RL106	RL106	800V	560V	800V
RL107	RL107	1000V	700V	1000V

Electrical Characteristics @ 25°C Unless Otherwise Specified

Average Forward	I _{F(AV)}	1.0A	T _A = 75°C	
Current				
Peak Forward Surge	I _{FSM}	30A	8.3ms, half sine	
Current				
Maximum				
Instantaneous	VF	1.1V	I _{FM} = 1.0A;	
Forward Voltage			T _J = 25°C*	
Maximum DC				
Reverse Current At	I _R	5.0µA	T _J = 25°C	
Rated DC Blocking		50µA	T₁ = 125°C	
Voltage		•	Ŭ	
Typical Junction	CJ	15pF	Measured at	
Capacitance	_		1.0MHz, V _R =4.0V	
Pulse test: Pulse width 300 usec. Duty cycle 2%				

RL107 1 Amp Rectifier 50 - 1000 Volts

RL101

THRU



DIMENSIONS					
	INCHES		ММ		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.166	.205	4.10	5.20	
В	.080	.107	2.00	2.70	
С		.024		.60	
D	1.000		25.40		

300 µsec,

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RL101 thru RL107



Instantaneous Forward Current - Amperesversus Instantaneous Forward Voltage - Volts





Average Forward Rectified Current - Amperes/ersus Ambient Temperature $\ -^{\circ}C$



Reverse Voltage - Volts

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RL101 thru RL107



40

60 80 100



Cycles Peak Forward Surge Current - Amperesversus Number Of Cycles At 60Hz - Cycles

Figure 5

60

50

40

30

10

0

2

4 6 8 10 20

Amps 20

Peak Forward Surge Current

Instantaneous Reverse Leakage Current - MicroAmperesersus Percent Of Rated Peak Reverse Voltage - Volts







A-405 Outline Only



DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
A	.709	.748	18.0	20.0	
В	.460	.540	11.7	13.7	
С		.106		2.7	
D		.205		5.2	
E	.610	.650	15.5	16.5	
F		.354		9.0	
G		.177		4.5	
Н	.177	.217	4.5	5.5	
J	.124	.179	3.15	4.55	
K	.146	.169	3.7	4.3	
M	.677	.748	17.2	19.0	
N	.343	.384	8.70	9.75	
Р	.021	.025	.54	.64	
S		±.079		±2.0	
Т	.016	.031	. 4	.8	

PACKING METHODS

<u>P/N</u> EXAMPLE: <u>A</u>=N:PANASERT <u>A B C</u>

- $\underline{B} = 0$: NON INSULATION COATING---LEAD FIRST OUT.
 - 1: INSULATION COATING---LEAD FIRST OUT.
 - 2: NON INSULATION COATING---BODY FIRST OUT.
 - 3: INSULATION COATING---BODY FIRST OUT.
- \underline{C} = 1: FOR CATHODE DOWN, IN BULK.
 - 2: FOR CATHODE UP, IN BULK.
 - 3: FOR CATHODE DOWN, IN REEL.
 - 4: FOR CATHODE UP, IN REEL

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Revision: 4



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