TOSHIBA Diodes for Protecting Against ESD Epitaxial Planar Type

# DF2S12FU

# **Diodes for Protecting Against ESD**

- Two-pin ultra-small packages are suitable for higher mounting densities
- Small total capacitance: CT = 15pF (typ.)
- Zener voltage correspond to E24 series.

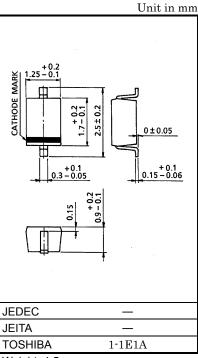
## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Power dissipation	Р	150	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual

Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 4.5 mg

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Zener voltage	$V_{Z}$	_	I <sub>Z</sub> = 5mA	11.4	12.0	12.6	V
Dynamic impedance	Z <sub>Z</sub>	_	I <sub>Z</sub> = 5mA	_	10	_	Ω
Knee dynamic impedance	Z <sub>ZK</sub>	_	I <sub>Z</sub> = 0.5mA	_	20	_	Ω
Reverse current	I <sub>R</sub>	_	V <sub>R</sub> = 9V	_	_	0.05	μΑ
Total capacitance	CT	_	V <sub>R</sub> = 0V	_	15	-	pF

#### Marking

## **Equivalent Circuit (Top View)**

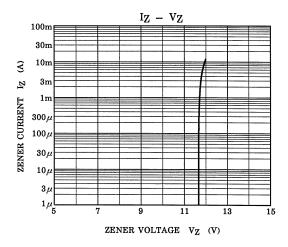


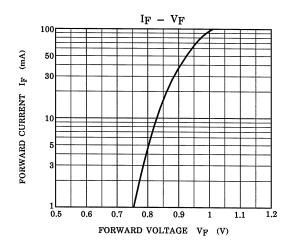


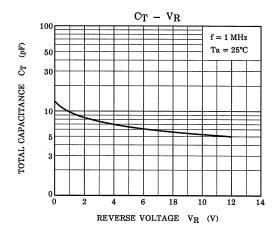
# **Guaranteed Level of ESD Immunity**

Test Condition	ESD Immunity Level		
IEC61000-4-2 (contact discharge)	$\pm20\;\mathrm{kV}$		

Criterion: No damage to device elements







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20070701-EN GENERAL

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