

RS6513

2A, Asynchronous PWM Control DC/DC Step-Down Regulator

General Description

RS6513 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to vary the duty ratio linearly from 0 up to 100%. This converter also contains an error amplifier circuit as well as a soft-start circuit that prevents overshoot at startup. An enable function, an over current protect function and a short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced from 350KHz to 30KHz. Also, an internal compensation block is built in to minimum external component count.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8 mini-package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 20V, it is also suitable for the operation via an AC adapter.

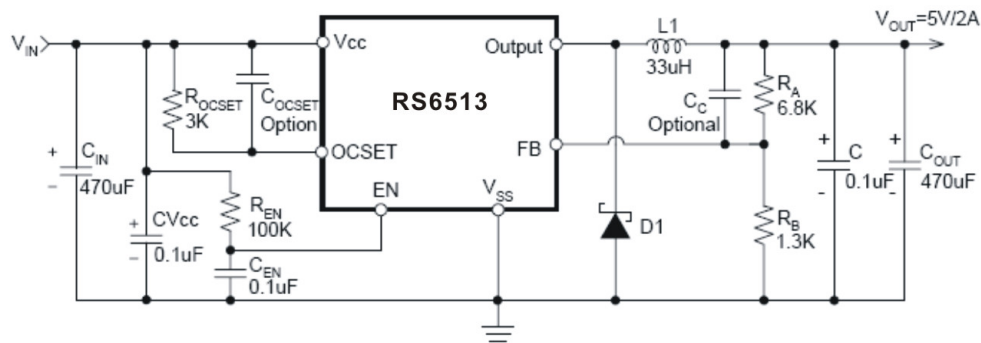
Features

- Input voltage: 3.6V to 20V
- Output voltage: 0.8V to V_{CC}
- Duty ratio: 0% to 100% PWM control
- Oscillation frequency: 350KHz typical
- Soft-start, Current limit, Enable function
- Thermal Shutdown function
- Built-in internal SW P-channel MOS
- SOP-8 package
- RoHS Compliant and 100% Lead (Pb)-Free and Green (Halogen Free with Commercial Standard)

Applications

- PC Motherboard
- LCD Monitor
- Graphic Card
- DVD-Video Player
- Telecom Equipment
- ADSL Modem
- Printer and other Peripheral Equipment
- Microprocessor core supply
- Networking power supply

Application Circuits



Note: $V_{OUT} = V_{FB} \times (1 + R_A/R_B)$
 $R_B = 0.7K \sim 5K \text{ ohm}$

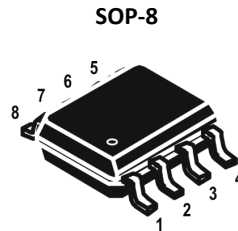
$V_{IN} = 12V, I_{MAX} = 2A$			
V_{OUT}	2.5V	3.3V	5V
L1 Value	22uH	27uH	33uH



This integrated circuit can be damaged by ESD. Orister Corporation recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Pin Assignments

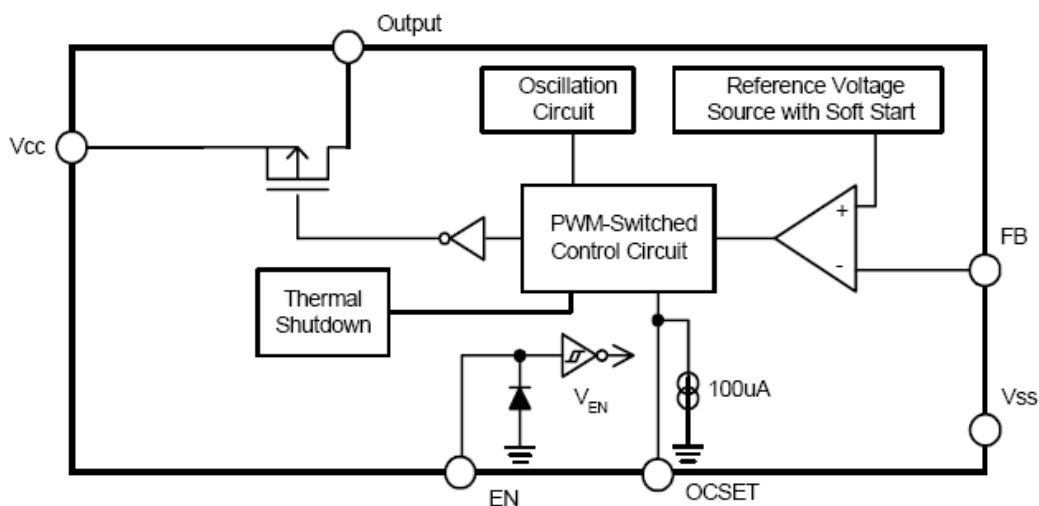


PACKAGE	PIN	SYMBOL	DESCRIPTION
SOP-8	1	FB	Feedback Pin
	2	EN	POWER OFF Pin: H: Normal operation (Step-down operation) L: Step-down operation stopped (All circuits deactivated)
	3	OCSET	Add an external resistor to set max output current
	4	VCC	Power Supply Pin
	5, 6	OUTPUT	Switch Pin. Connect external inductor/diode here. Minimize trace area at this pin to reduce EMI
	7, 8	VSS	Ground Pin

Ordering Information

DEVICE	DEVICE CODE
RS6513-XX Y Z	<p>XX is nominal output voltage designator: "Blank"=ADJ</p> <p>Y is package designator : S : SOP-8</p> <p>Z is Lead Free designator : P: Commercial Standard, Lead (Pb) Free and Phosphorous (P) Free Package G: Green (Halogen Free with Commercial Standard)</p>

Block Diagram



Absolute Maximum Ratings

Symbol	Parameter	Range	Unit
V_{CC}	VCC Pin Voltage	$V_{SS}-0.3$ to $V_{SS}+22$	V
V_{FB}	Feedback Pin Voltage	$V_{SS}-0.3$ to V_{CC}	V
V_{EN}	EN Pin Voltage	$V_{SS}-0.3$ to $V_{IN}+0.3$	V
V_{OUT}	Switch Pin Voltage	$V_{SS}-0.3$ to $V_{IN}+0.3$	V
P_D	Power Dissipation	Internally limited	mW
T_{OPR}	Operating Temperature Range	-20 to +125	°C
T_{STG}	Storage Temperature Range	-40 to +150	°C

Electrical Characteristics ($T_A=25^\circ\text{C}$, $V_{IN}=12\text{V}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V_{IN}	Input Voltage	-	3.6	-	20	V
V_{FB}	Feedback Voltage	$I_{OUT}=0.1\text{A}$	0.782	0.8	0.818	V
I_{FB}	Feedback Bias Current	$I_{OUT}=0.1\text{A}$	-	0.1	0.5	uA
I_{SW}	Switch Current	-	2.5	-	-	A
I_{SS}	Current Consumption During Power Off	$V_{EN}=0\text{V}$	-	10	-	uA
$\Delta V_{OUT}/V_{OUT}$	Line Regulation	$V_{IN}=5\text{V}$ to 18V	-	2	4	%
$\Delta V_{OUT}/V_{OUT}$	Load Regulation	$I_{OUT}=0.1$ to 2A	-	0.2	0.5	%
$V_{DROPOUT}$	V_{IN} to V_{OUT} Dropout Voltage	$V_{IN}=3.6\text{V}$, $I_{OUT}=2\text{A}$	-	0.5	-	V
F_{OSC}	Oscillation Frequency	Measure waveform at SW pin	300	350	400	KHz
F_{OSC1}	Frequency of Current Limit or Short Circuit Protect	Measure waveform at SW pin	10	-	-	KHz
V_{SH}	EN Pin Input Voltage	Evaluate oscillation at SW pin	2.0	-	-	V
V_{SL}		Evaluate oscillation stop at SW pin	-	-	0.8	
I_{SH}	EN Pin Input Leakage Current	-	-	20	-	uA
I_{SL}		-	-	-10	-	uA
I_{OCSET}	OCSET Pin Bias Current	-	75	90	105	uA
T_{SS}	Soft-Start Time	-	0.3	2	5	ms
$R_{DS(ON)}$	Internal MOSFET $R_{DS(ON)}$	$V_{IN}=5\text{V}$, $V_{FB}=0\text{V}$	-	110	150	mΩ
		$V_{IN}=12\text{V}$, $V_{FB}=0\text{V}$	-	70	100	
EFFI	Efficiency	$V_{IN}=12\text{V}$, $V_{OUT}=5\text{V}$, $I_{OUT}=2\text{A}$	-	90	-	%
θ_{JA}	Thermal Resistance Junction-to-Ambient	-	-	65	-	°C/W

Detail Description

PWM Control

The RS6513 consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the RS6513, the pulse width varies in a range from 0 to 100%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

Under Voltage Lockout

The under voltage lockout circuit of the RS6513 assures that the high-side MOSFET driver outputs remain in the off state whenever the supply voltage drops below 3.3V. Normal operation resumes once V_{CC} rises above 3.5V.

$R_{DS(ON)}$ Current Limiting

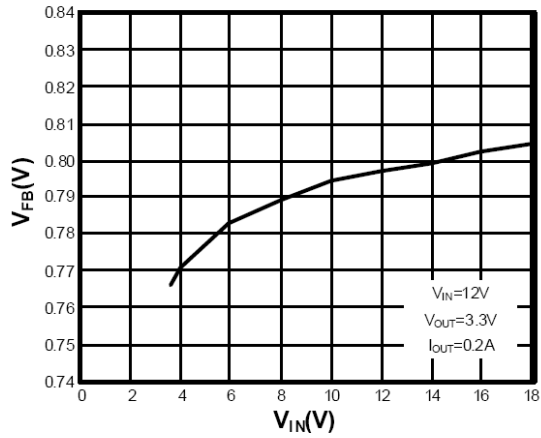
The current limit threshold is setting by the external resistor connecting from V_{CC} supply to OCSET. The internal 100uA sink current crossing the resistor sets the voltage at the pin of OCSET. When the PWM voltage is less than the voltage at OCSET, an over-current condition is triggered.

$$I_{LOAD} \times R_{DS(ON)} = I_{OCSET} \times R_{OCSET}$$

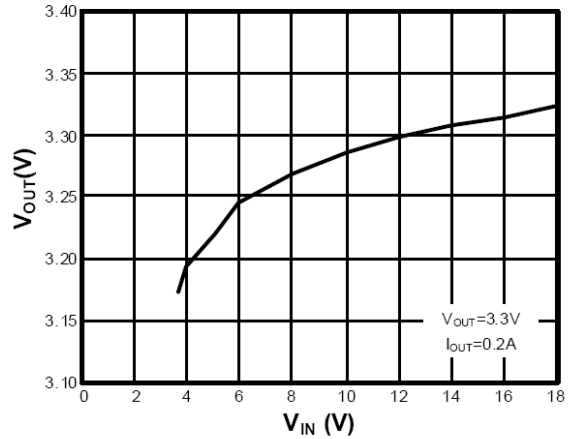
See above formula for setting the current limit value.

Typical Performance Characteristics

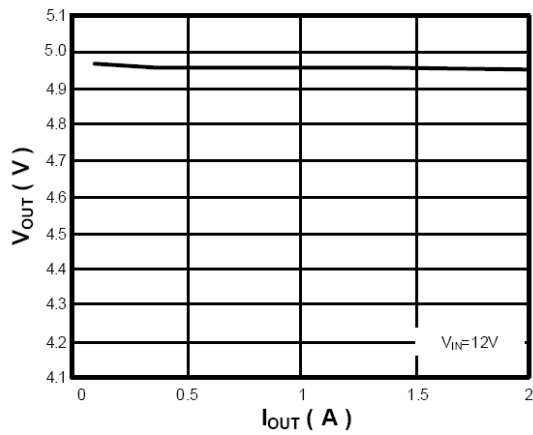
V_{in} vs. FB



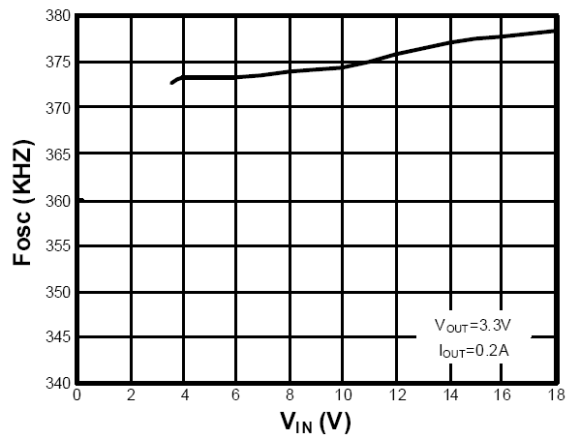
Line Regulation



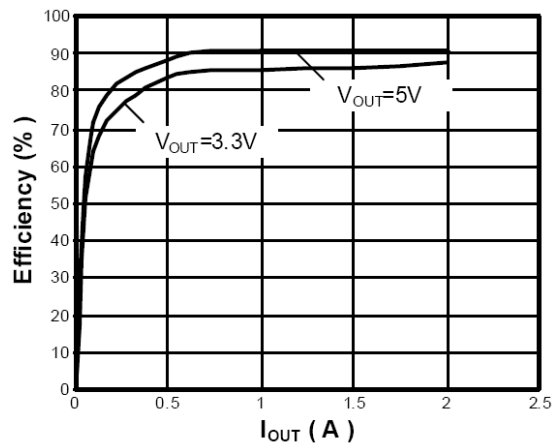
Load Regulation



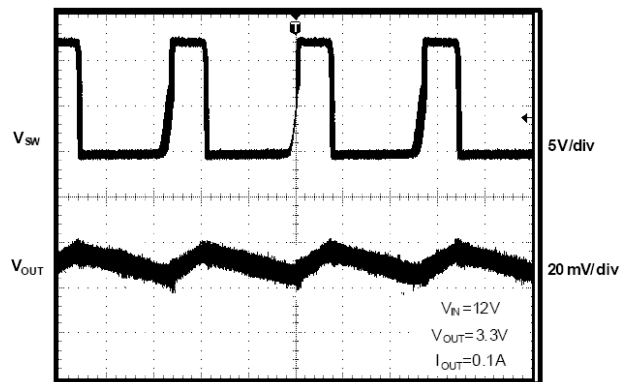
V_{IN} v.s Frequency



Efficiency

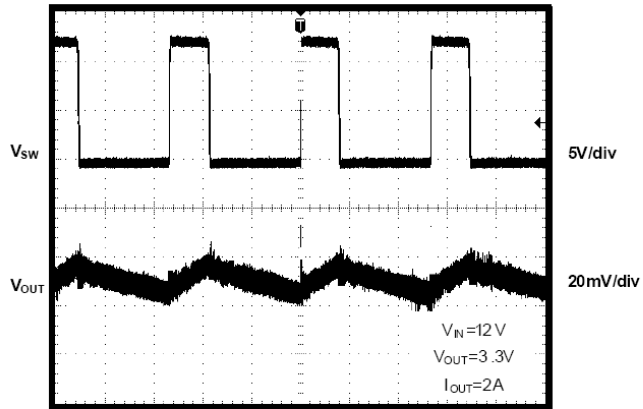


V_{out} Ripple

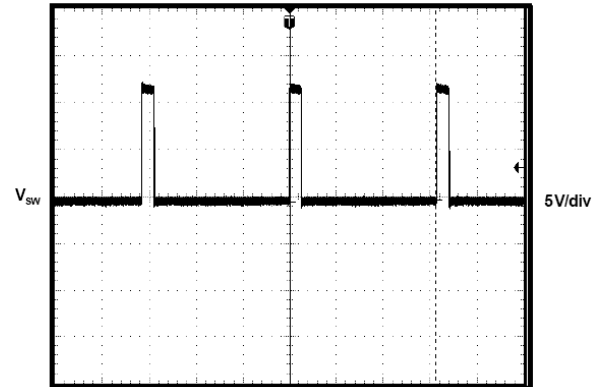


Typical Performance Characteristics

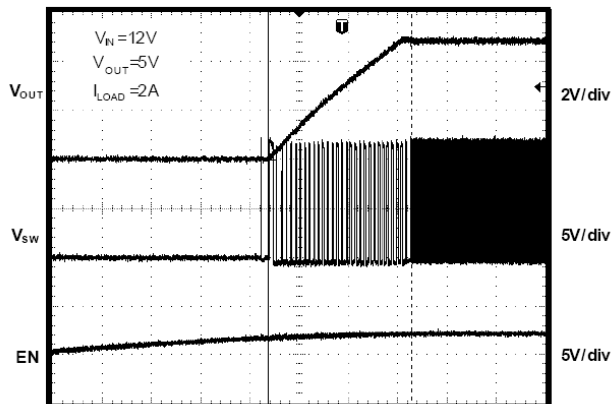
Vout Ripple



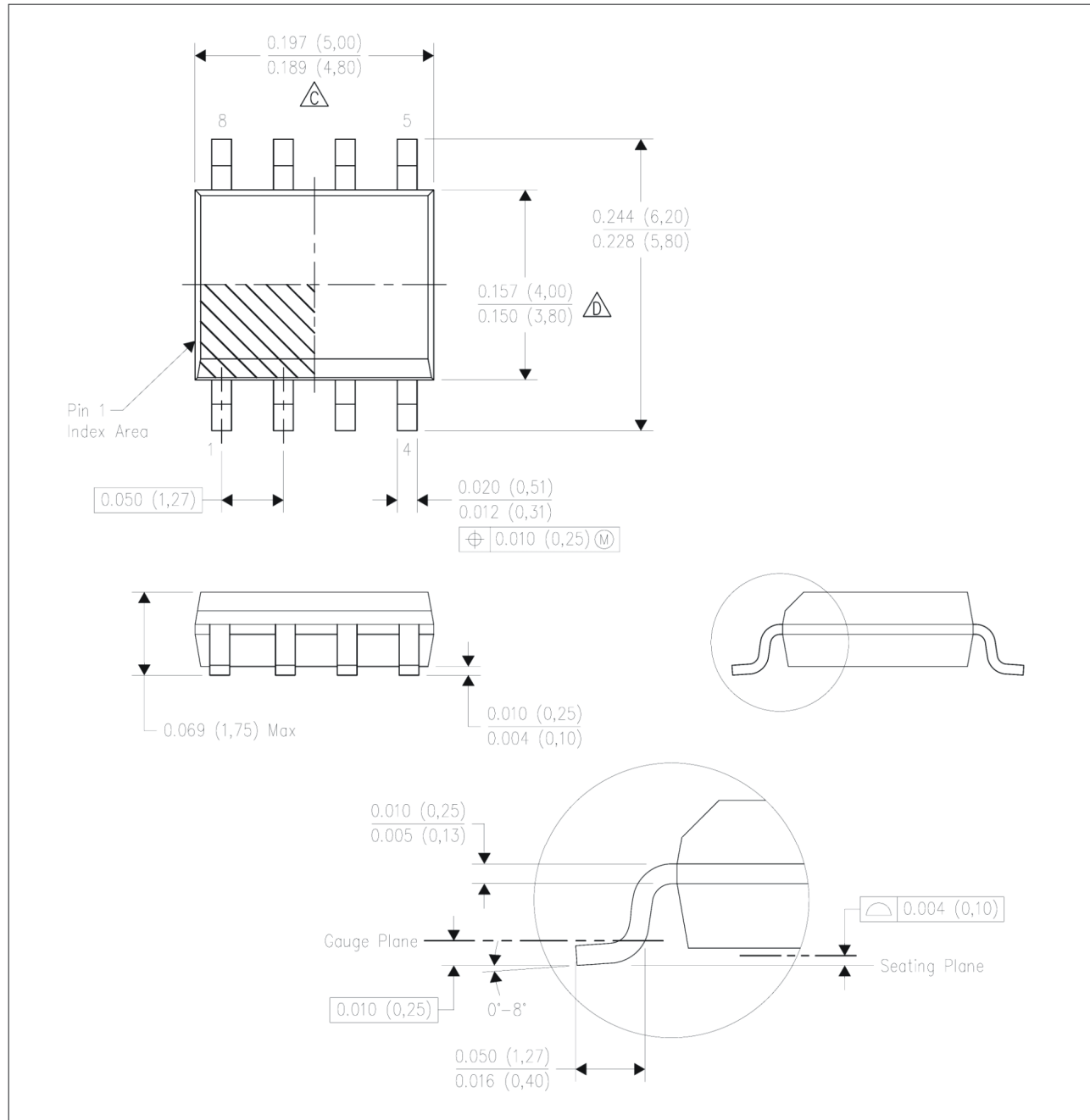
Frequency of short current protect



Soft start time



SOP-8 Dimension

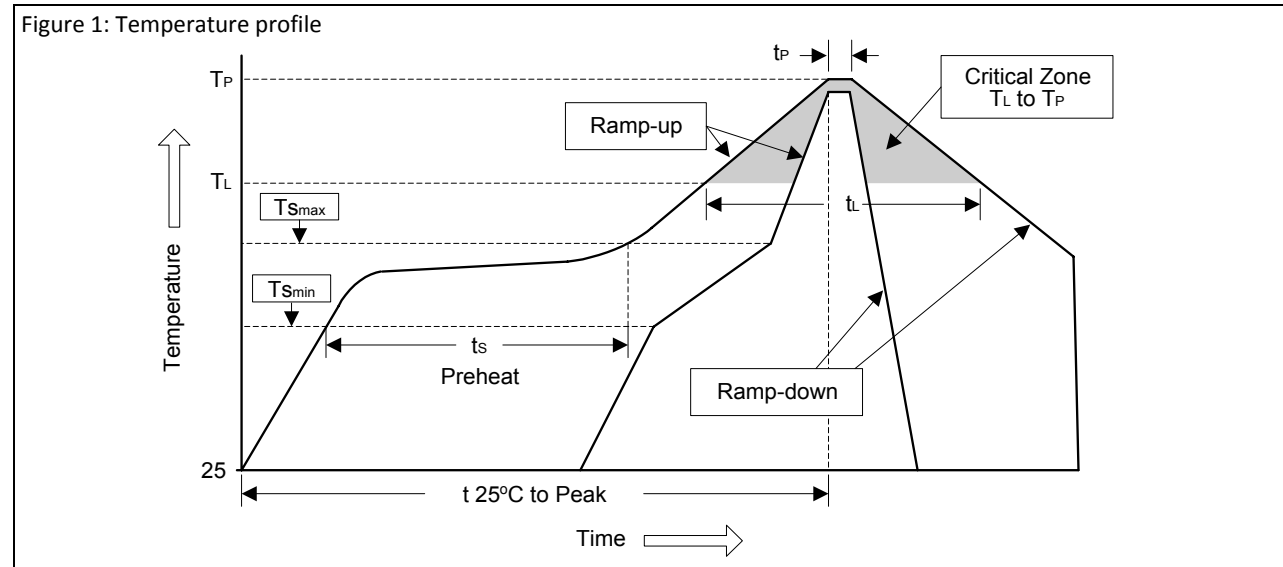


NOTES:

- All linear dimensions are in millimeters (inches).
- This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0.15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0.43) per side.
- Falls within JEDEC MS-012 variation AA.

Soldering Methods for Orister's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_p)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

Important Notice:

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