

HVL388CM

Variable Capacitance Diode for VCO

REJ03G0229-0200 Rev.2.00 Mar 15, 2006

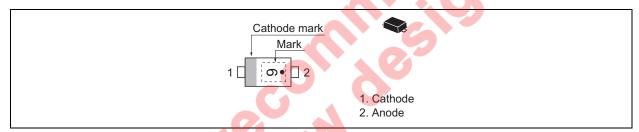
Features

- High capacitance ratio. (n = 1.880 min)
- Low series resistance. (rs = $0.75 \Omega \text{ max}$)
- Thin Extremely small Flat Lead Package (TEFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code	
HVL388CM	9	TEFP	PUSF0002ZA-A	

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V_R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

Electrical Characteristics

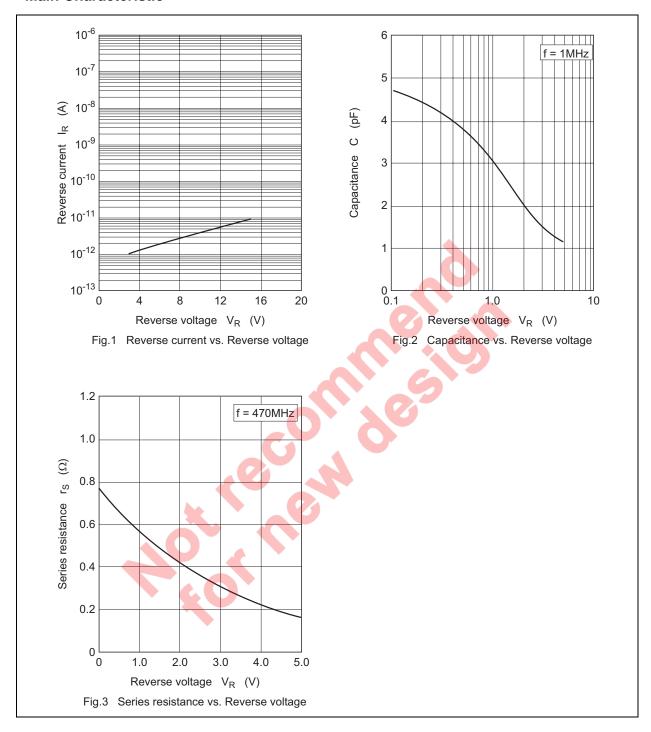
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 15 V
	I _{R2}	_	_	50		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	3.162	_	3.465	pF	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$
	C ₃	1.570	_	1.720		$V_R = 3 \text{ V, f} = 1 \text{ MHz}$
Capacitance ratio	n	1.880	_	2.150		C ₁ / C ₃
Series resistance	r _S			0.75	Ω	V _R = 1 V, f = 470 MHz

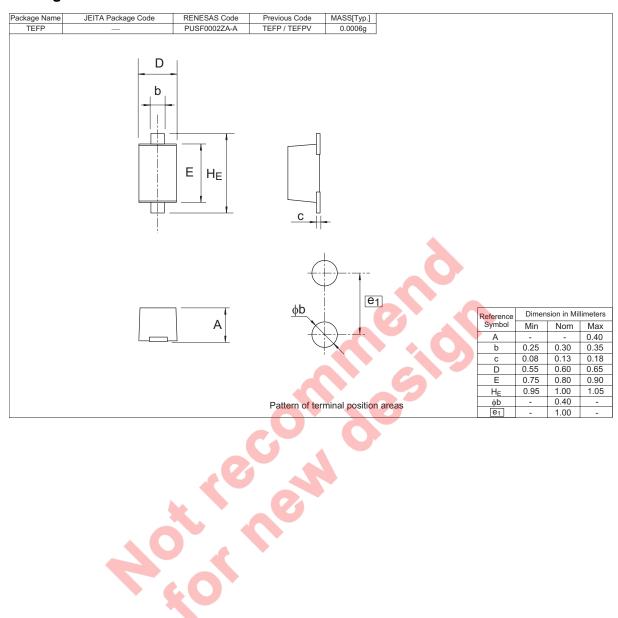
Note: For TEFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.



Main Characteristic



Package Dimensions



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