

HVL381C

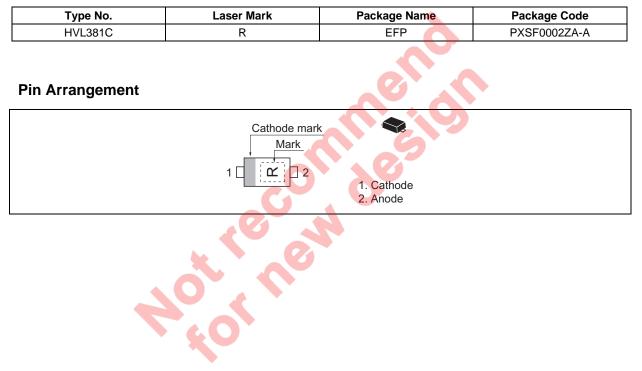
Variable Capacitance Diode for VCO

REJ03G0224-0200 Rev.2.00 Mar 14, 2006

Features

- High capacitance ratio. (n = 1.65 min)
- Low series resistance. (rs = 0.50Ω max)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

Electrical Characteristics

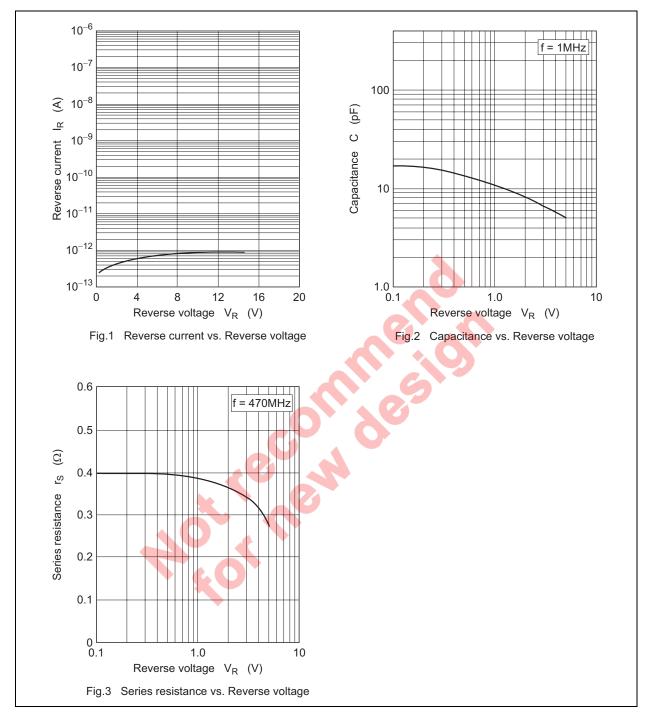
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	—	10	nA	V _R = 15 V
	I _{R2}	_	_	100		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	10.2	—	10.8	pF	$V_{R} = 1 V, f = 1 MHz$
	C ₃	5.90	—	6.35		$V_{R} = 3 V, f = 1 MHz$
Capacitance ratio	n	1.650	—	1.785	>	C ₁ / C ₃
Series resistance	rs		_	0.50	Ω	$V_{R} = 1 V, f = 470 MHz$

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Rev.2.00 Mar 14, 2006 page 2 of 4

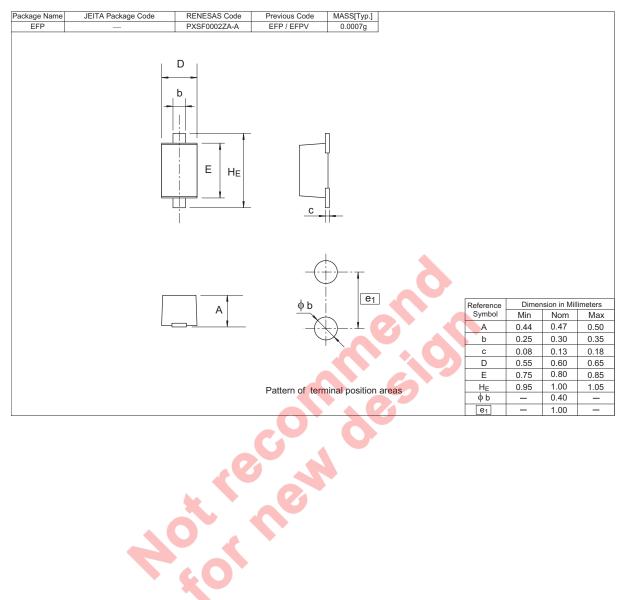
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Main Characteristic





Package Dimensions





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Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510