

HVL192

Silicon Epitaxial Planar Pin Diode for Wireless LAN

REJ03G0417-0100 Rev.1.00 Nov 24, 2005

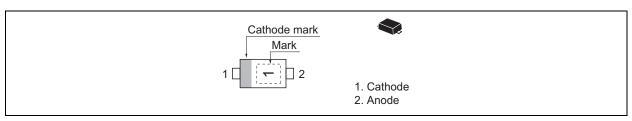
Features

- Suitable for an antenna switches of wireless LAN and a cordless telephone.
- Super -Low capacitance.(C = 0.30 pF max)
- Low forward resistance. (rf = $3.2 \Omega \text{ max}$)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HVL192	1	EFP	PXSF0002ZA-A
			(EFP)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	30	V
Forward current	I _F	50	mA
Power dissipation	Pd	100	mW
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

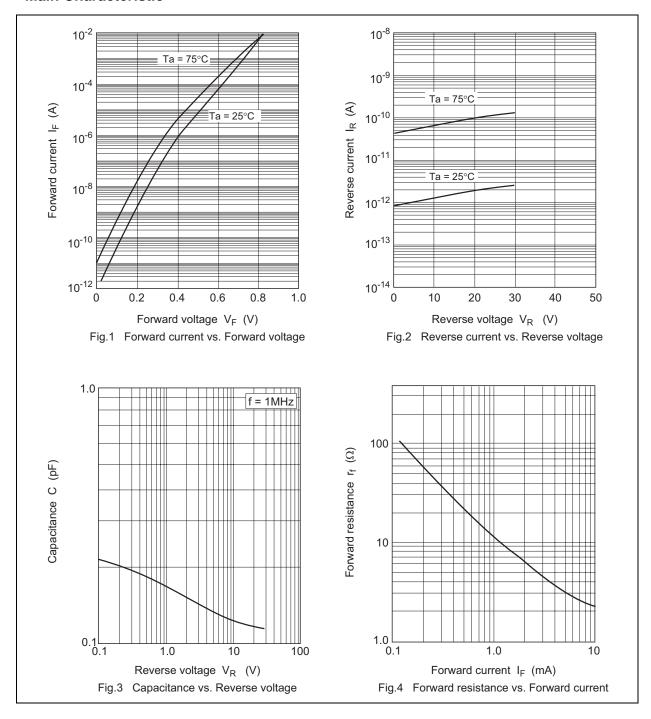
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

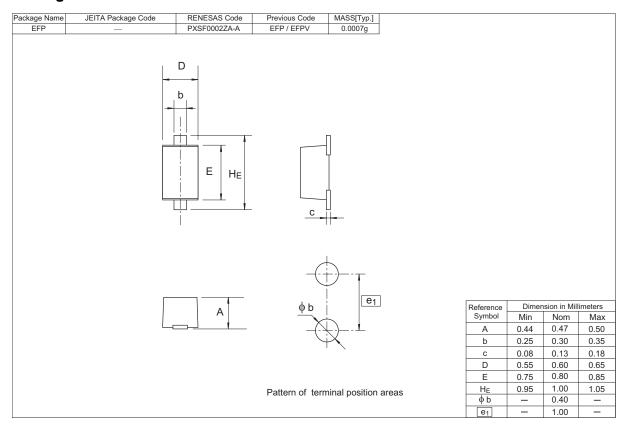
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _R	_	_	100	nA	V _R = 30 V
Forward voltage	V _F	_	_	1.0	V	I _F = 10 mA
Capacitance	С	_	_	0.30	pF	V _R = 1 V, f = 1 MHz
Forward resistance	r _f	_	_	3.2	Ω	I _F = 10 mA, f = 100 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic



Package Dimensions



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