# RENESAS

# HVL147

Silicon Epitaxial Trench Pin Diode for Antenna Switching

REJ03G0393-0300 Rev.3.00 Jan 13, 2006

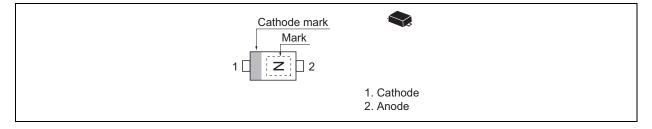
### Features

- Adopting the trench structure improves low capacitance. (C = 0.31 pF max)
- Low forward resistance. ( $rf = 1.5 \Omega max$ )
- Low operation current.
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code	
HVL147	Ν	EFP	PXSF0002ZA-A	

### **Pin Arrangement**





### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Symbol Value	
Reverse voltage	V <sub>R</sub>	30	V
Forward current	I <sub>F</sub>	100	mA
Power dissipation	Pd	100	mW
Junction temperature	Тј	125	°C
Storage temperature	Tstg	-55 to +125	C°

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

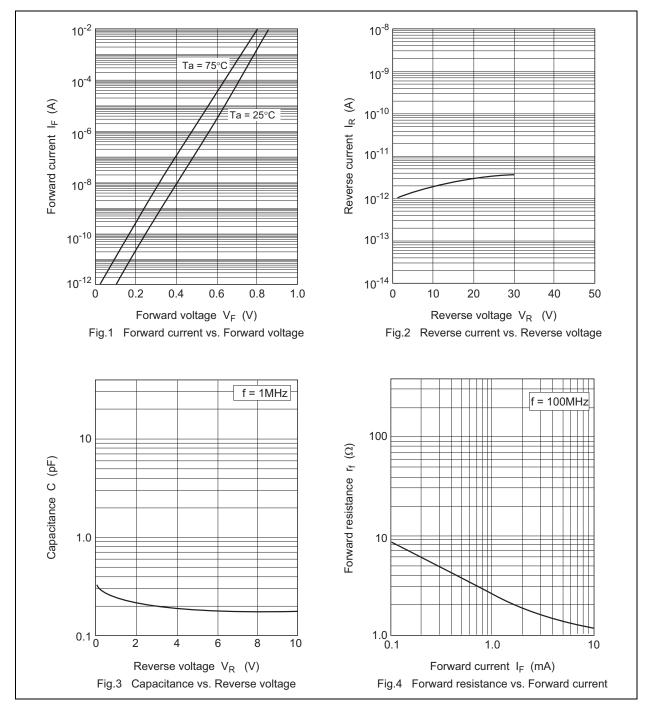
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R</sub>	—	_	100	nA	V <sub>R</sub> = 30 V
Forward voltage	V <sub>F</sub>	—	_	1.00	V	I <sub>F</sub> = 10 mA
Capacitance	С	—	_	0.31	pF	V <sub>R</sub> = 1 V, f = 1 MHz
Forward resistance	r <sub>f</sub>	—	2.5	-	Ω	I <sub>F</sub> = 2 mA, f = 100 MHz
		—	—	1.5		I <sub>F</sub> = 10 mA, f = 100 MHz
ESD-Capability *1	—	100		_	V	C = 200 pF, R = 0 $\Omega$ , Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ;  $I_R > 100 \ nA$  at  $V_R$  = 30 V

2. For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

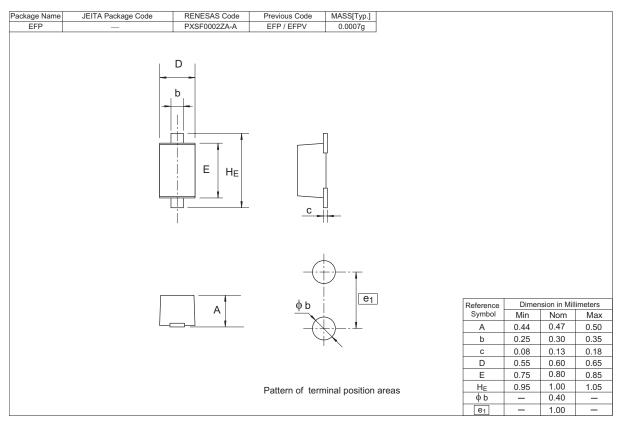


### **Main Characteristic**





## **Package Dimensions**





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