

Surface Mount Voltage Controlled Oscillator

PCS 1770 - 1850 MHz

MLO81100-01810

V1.00

Features

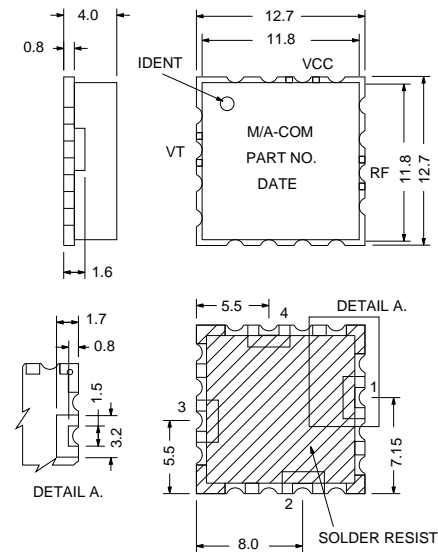
- Miniature Size
- Surface Mount Package
- Electrically Shielded
- Low Phase Noise
- Highly Linear Tuning

Description

The MLO81100-01810 is a fundamental single ended oscillator designed for use in cost sensitive wireless and telemetry applications. The device has been optimised by careful selection of the bipolar transistor and varactor diode for low phase noise and highly linear tuning characteristics.

The low profile surface mount package used provides for electrical shielding, ease of assembly and repeatable performance. M/A-COM's surface mount manufacturing techniques together with automated assembly and electrical testing ensure a high degree of electrical and mechanical repeatability at low cost and in high volume. Manufacturing is carried out in an ISO 9000 qualified facility.

LSM1 Package



Electrical Specifications¹, T_A = +25°C, V_{CC} = +5V (unless otherwise stated)

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Frequency Range		MHz	1770		1850
Tuning Voltage (V _T) ²		V	+0.5		+4.5
RF Output Power ³	1770 - 1850 MHz	dBm	-2.0		+2.0
Supply Voltage (V _{CC}) ⁴		V	+4.75	+5.00	+5.25
Supply Current (I _{CC})		mA			15
Phase Noise ⁵	SSB at 100 KHz offset from carrier	dBc/Hz		-120	-116
Tuning Sensitivity	1770 - 1850 MHz	MHz/V		30	
Tuning Linearity	1770 - 1850 MHz	Ratio		1.5	2.0
Modulation Bandwidth	3 dB bandwidth	MHz	10		
Harmonic Outputs ⁶		dBc			-15
Frequency Pushing	V _{CC} 4.75V to 5.25V	MHz/V			3.0
Frequency Pulling	1.5:1 VSWR load, all phases	MHz			5.0
Frequency Drift		MHz/°C		0.15	
Tune Input Capacitance		pF			50

1. All specifications apply with a 50 ohm load impedance.
2. Tuning voltages shown are the minimum and maximum voltages required to tune the frequency range including temperature effects -20°C to +70°C. Devices will oscillate normally with tuning voltages from 0V to +10V.
3. Output power window includes unit to unit variation, temperature effects -20°C to +70°C and frequency flatness (typically ±0.5 dB at any constant temperature).
4. Devices may be operated at lower supply voltage with reduced performance.
5. For typical phase noise at other offsets see phase noise curve.
6. No non harmonic spurious visible when measured on a test system with a dynamic range of 70 dB.

This Preliminary Data Sheet Contains Typical Electrical Specifications Which May Change Prior To Final Introduction.

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