

EMP105

ISSUED DATE: 07-12-04

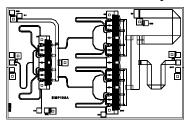
4.0 - 5.5 GHz Power Amplifier MMIC

FEATURES

- 4.0 5.5 GHz Operating Frequency Range
- 32.5dBm Output Power at 1dB Compression
- 18.0 dB Typical Small Signal Gain
- -40dBc OIMD3 @Each Tone Pout 22dBm

APPLICATIONS

- Point-to-point and point-to-multipoint radio
- Military Radar Systems



Dimension: 2200um X 3530um Thickness: 65um <u>+</u> 15um



Caution! ESD sensitive device.

ELECTRICAL CHARACTERISTICS (Ta = 25 °C, 50 ohm, VDD=10V, IDQ=950mA)

	,				
SYMBOL	PARAMETER/TEST CONDITIONS	MIN	TYP	MAX	UNITS
F	Operating Frequency Range	4.0		5.5	GHz
P1dB	Output Power at 1dB Gain Compression	31.5	32.5		dBm
Gss	Small Signal Gain	16.0	18.0		dB
OIMD3	Output 3 rd Order Intermodulation Distortion @∆f=10MHz, Each Tone Pout 22dBm		-40		dBc
Input RL	Input Return Loss		-11	-8	dB
Output RL	Output Return Loss		-6		dB
ldss	Saturate Drain Current V _{DS} =3V, V _{GS} =0V		1680		mA
V_{DD}	Power Supply Voltage		10		V
Rth	Thermal Resistance (Au-Sn Eutectic Attach)		7		°C/W
Tb	Operating Base Plate Temperature	- 35		+ 80	°C

ABSOLUTE MAXIMUM RATINGS FOR CONTINUOUS OPERATION^{1,2}

SYMBOL	CHARACTERISTIC	VALUE	
V_{DS}	Drain to Source Voltage	10V	
V_{GS}	Gate to Source Voltage	- 4V	
I_{DD}	Drain Current	ldss	
I _{GSF}	Forward Gate Current	35 mA	
P_{IN}	Input Power	@ 3dB compression	
T _{CH}	Channel Temperature	150°C	
T_{STG}	Storage Temperature	-65/150°C	
P_T	Total Power Dissipation	17W	

^{1.} Operating the device beyond any of the above rating may result in permanent damage.

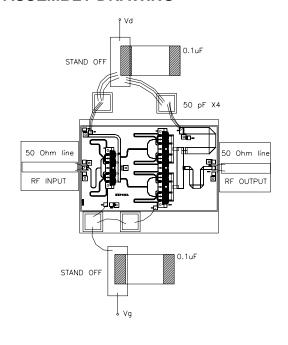
^{2.} Bias conditions must also satisfy the following equation $V_{DS}*I_{DS} < (T_{CH} - T_{HS})/R_{TH}$; where T_{HS} = ambient temperature



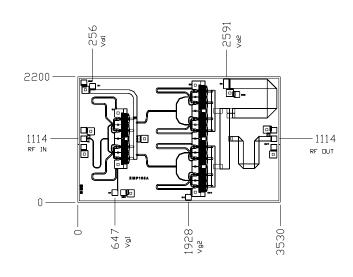


ISSUED DATE: 07-12-04

ASSEMBLY DRAWING



4.0 – 5.5 GHz Power Amplifier MMIC CHIP OUTLINE



All dimensions in microns

TYPICAL PERFORMANCE

