Elektrische Eigenschaften — Electrical properties

Höchstzulässige Werte — Maximum rated values

Periodische Vorwärts- und Rückwärts-Spitzensperrspannung	repetitive peak forward off-state and reverse voltages	$t_{vj} = -40°C ... t_{vj\,max}$	V_{DRM}, V_{RRM}		800, 1000	V
					1100, 1200	V
					1300*	V
Vorwärts-Stoßspitzen-sperrspannung	non repetitive peak forward off-state voltage	$t_{vj} = -40°C ... t_{vj\,max}$	$V_{DSM} = V_{DRM}$			
Rückwärts-Stoßspitzen-sperrspannung	non repetitive peak reverse voltage	$t_{vj} = +25°C ... t_{vj\,max}$	$V_{RSM} = V_{RRM}$		+ 100	V
Durchlaßstrom-Grenzeffektivwert	RMS on-state current		I_{TRMSM}		750	A
Dauergrenzstrom	average on-state current	t_C = 85°C	I_{TAVM}		408	A
		tc = 76°C			477	A
Stoßstrom-Grenzwert	surge current	t, = 25°C, t_p = 10 ms	I_{TSM}		7200	A
		$t_{vj} = t_{vj\,max}$, p = 10 ms			6400	A
Grenzlastintegral	I^2t-value	t_{vj} = 25°C, t_p = 10 ms	I^2t		280	kA^2s
		$t_{vj} = t_{vj\,max}$, t_p = 10 ms			205	kA^2s
Kritische Stromsteilheit	critical rate of rise of on-state current	$v_D \leq 67\%\ V_{DRM}$, f = 50 Hz	$(di/dt)_{cr}$		200	A/µs
		i_{GM} = 1 A, di_G/dt = 1 Alps				
					[1]	[2]
Kritische Spannungssteilheit	critical rate of rise of off-state voltage	$t_{vj} = t_{vj\,max}$, v_D = 67% V_{DRM}	$(dv/dt)_{cr}$	B:	50	50 V/µs
				C*:	500	500 V/µs
				L:	500	50 V/µs
				M*:	1000	500 V/µs

Charakteristische Werte — Characteristic values

Durchlaßspannung	on-state voltage	$t_{vj} = t_{vj\,max}$, i_T = 1400 A	v_T	max.	2,2	V
Schleusenspannung	threshold voltage	$t_{vj} = t_{vj\,max}$	$V_{T(TO)}$		1,2	V
Ersatzwiderstand	slope resistance	$t_{vj} = t_{vj\,max}$	r_T		0,63	mΩ
Zündstrom	gate trigger current	t_{vj} = 25°C, v_D = 12 V	I_{GT}	max.	250	mA
Zündspannung	gate trigger voltage	t_{vj} = 25°C, v_D = 12 V	V_{GT}	max.	2,2	V
Nicht zündender Steuerstrom	gate non-trigger current	$t_{vj} = t_{vj\,max}$, v_D = 12v	I_{GD}	max.	10	mA
Nicht zündende Steuerspannung	gate non-trigger voltage	$t_{vj} = t_{vj\,max}$, v_D = 0,5 V_{DRM}	V_{GD}	max.	0,25	V
Haltestrom	holding current	t_{vj} = 25°C, v_D = 12 V, R_A = 10 Ω	I_H	max.	250	mA
Einraststrom	latching current	t_{vj} = 25°C, v_D = 12 V, $R_{GK} \geq$ 10 62	I_L	max.	1	A
		i_{GM} = 1 A, di_G/dt = 1 A/µs, t_g = 20 ps				
Vorwärts- u. Rückwärts-Sperrstrom	forward off-state and reverse Currents	$t_{vj} = t_{vj\,max}$, v_D = VD,,, $v_R = V_{RRM}$	i_D, iR	max.	50	mA
Zündverzug	gate controlled delay time	t_{vj} = 25°C, i_{GM} = 1 A, di_G/dt = 1 A/µs	t_{gd}	max.	1,2	µs
Freiwerdezeit	circuit commutated turn-off time	siehe Techn. Erl./see Techn. Inf.	t_q s:	max.	18	µs
			E:	max.	20	µs
			F:	max.	25	µs

Thermische Eigenschaften — Thermal properties

Innerer Wärmewiderstand	thermal resistance, junction to case					
für beidseitige Kühlung	for two-sided cooling	Θ = 180° el, sin	R_{thJC}	max.	0,053	°C/W
		DC		max.	0,05	°C/W
für anodenseitige Kühlung	for anode-sided cooling	Θ = 180° el, sin	$R_{thJC(A)}$	max.	0,088	°C/W
		DC		max.	0,085	°C/W
für kathodenseitige Kühlung	for cathode-sided cooling	Θ = 180° el, sin	$R_{thJC(K)}$	max.	0,123	°C/W
		DC		max.	0,12	°C/W
Übergangswärmewiderstand	thermal resistance, case to heatsink	beidseitig/two-sided	R_{thCK}	max.	0,01	°C/W
		einseitiglone-sided		max.	0,02	°C/W
Höchstzul. Sperrschichttemperatur	max. junction temperature		$t_{vj\,max}$		125	°C
Betriebstemperatur	Operating temperature		$t_{c\,op}$		−40 ... + 125	°C
Lagertemperatur	storage temperature		t_{stg}		−40 ... + 140	°C

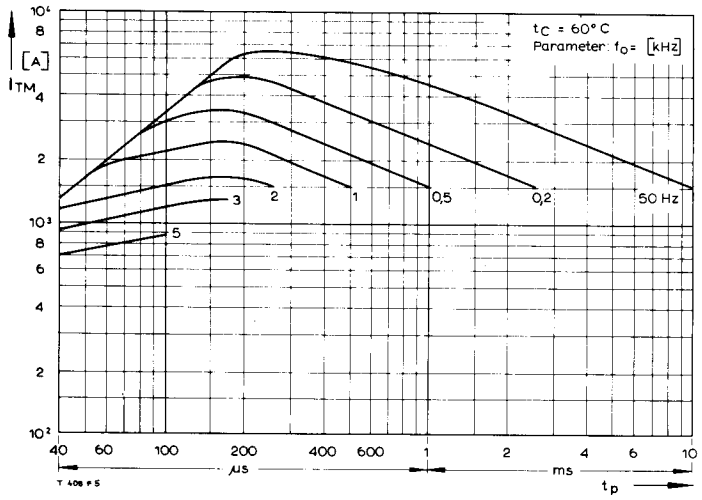
Mechanische Eigenschaften — Mechanical properties

Si-Element mit Druckkontakt	Si-pellet with pressure contact				
Anpreßkraft	Clamping force		F		5 ... 10 kN
Gewicht	weight		G	typ.	100 g
Kriechstrecke	Creepage distance				17mm
Feuchteklasse	humidity classification	DIN 40040			C
Schwingfestigkeit	Vibration resistance	f = 50 Hz			50 m/s^2
Maßbild	outline	DIN 41814-152A4			Seite/page 154

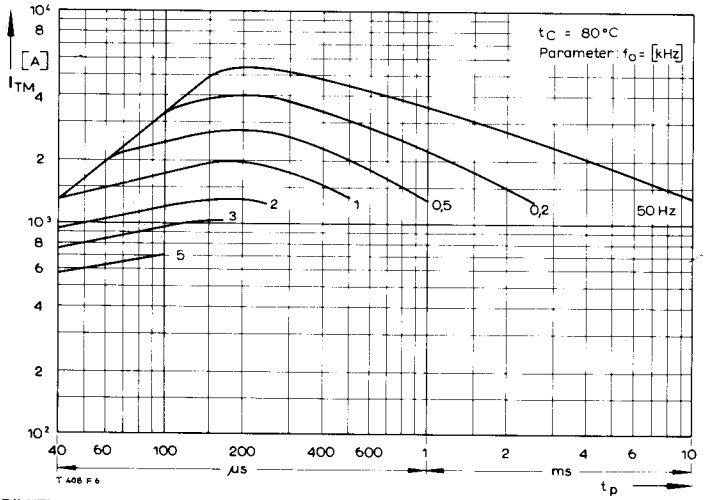
* Für größere Stückzahlen bitte Liefertermin erfragen/Delivery for larger quantities on request

1) Werte nach DIN IEC 747-6 (ohne vorausgehende Kommutierung)/Values to DIN IEC 747-6 (without prior commutation)

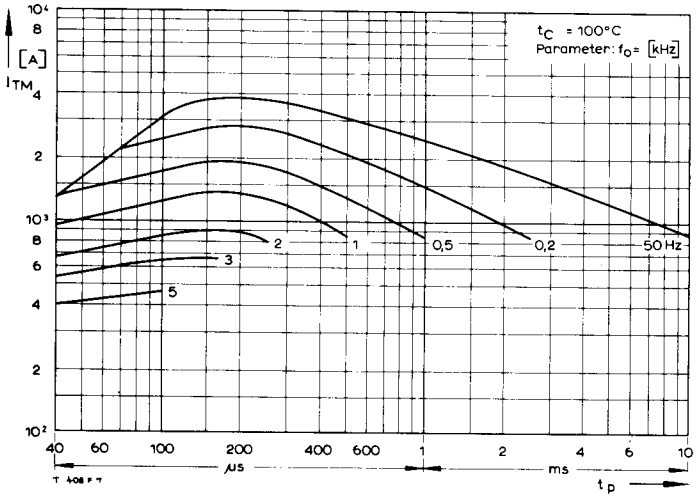
2) Unmittelbar nach der Freiwerdezeit, vgl. Meßbedingungen fü t_q/Immediately after circuit commutated turn-off time, see Parameters t_q



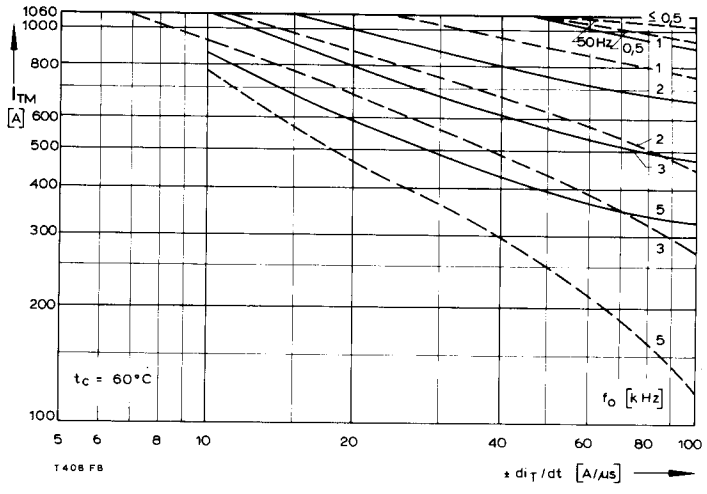
Bild/Fig. 1



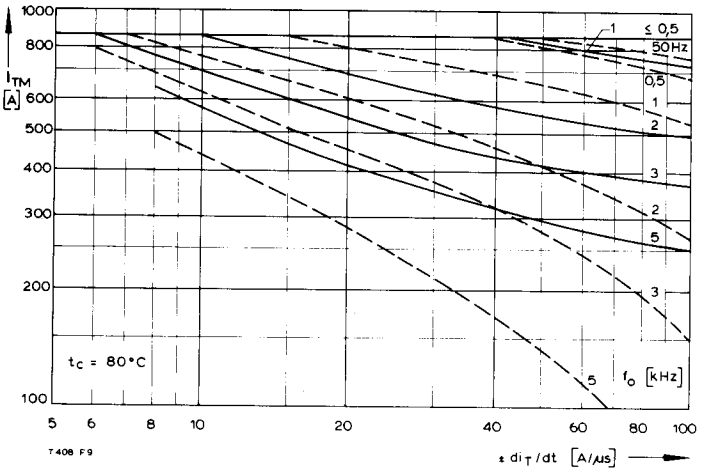
Bild/Fig. 2



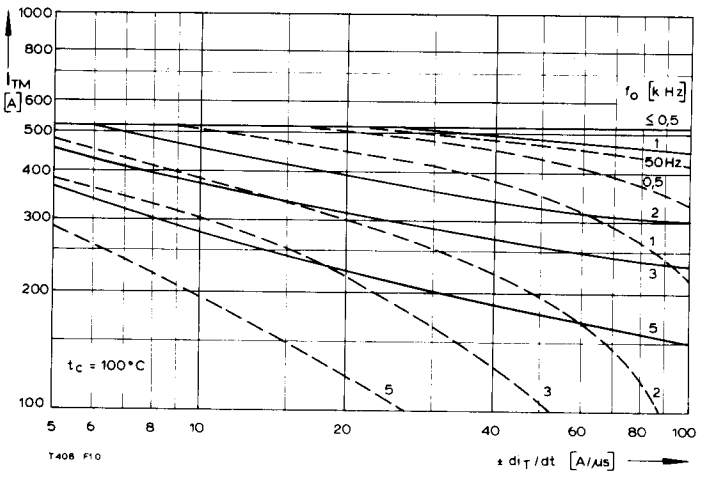
Bild/Fig. 3



Bild/Fig. 4



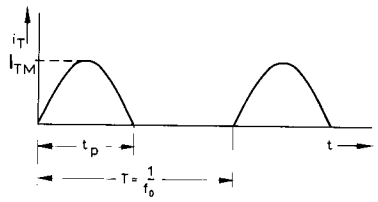
Bild/Fig. 5



Bild/Fig. 6

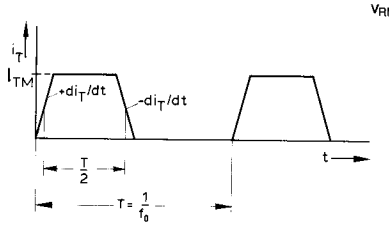
Bild/Fig. 1, 2, 3
Steuergenerator/pulse generator:
i_G = 1 A, di_G/dt = 1 A/µs

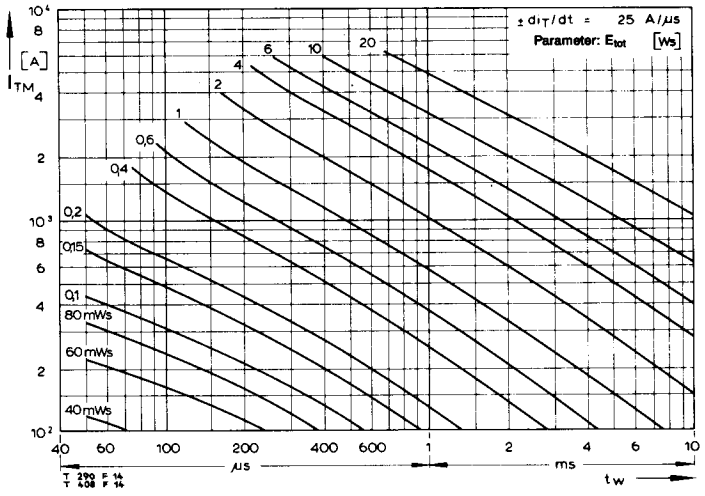
RC-Glied/RC-network:
R [Ω] ≥ 0,02 v_{DM} [V]
C ≤ 0,22 µF
v_{DM} ≤ 0,67 V_{DRM}



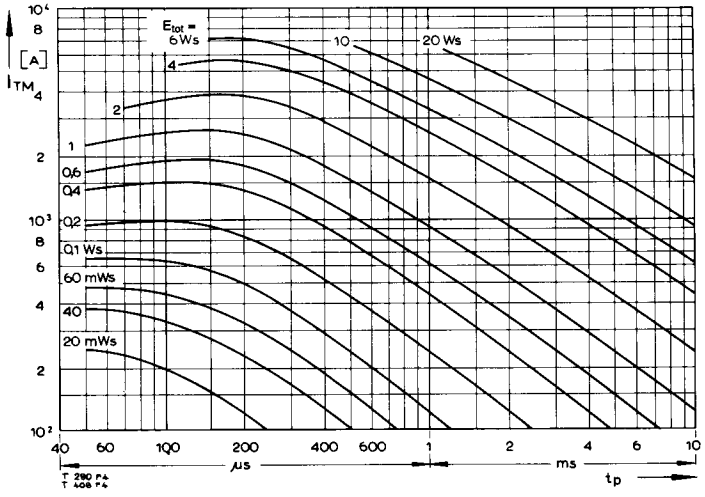
Bild/Fig. 4, 5, 6
Steuergenerator/pulse generator:
i_G = 1 A, di_G/dt = 1 A/µs

RC-Glied/RC-network:
R [Ω] ≥ 0,02 v_{DM} [V]
C ≤ 0,33 µF
v_{DM} ≤ 0,67 V_{DRM}
dv_R/dt ≤ 600 V/µs
v_{RM} ≤ 0,67 V_{RRM}

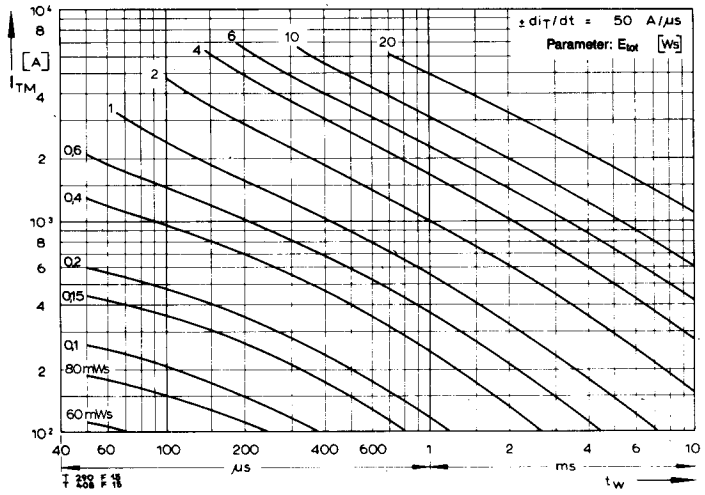




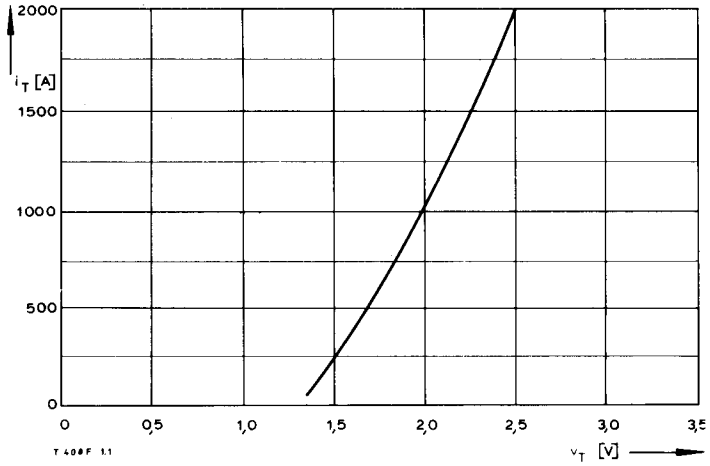
Bild/Fig. 10



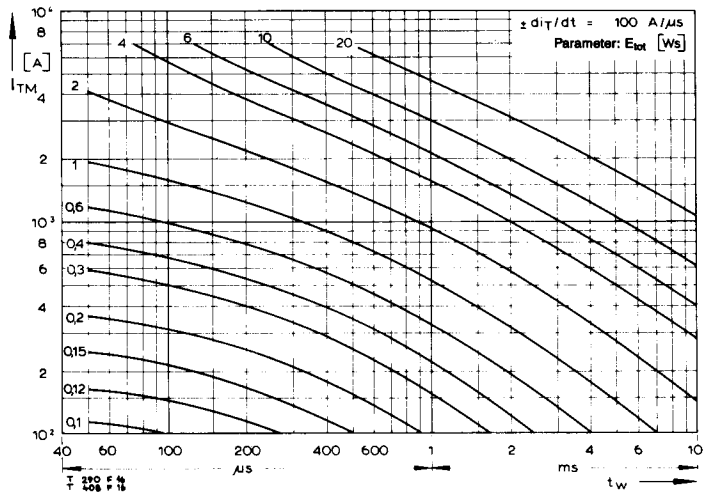
Bild/Fig. 13



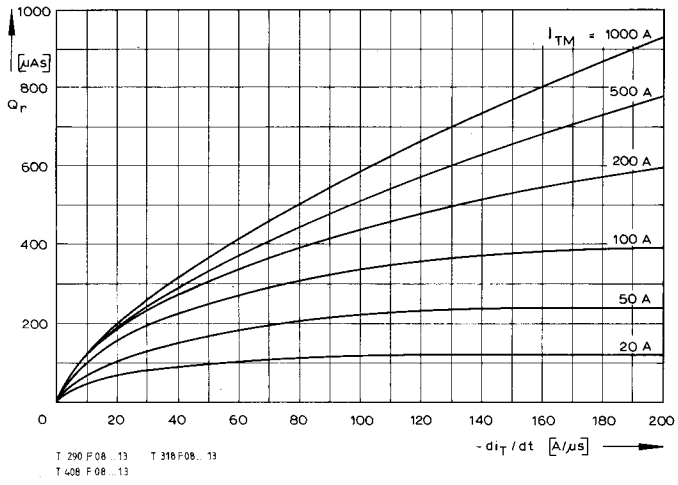
Bild/Fig. 11



Bild/Fig. 14



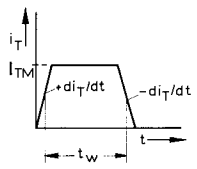
Bild/Fig. 12



Bild/Fig. 15

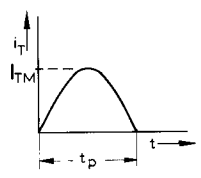
Bild/Fig. 10, 11, 12
Steuergenerator/pulse generator:
i_G = 1 A, di_G/dt = 1 A/µs

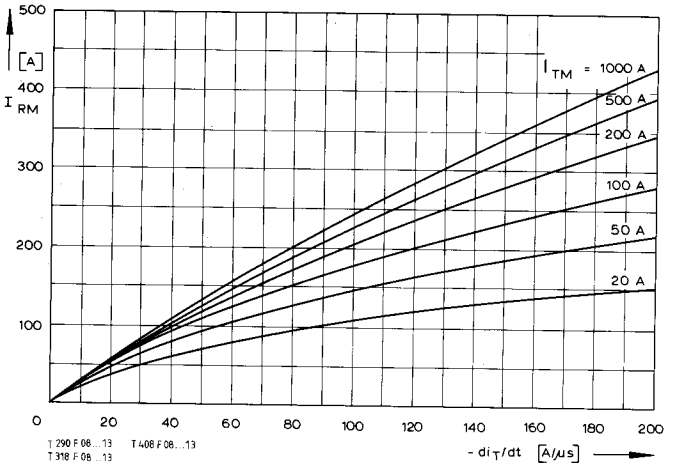
RC-Glied/RC-network:
R [Ω] ≥ 0,02 v_{DM} [V]
C ≤ 0,33 µF
$v_{DM} \leq 0{,}67\ V_{DRM}$
$dv_R/dt \leq 600$ V/µs
$v_{RM} \leq 0{,}67\ V_{RRM}$



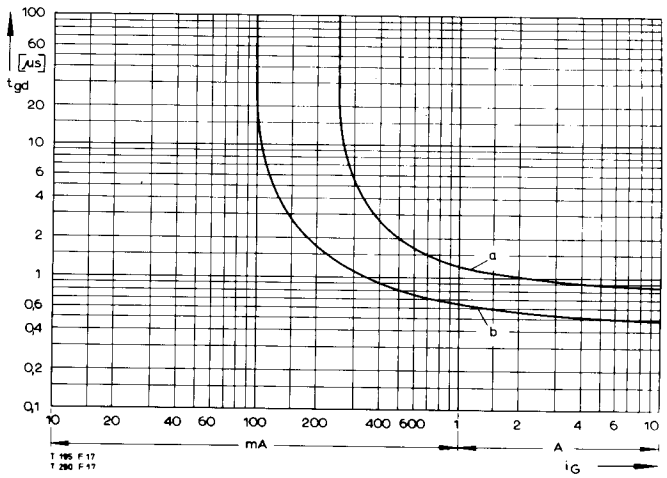
(zu Bild/to Fig. 13)
Steuergenerator/pulse generator:
i_G = 1 A, di_G/dt = 1 A/µs

RC-Glied/RC-network:
R [Ω] ≥ 0,02 v_{DM} [V]
C ≤ 0,22 µF

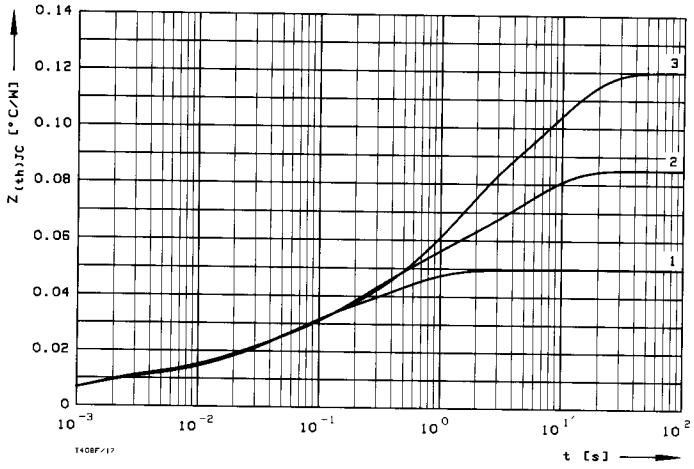




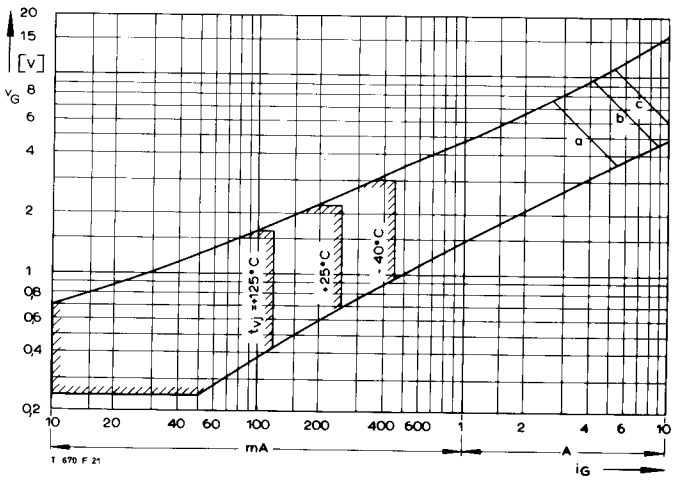
Bild/Fig. 16
Rückstromspitze $I_{RM} = f(-di/dt)$, $t_{vj} = t_{vj\,(max)}$, $v_R = 0{,}5\ V_{RRM}$, $v_{RM} = 0{,}8\ V_{RRM}$
Peak reverse recovery current $I_{RM} = f(-di/dt)$, $t_{vj} = t_{vj\,(max)}$, $v_R = 0.5\ V_{RRM}$, $v_{RM} = 0.8\ V_{RRM}$
Parameter: Durchlaßstrom/On-state current I_{TM}



Bild/Fig. 18
Zündverzug/Gate controlled delay time $t_{gd} = f(i_{GM})$, $t_{vj} = 25°C$, $di_G/dt = i_{GM}/1\ \mu s$
a – Maximaler Verlauf/Limiting characteristic
b – Typischer Verlauf/Typical characteristic



Bild/Fig. 17
Transienter innerer Wärmewiderstand $Z_{(th)JC} = f(t)$, DC
Transient thermal impedance $Z_{(th)JC} = f(t)$, DC
1 Beidseitige Kühlung/two-sided cooling
2 Anodenseitige Kühlung/anode side cooling
3 Kathodenseitige Kühlung/cathode side cooling



Bild/Fig. 19
Steuercharakteristik mit Zündbereichen/Gate characteristic with triggering areas
$v_G = f(i_G)$, $V_D = 12$ V

Parameter:		a	b	c
Steuerimpulsdauer/Trigger pulse duration t_g	[ms]	10	1	0,5
Höchstzulässige Spitzensteuerverlustleistung/ Max. rated peak gate power dissipation P_{GM}	[W]	20	40	60

Analytische Elemente des transienten Wärmewiderstandes Z_{thJC} für DC
Analytical elements of transient thermal impedance Z_{thJC} for DC

Kühlung cooling	Pos. n	1	2	3	4	5	6	7
beidseitig	R_{thn} [°C/W]	0,0105	0,00283	0,0167	0,0188	0,00116		
two-sided	τ_n [s]	0,0113	0,0255	0,0511	0,429	2,49		
anodenseitig	R_{thn} [°C/W]	0,0094	0,00974	0,0182	0,0161	0,0316		
anode-sided	τ_n [s]	0,000984	0,017	0,15	0,6	5,0		
kathodenseitig	R_{thn} [°C/W]	0,00928	0,0145	0,00868	0,0401	0,0475		
cathode-sided	τ_n [s]	0,000939	0,0285	0,156	1,12	9,1		

Analytische Funktion/analytical function:

$$Z_{thJC} = \sum_{n=1}^{n_{max}} R_{thn}\ (1 - EXP\ (-t/\tau_n))$$

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