BT139 series H

## Triacs high noise immunity

## GENERAL DESCRIPTION

Glass passivated triacs in a plastic envelope, intended for use in applications requiring high noise immunity in addition to high, bidirectional blocking voltage capability and thermal cycling performance. Typical applications include motor control, industrial lighting, heating and static switching.

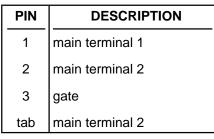
## PINNING - TO220AB

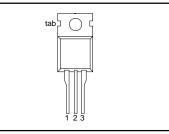
## QUICK REFERENCE DATA

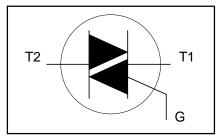
SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V <sub>drm</sub> I <sub>t(rms)</sub> I <sub>tsm</sub>	BT139- Repetitive peak off-state voltages RMS on-state current Non-repetitive peak on-state current	<b>500H</b> 500 16 140	<b>600H</b> 600 16 140	<b>800H</b> 800 16 140	V A A

## PIN CONFIGURATION

## SYMBOL







## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT	
V <sub>drm</sub>	Repetitive peak off-state voltages		-	<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	V
I <sub>T(RMS)</sub> I <sub>TSM</sub>	RMS on-state current Non-repetitive peak on-state current	full sine wave; $T_{mb} \le 99 \degree C$ full sine wave; $T_j = 25 \degree C$ prior to surge	-		16		A
		t = 20 ms	-		140		A
		t = 16.7 ms	-		150		A A <sup>2</sup> s
l²t dl <sub>⊤</sub> /dt	I <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after	t = 10 ms $I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu \text{s}$	-		98		A²s
	triggering	T2+ G+ T2+ G- T2- G- T2- G+	- - -		50 50 50		A/μs A/μs A/μs
I <sub>GM</sub> V <sub>GM</sub> P <sub>GM</sub>	Peak gate current Peak gate voltage Peak gate power	12- 6+			10 2 5 5		A/μs A V W
$ \begin{array}{c} P_{G(AV)} \\ T_{stg} \\ T_{j} \end{array} $	Average gate power Storage temperature Operating junction temperature	over any 20 ms period	- -40 -		0.5 150 125		° ℃ S

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

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## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-mb</sub> R <sub>th j-a</sub>	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle in free air	-	- - 60	1.2 1.7 -	K/W K/W K/W

## STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
I <sub>GT</sub>	Gate trigger current	$V_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$					
01			T2+ G+	10	14	50	mA
			T2+ G-	10	17	50	mA
			T2- G-	10	18	50	mA
			T2- G+	10	40	100	mA
IL .	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$					
			T2+ G+	-	10	60	mA
			T2+ G-	-	25	90	mA
			T2- G-	-	12	60	mA
-			T2- G+	-	14	90	mA
I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$		-	8	60	mA
I <sub>H</sub> V <sub>T</sub> V <sub>GT</sub>	On-state voltage	$I_{T} = 20 \text{ A}$		-	1.2	1.6	V
V <sub>GT</sub>	Gate trigger voltage	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$		-	0.7	1.5	V
		$V_{\rm D} = 400 \text{ V}; I_{\rm T} = 0.1 \text{ A}; T_{\rm i} = 125 \text{ S}$	°C	0.25	0.4	-	V
I <sub>D</sub>	Off-state leakage current	$V_D = V_{DRM(max)}$ ; $T_j = 125 \ ^{\circ}C$		-	0.1	0.5	mA

## **DYNAMIC CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV <sub>D</sub> /dt	Critical rate of rise of	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 \ ^{\circ}C;$	200	500	-	V/µs
dV <sub>com</sub> /dt	off-state voltage Critical rate of change of	exponential waveform; gate open circuit $V_{DM} = 400 \text{ V}; \text{ T}_{j} = 95 ^{\circ}\text{C}; \text{ I}_{T(RMS)} = 16 \text{ A};$	10	20	-	V/µs
t <sub>gt</sub>	commutating voltage Gate controlled turn-on time	$dI_{com}/dt = 7.2 \text{ A}'ms; \text{ gate open circuit}$ $I_{TM} = 20 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A};$ $dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	-	μs

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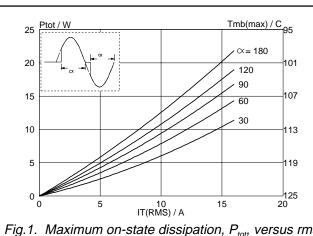
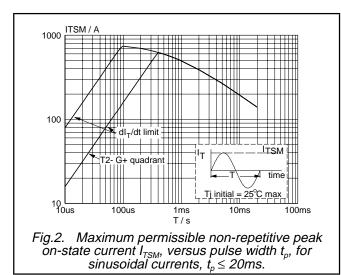
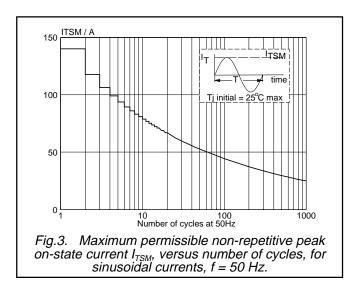
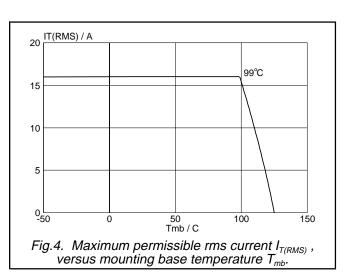


Fig.1. Maximum on-state dissipation,  $P_{tot}$ , versus rms on-state current,  $I_{T(RMS)}$ , where  $\alpha = conduction$  angle.







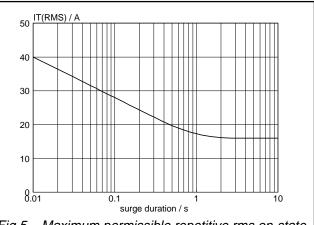
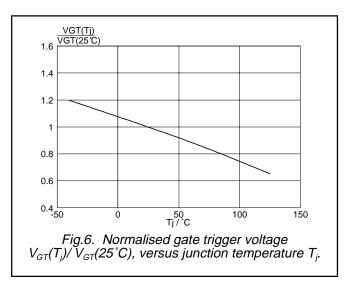
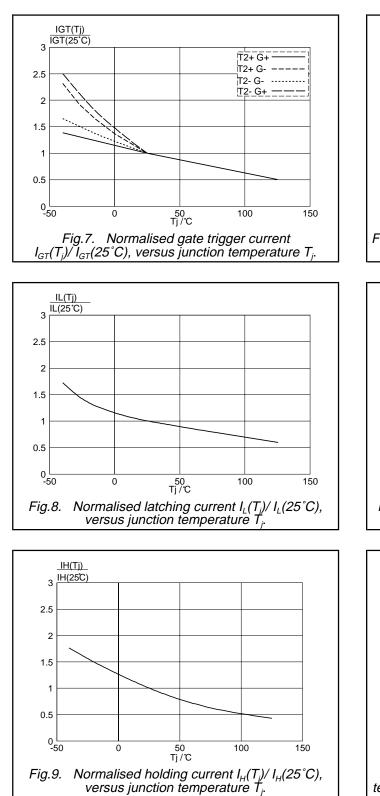
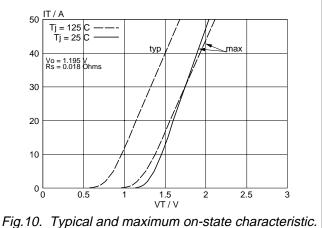


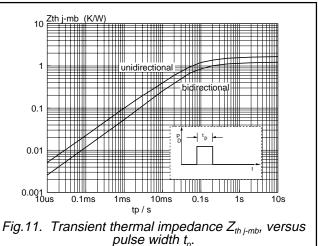
Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{mb} \le 99^{\circ}C$ .

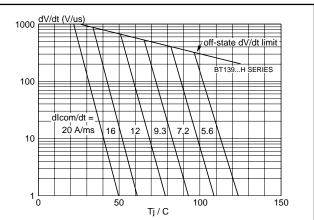


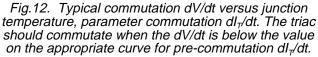
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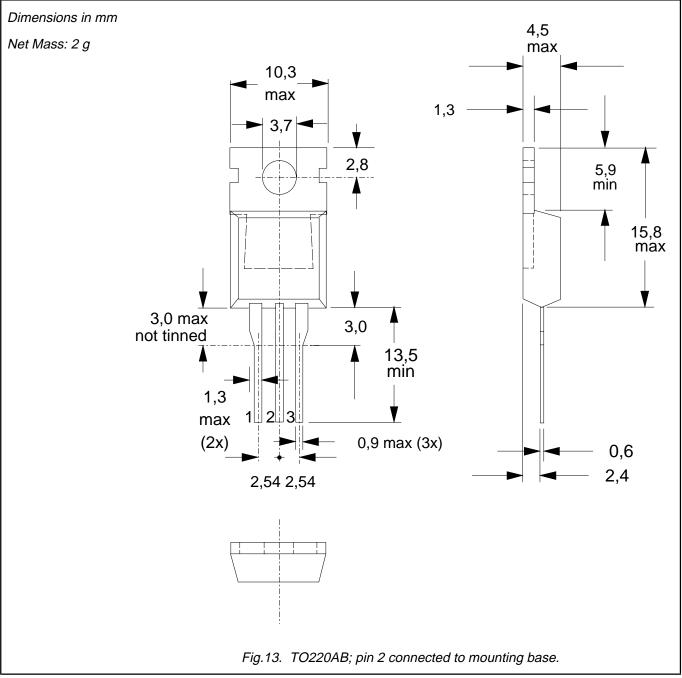




#### Product specification

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#### **MECHANICAL DATA**



**Notes** 1. Refer to mounting instructions for TO220 envelopes. 2. Epoxy meets UL94 V0 at 1/8".

# DEFINITIONS

Data sheet status	Data sheet status					
Objective specification This data sheet contains target or goal specifications for product development.						
Preliminary specification	Preliminary specification This data sheet contains preliminary data; supplementary data may be published later.					
Product specification	act specification This data sheet contains final product specifications.					
Limiting values						
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.						
Application information						
Where application information is given, it is advisory and does not form part of the specification.						
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