TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSIV)

# **TPCA8026**

Lithium-Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- · Small footprint due to a small and thin package
- Low drain-source ON-resistance: RDS (ON) = 1.8 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}|$  =100 S (typ.)
- Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- Enhancement mode:  $V_{th} = 1.3$  to 2.5 V ( $V_{DS} = 10$  V,  $I_{D} = 1$  mA)

## Absolute Maximum Ratings (Ta = 25°C)

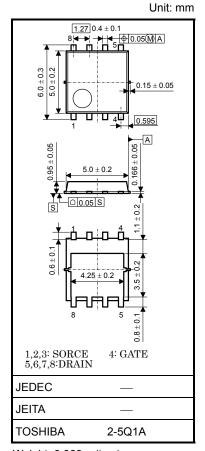
Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	30	٧	
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$ )	$V_{DGR}$	30	V	
Gate-source voltage		$V_{GSS}$	±20	٧	
Drain current	DC (Note 1)	I <sub>D</sub>	45	Α	
Brain current	Pulsed (Note 1)	I <sub>DP</sub>	135	A	
Drain power dissipati	on (Tc = 25°C)	$P_{D}$	45	W	
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	$P_{D}$	2.8	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	P <sub>D</sub>	1.6	W	
Single-pulse avalance	he energy (Note 3)	E <sub>AS</sub>	263	mJ	
Avalanche current		I <sub>AR</sub>	45	Α	
Repetitive avalanche	energy c = 25°C) (Note 4)	E <sub>AR</sub>	3.4	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate

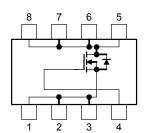
reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.069 g (typ.)

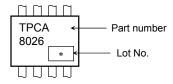
#### **Circuit Configuration**



#### **Thermal Characteristics**

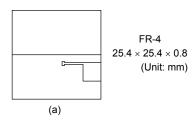
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	78.1	°C/W

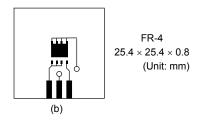
## Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)

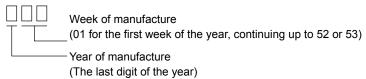




Note 3:  $V_{DD} = 24 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial), L = 0.1 mH,  $I_{AR} = 45 \text{ A}$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)

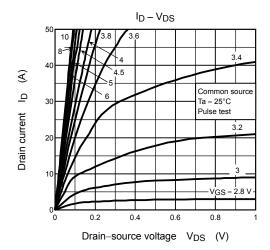


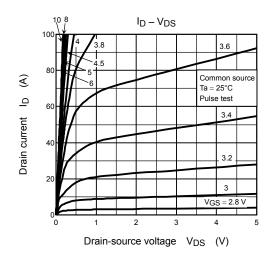
# **Electrical Characteristics (Ta = 25°C)**

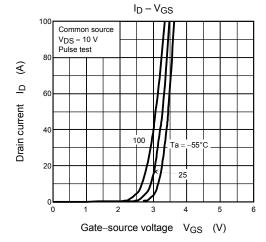
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source bre	akdown voltago	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	30	_	_	V
Dialii-source bre	akdown voltage	V (BR) DSX	$I_D = 10$ mA, $V_{GS} = -20$ V	10	±100 10 10 10 10 10	v	
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	—     2.7     4.5       —     1.8     2.2       50     100     —		٧	
Drain-source ON	resistance	Ppo (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 23 \text{ A}$	—     —     ±100       —     —     10       30     —     —       10     —     —       1.3     —     2.5       —     2.7     4.5       —     1.8     2.2       50     100     —       —     4200     —       —     1400     —       —     15     —       —     30     —       —     36     —       —     111     —       —     113     —	mΩ		
Diaiii-souice On	-resistance	R <sub>DS</sub> (ON)	$V_{GS} = 10 \text{ V}, I_D = 23 \text{ A}$				
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_D = 23 \text{ A}$	50	100		S
Input capacitance	e	C <sub>iss</sub>			4200		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1000	_	pF
Reverse transfer capacitance  Output capacitance		Coss			1400		
	Rise time	t <sub>r</sub>	V <sub>20</sub> 10 V		15		
Switching time	Turn-on time	t <sub>on</sub>	V <sub>GS</sub> 10 V	_	30	_	20
Switching time	Fall time	t <sub>f</sub>	C; \$ \$ 00   00   00   00   00   00   00	_	36	_	ns ns
	Turn-off time	t <sub>off</sub>			111	ı	
Total gate charge (gate-source plus		Qg		_	113 —		
Gate-source charge 1		Q <sub>gs1</sub>	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$		13		nC
Gate-drain ("mille	er") charge	Q <sub>gd</sub>		_	42	_	

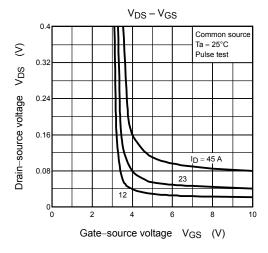
# Source-Drain Ratings and Characteristics (Ta = 25°C)

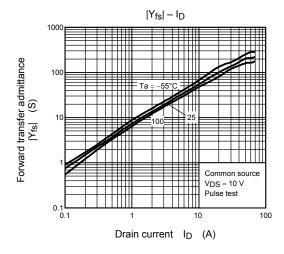
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	$I_{DRP}$	_	_	_	135	Α
Forward voltage (diode)			V <sub>DSF</sub>	$I_{DR} = 45 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

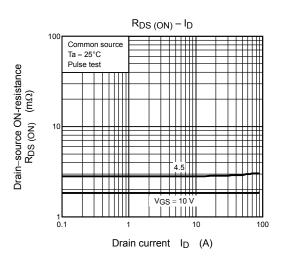




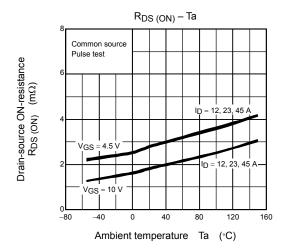


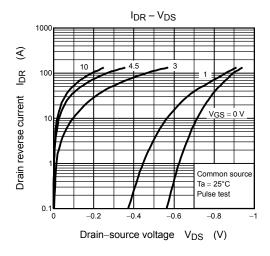


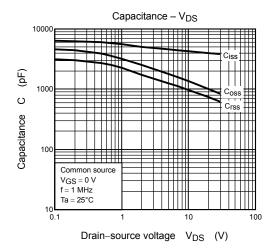


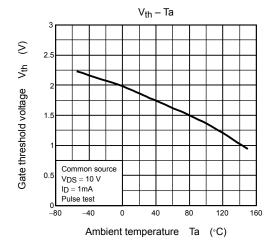


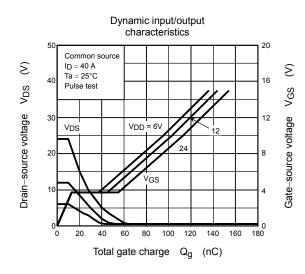
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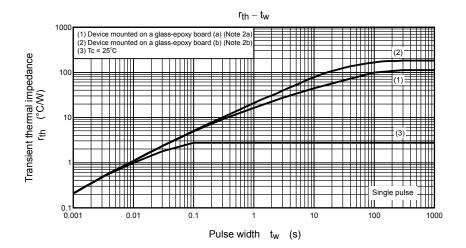


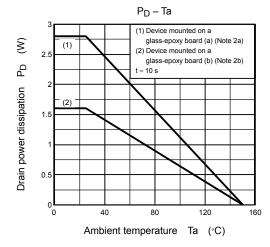


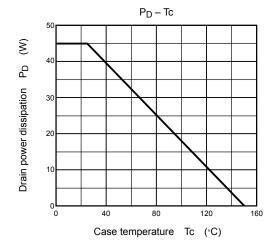


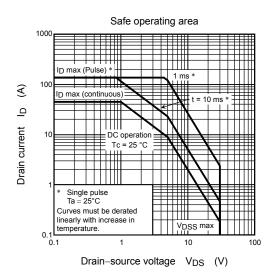


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20070701-EN GENERAL

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