

# PC827/PC847

## High Density Mounting Type Photocoupler

\* Lead forming type (I type) and taping reel type (P type) are also available.

\*\* TÜV (VDE0884) approved type is also available as an option.

### ■ Features

1. Current transfer ratio (CTR:MIN. 50% at  $I_F=5\text{mA}, V_{CE}=5\text{V}$ )
2. High isolation voltage between input and output ( $V_{\text{iso (rms)}}:5\text{kV}$ )
3. Compact dual-in-line package  
**PC827**:2-channel type  
**PC847**:4-channel type
4. Recognized by UL, file No. E64380

### ■ Applications

1. OA equipment
2. Copiers
3. Home appliances

### ■ Absolute Maximum Ratings

( $T_a=25^\circ\text{C}$ )

	Parameter	Symbol	Rating	Unit
Input	Forward current	$I_F$	50	mA
	*1 Peak forward current	$I_{FM}$	1	A
	Reverse voltage	$V_R$	6	V
	Power dissipation	$P$	70	mW
Output	Collector-emitter voltage	$V_{CEO}$	35	V
	Emitter-collector voltage	$V_{ECO}$	6	V
	Collector current	$I_C$	50	mA
	Collector power dissipation	$P_C$	150	mW
Total power dissipation		$P_{\text{tot}}$	200	mW
*2 Isolation voltage		$V_{\text{iso (rms)}}$	5	kV
Operating temperature		$T_{\text{opr}}$	-30 to +100	$^\circ\text{C}$
Storage temperature		$T_{\text{stg}}$	-55 to +125	$^\circ\text{C}$
*3 Soldering temperature		$T_{\text{sol}}$	260	$^\circ\text{C}$

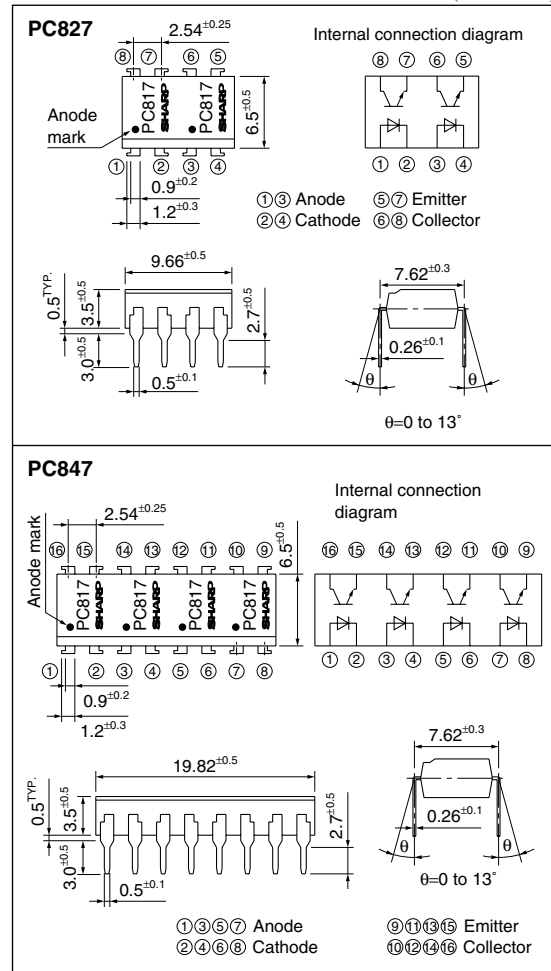
\*1 Pulse width $\leq 100\mu\text{s}$ , Duty ratio:0.001

\*2 40 to 60%RH, AC for 1 minute

\*3 For 10s

### ■ Outline Dimensions

(Unit : mm)



Notice In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.  
 Internet Internet address for Electronic Components Group <http://sharp-world.com/ecg/>

### ■ Electro-optical Characteristics

(T<sub>a</sub>=25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input	Forward voltage	V <sub>F</sub>	I <sub>F</sub> =20mA	–	1.2	1.4	V
	Peak forward voltage	V <sub>FM</sub>	I <sub>FM</sub> =0.5V	–	–	3.0	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> =4V	–	–	10	μA
	Terminal capacitance	C <sub>t</sub>	V=0, f=1kHz	–	30	250	pF
Output	Collector dark current	I <sub>CEO</sub>	V <sub>CE</sub> =20V, I <sub>F</sub> =0	–	–	100	nA
	Collector current	I <sub>C</sub>	I <sub>F</sub> =5mA, V <sub>CE</sub> =5V	2.5	–	30.0	mA
Transfer characteristics	Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>F</sub> =20mA, I <sub>C</sub> =1mA	–	0.1	0.2	V
	Isolation resistance	R <sub>ISO</sub>	DC500V, 40 to 60%RH	5×10 <sup>10</sup>	10 <sup>11</sup>	–	Ω
	Floating capacitance	C <sub>f</sub>	V=0, f=1MHz	–	0.6	1.0	pF
	Cut-off frequency	f <sub>c</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =2mA, R <sub>L</sub> =100Ω, –3dB	–	80	–	kHz
	Response time	Rise time	t <sub>r</sub>	V <sub>CE</sub> =2V, I <sub>C</sub> =2mA, R <sub>L</sub> =100Ω	–	4	18
Fall time		t <sub>f</sub>	–		3	18	μs

### ■ Rank Table

(I<sub>F</sub>=5mA, V<sub>CE</sub>=5V, T<sub>a</sub>=25°C)

Model No.	Rank mark	I <sub>C</sub> (mA)
PC8*7AB	A or B	4.0 to 13.0
PC8*7BC	B or C	6.5 to 20.0
PC8*7CD	C or D	10.0 to 30.0
PC8*7AC	A, B or C	4.0 to 20.0
PC8*7BD	B, C or D	6.5 to 30.0
PC8*7AD	A, B, C or D	4.0 to 30.0
PC8*7	A, B, C, D or no mark	2.5 to 30.0

\*:2 or 4

Fig.1 Forward Current vs. Ambient Temperature

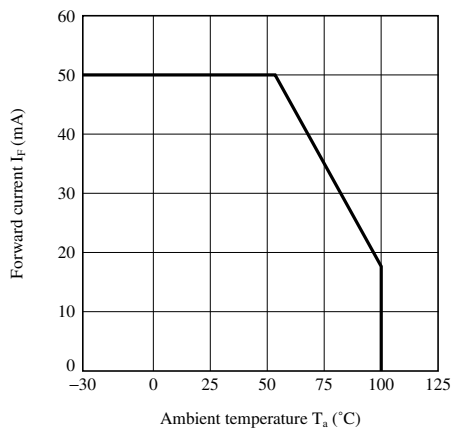


Fig.2 Collector Power Dissipation vs. Ambient Temperature

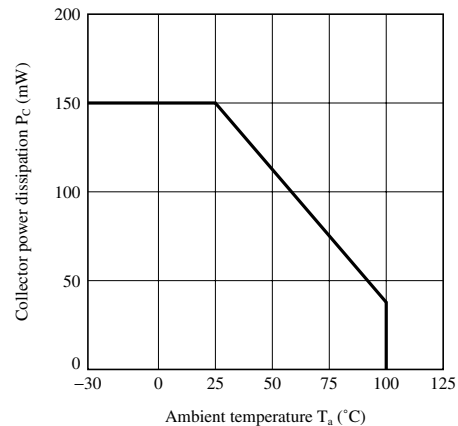


Fig.3 Peak Forward Current vs. Duty Ratio

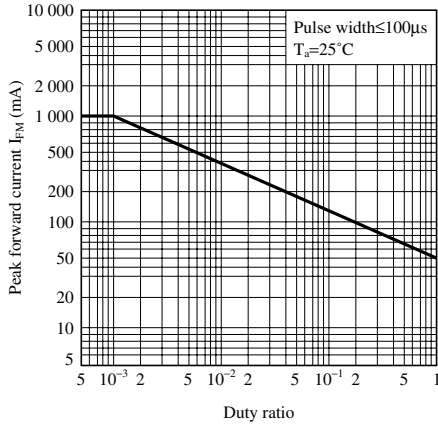


Fig.4 Current Transfer Ratio vs. Forward Current

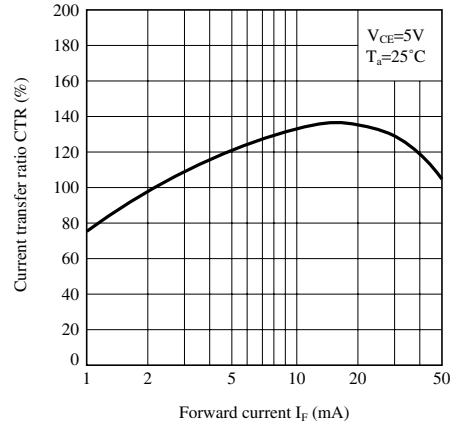


Fig.5 Forward Current vs. Forward Voltage

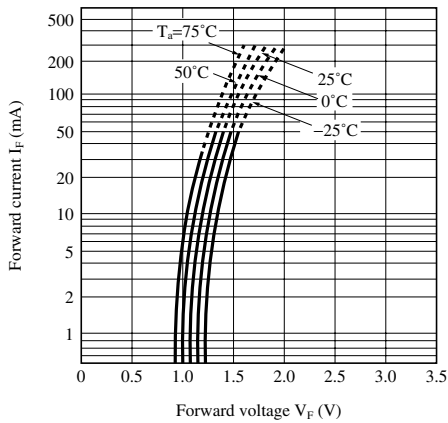


Fig.6 Collector Current vs. Collector-emitter Voltage

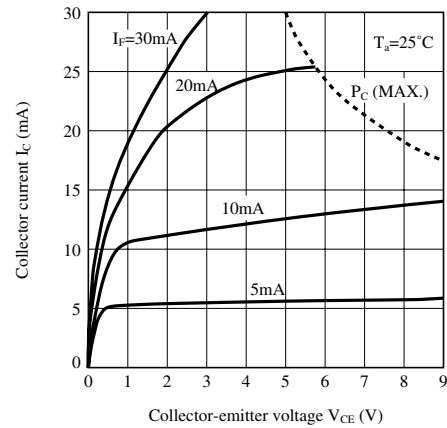


Fig.7 Relative Current Transfer Ratio vs. Ambient Temperature

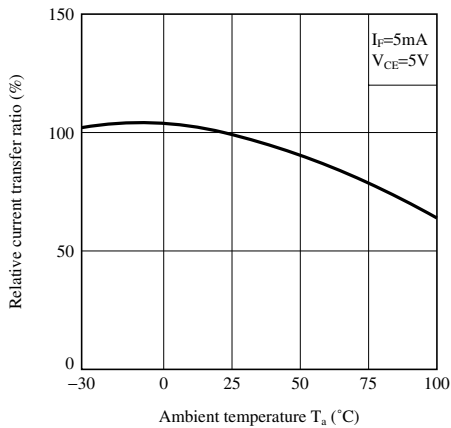
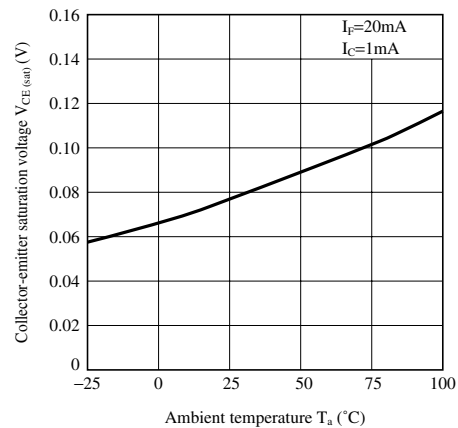
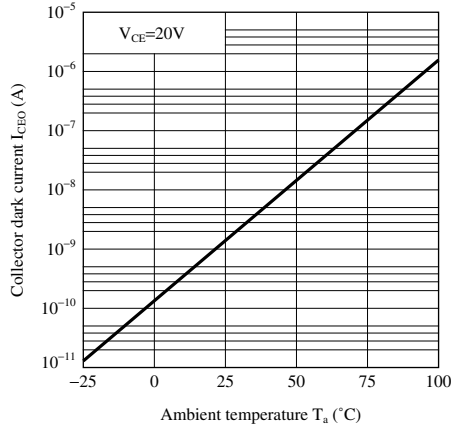


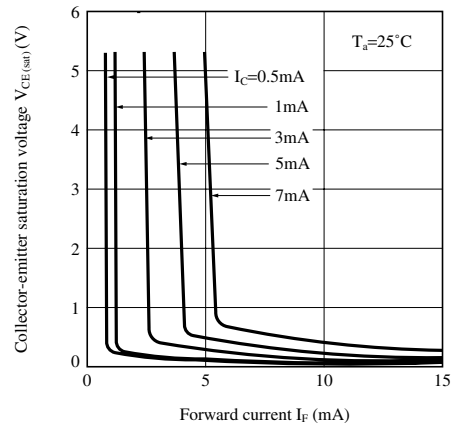
Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature



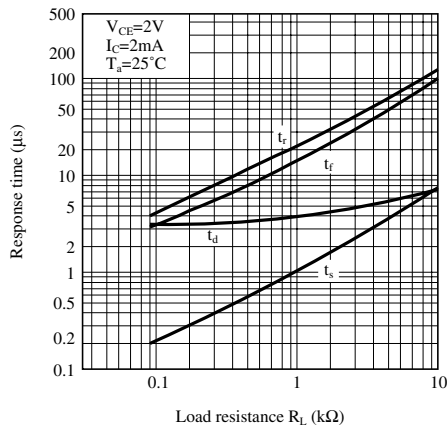
**Fig.9 Collector Dark Current vs. Ambient Temperature**



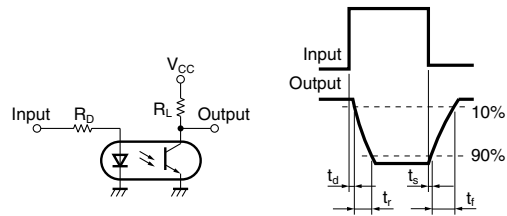
**Fig.10 Collector-emitter Saturation Voltage vs. Forward Current**



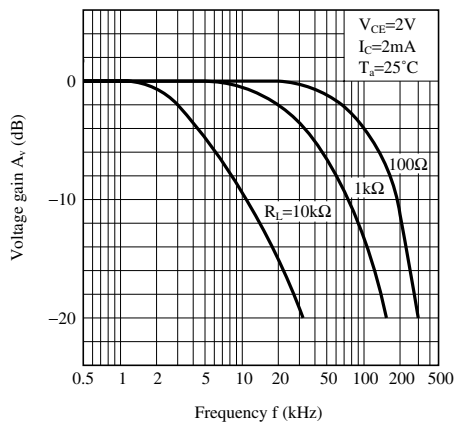
**Fig.11 Response Time vs. Load Resistance**



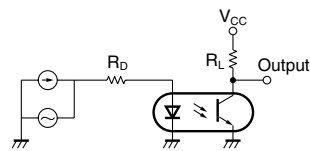
**Test Circuit for Response Time**



**Fig.12 Frequency Response**



**Test Circuit for Frequency Response**



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