The RF Line **UHF Linear Amplifier**

Designed for linear amplifier applications in 50 Ohm systems requiring wide bandwidth, low noise, and low distortion. Internal DC blocking on RF ports reduces external component count and related circuit area. This hybrid utilizes push–pull circuit design.

Supply Voltage: 15 Vdc (MHL8115)

28 Vdc (MHL8118)

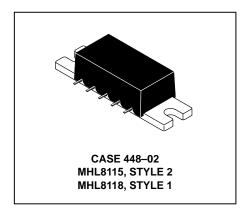
Third Order Intercept: 41.5 dBm TypPower Gain: 17.5 dB Typ (@ 900 MHz)

• Excellent Phase Linearity and Group Delay Characteristics

• 50 Ohm Input/Output Impedances

MHL8115 MHL8118

1 W, 17.5 dB 50-1000 MHz LINEAR AMPLIFIERS



ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating		Symbol	Value	Unit
DC Supply Voltage	MHL8115 MHL8118	VCC	18 32	Vdc
RF Input Power		P _{in}	+20	dBm
Storage Temperature Range		T _{stg}	-40 to +100	°C
Operating Case Temperature Range		ТC	-20 to +100	°C

ELECTRICAL CHARACTERISTICS ($T_C = +25^{\circ}C$; $V_{CC} = 15$ Vdc (MHL8115), 28 Vdc (MHL8118); 50 Ω System)

Characteristic		Symbol	Min	Тур	Max	Unit
Supply Current	MHL8115 MHL8118	IDC	_	700 400	760 440	mA
Power Gain	(f = 900 MHz)	PG	16.5	17.5	_	dB
Gain Flatness	(f = 50-1000 MHz)	FL	_	1.0	2.0	dB
Power Output @ 1 dB Comp.	(f = 900 MHz)	P _{out} 1 dB	29	30	_	dBm
Third Order Intercept (f1 = 879 MHz, f2 = 884 MHz)		ITO	40.5	41.5	_	dBm
Input/Output VSWR	(f = 50-900 MHz) (f = 900-1000 MHz)	VSWR	_	_	2.0:1 2.6:1	
Noise Figure, Broadband	(f = 500 MHz) (f = 1000 MHz)	NF	_	7.5 8.5	8.5 9.5	dB
Second Harmonic Distortion (P ₀ = 100 mW, f _{2H} = 1000 MHz)		dso	—	-55	-45	dB
Second Order Intermodulation Distortion ($P_0 = 2.75$ dBm, $f_1 = 373$ MHz, $f_2 = 450$ MHz)		IM2	_	-65	-60	dB
Intermodulation Distortion, 3 Tone (f = 860 MHz, P _{Sync} = 200 mW)		IM3	_	-60	_	dB

REV 1



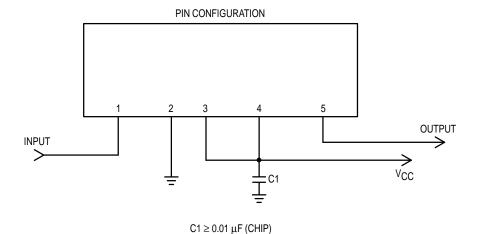


Figure 1. MHL8115 External Connections (Case 448–02, Style 2)

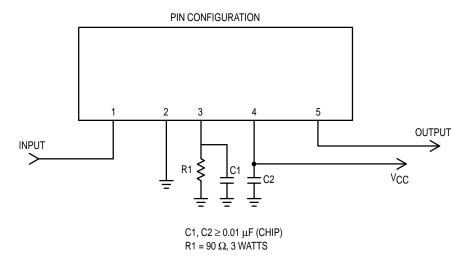
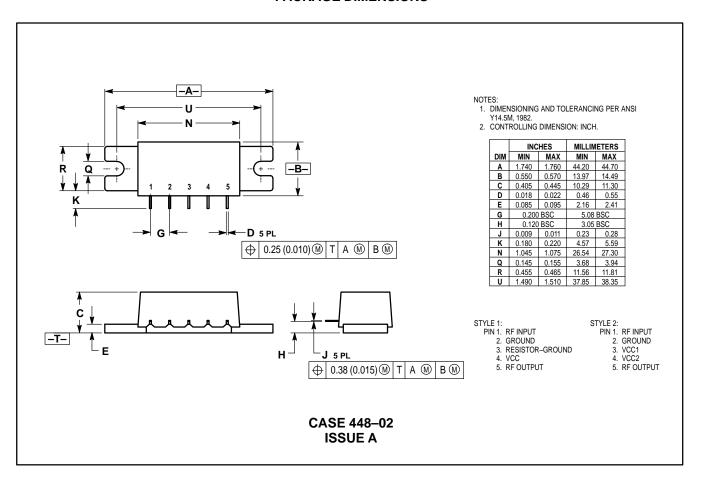


Figure 1. MHL8118 External Connections (Case 448–02, Style 1)

PACKAGE DIMENSIONS



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MHL8115/D