## **Data Sheet**

## BUY25CS54A-01

# HiRel RadHard Power-MOS

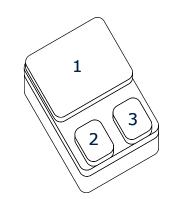
- Low R<sub>DS(on)</sub>
- Single Event Effect (SEE) hardened LET 55, Range: 90µm

 $V_{GS} = -15V$ ,  $V_{DS} = 250V$ , approved  $V_{GS} = -20V$ ,  $V_{DS} = 160V$ , approved

- Total Ionisation Dose (TID) hardened 100 kRad approved (Level R)
- Hermetically sealed
- N-channel

# Cesa Space Qualified

ESCC Detail Spec. No.: 5205/027



Туре	Marking	Pin Configuration			Package	
		1	2	3	-	
BUY25CS54A-01	-	D	G	S	-	SMD2

### **Maximum Ratings**

Parameter	Symbol	Values	Unit
Drain Source Voltage	V <sub>DS</sub>	250	V
Gate Source Voltage	$V_{GS}$	+/- 20	V
Drain Gate Voltage	$V_{DG}$	250	V
Continuous Drain Current $T_C = 25$ °C $T_C = 100$ °C	I <sub>D</sub>	54 34	A
Continuous Source Current	Is	54	А
Drain Current Pulsed, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>DM</sub>	214	Apk
Total Power Dissipation 1)	P <sub>tot</sub>	250	W
Operating and Storage Temperature	T <sub>op</sub>	-55 to + 150	°C
Avalanche Energy	E <sub>AS</sub>	380	mJ

### **Thermal Characteristics**

Thermal Resistance (Junction to Case)	R <sub>th JC</sub>	0.5	K/W
Soldering Temperature	T <sub>sol</sub>	250	°C

## Notes.:

1) For  $T_S \leq 25^{\circ} \text{C}$ . For  $T_S > 25^{\circ} \text{C}$  derating is required.

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# **Data Sheet**

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# Electrical Characteristics, at T<sub>A</sub>=25°C; unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	max.		
DC Characteristics					
Breakdown Voltage Drain to Source $I_D = 0.25 \text{mA}$ , $V_{GS} = 0 \text{V}$	B <sub>VDSS</sub>	250	-	V	
Gate Threshold Voltage $I_D = 1.0 \text{mA}, V_{DS} \ge V_{GS}$	$V_{GS(th)}$	2.0	4.0	V	
Gate to Source Leakage Current $V_{DS} = 0V$ , $V_{GS} = +/-20V$	I <sub>GSS</sub>	-	+/-100	nA	
Drain Current V <sub>DS</sub> = 200V, V <sub>GS</sub> = 0V	I <sub>DSS</sub>	-	25	μΑ	
Drain Source On Resistance <sup>1)</sup> V <sub>GS</sub> = 10V, I <sub>D</sub> = 34A	r <sub>DS(ON)</sub>	-	0.03	Ω	
Source Drain Diode, Forward Voltage $^{1), 2)}$ $V_{GS} = 0V$ , $I_{S} = 54A$	V <sub>SD</sub>	-	1.2	V	
AC Characteristics					
Turn-on Delay Time $V_{DD} = 50\% V_{DS}$ , $I_D = 34A$ , $R_G = 4.7\Omega$	t <sub>d(ON)</sub>	-	80	ns	
Rise Time $V_{DD} = 50\% V_{DS}$ , $I_D = 34A$ , $R_G = 4.7\Omega$	t <sub>r</sub>	-	80	ns	
Turn-off Delay Time $V_{DD} = 50\% \ V_{DS}, \ I_D = 34A, \ R_G = 4.7\Omega$	t <sub>d(OFF)</sub>	-	130	ns	
Fall Time $V_{DD} = 50\% \ V_{DS}, \ I_D = 34A, \ R_G = 4.7\Omega$	t <sub>f</sub>	-	80	ns	
Reverse Recovery Time $V_{DD} < 50\% V_{DS}$ , $I_D = 54A$	t <sub>rr</sub>	-	700	ns	
Common Source Input Capacitance $V_{DS} = 100V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	C <sub>iss</sub>	9.0	14.0	nF	
Common Source Output Capacitance $V_{DS} = 100V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	C <sub>oss</sub>	600	1000	pF	
Common Source Reverse Transfer Capacitance $V_{DS} = 100V, V_{GS} = 0V, f = 1.0MHz$	C <sub>rss</sub>	5	30	pF	
Total Gate Charge $V_{DD} = 50\% V_{DS}, V_{GS} = 10V, I_D = 54A$	$Q_G$	-	180	nC	

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Notes.:
1) Pulsed Measurement: Pulse Width < 300µs, Duty Cycle <2.0%.

<sup>2)</sup> Measured within 2.0 mm of case.

# **Data Sheet**

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### **Electrical Characteristics**

at T<sub>A</sub>=125°C; unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	max.		
DC Characteristics					
Gate Threshold Voltage $I_D = 1.0$ mA, $V_{DS} \ge V_{GS}$	$V_{GS(th)}$	1.5	-	V	
Gate to Source Leakage Current $V_{DS} = 0V$ , $V_{GS} = +/-20V$	I <sub>GSS</sub>	-	+/-200	nA	
Drain Current $V_{DS} = 200V$ , $V_{GS} = 0V$	I <sub>DSS</sub>	-	250	μΑ	
Drain Source On Resistance $^{1)}$ $V_{GS} = 10V$ , $I_D = 34A$	r <sub>DS(ON)</sub>	-	0.07	Ω	

## Notes.:

### **Electrical Characteristics**

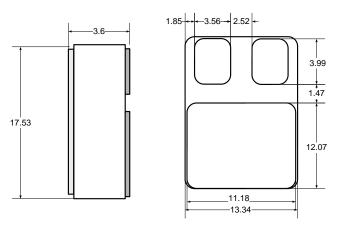
at T<sub>A</sub>=-55°C; unless otherwise specified

Parameter	Symbol	Values		Unit	
		min.	max.		
DC Characteristics					
Gate Threshold Voltage $I_D = 1.0 \text{mA}, V_{DS} \ge V_{GS}$	$V_{GS(th)}$	-	5.0	V	

<sup>1)</sup> Pulsed Measurement: Pulse Width < 300µs, Duty Cycle <2.0%.

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## SMD2 Package



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Dimensions are typical [mm]

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