

IGBT³ Chip

FEATURES:

- 1200V Trench + Field Stop technology
- 120µm chip
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

This chip is used for:

power module



Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code
SIGC32T120R3L	1200V	25A	6.5 x 4.87 mm ²	sawn on foil	Q67050- A4206-A101

MECHANICAL PARAMETER:

Raster size	6.5 x 4.87		
Emitter pad size (include gate pad)	3.4 x 4.99		
Gate pad size	1.14 x 1.14		
Area total / active	31.6 / 21.5		
Thickness	120	μm	
Wafer size	150	mm	
Flat position	180		
Max.possible chips per wafer	454 pcs		
Passivation frontside	Photoimide		
Emitter metallization	3200 nm AlSiCu		
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding		
Die bond	electrically conductive glue or solder		
Wire bond	Al, <500μm		
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm		
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C		

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MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, <i>T</i> _j =25 °C	V _{CE}	1200	V
DC collector current, limited by T _{jmax}	I _C	1)	Α
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	75	Α
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T_{j}, T_{stg}	-55 + 150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_{j} =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
Tarameter	Cymbol		min.	typ.	max.	0
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0 V , I_{C} = 1 mA	1200			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =25A	1.35	1.65	2.05	V
Gate-emitter threshold voltage	V _{GE(th)}	I _C =1mA , V _{GE} =V _{CE}	5.0	5.8	6.5	
Zero gate voltage collector current	I _{CES}	V _{CE} =1200V , V _{GE} =0V			3.35	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V , V _{GE} =20V			600	nA
Integrated gate resistor	R _{Gint}			8		Ω

ELECTRICAL CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
i arameter	Symbol	Conditions	min.	typ.	max.	Oiiit
Input capacitance	Ciss	V _{CE} =25V,		1808		pF
Output capacitance	Coss	$V_{GE}=0V$,		95		
Reverse transfer capacitance	Crss	f=1MHz		82		

SWITCHING CHARACTERISTICS (tested at component), Inductive Load

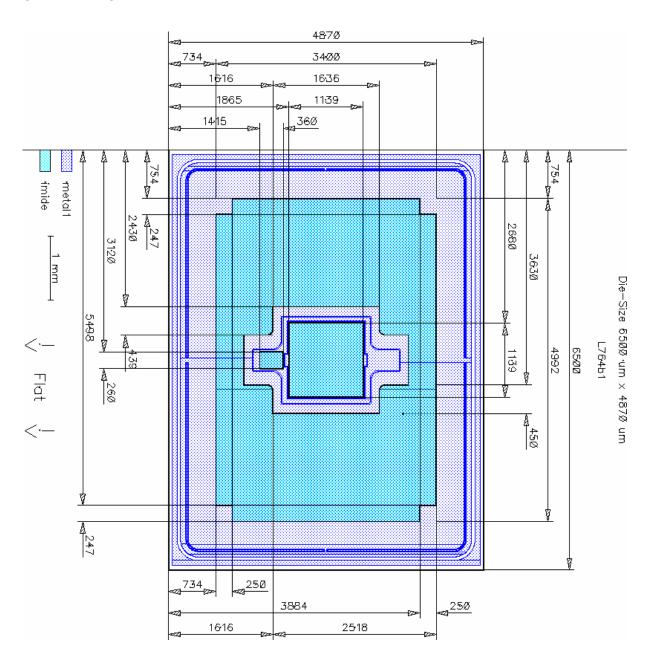
Parameter	Symbol	Conditions 1)	Value			Unit
- arameter	Cymbol		min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	T _j =125°C		0.09		μs
Rise time	t _r	$V_{\rm CC} = 600 \rm V$,		0.05		
Turn-off delay time	$t_{d(off)}$	I _C =25A, V _{GE} =-15/15V,		0.52		
Fall time	t _f	$R_{\rm G}$ = 36Ω		0.09		

¹⁾ values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet	tbd	
DESCRIPTION:		
AQL 0,65 for visual inspection according to failu	ure catalog	
	" . AW OTD	
Electrostatic Discharge Sensitive Device accord	ding to MIL-STD 883	

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Test-Normen Villach/Prüffeld

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