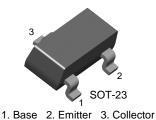


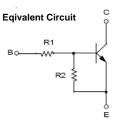
FJV3114R NPN Epitaxial Silicon Transistor

Features

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R1=4.7KΩ, R2=47KΩ)
- Complement to FJV4114R







Absolute Maximum Ratings * T_a = 25°C unless otherwise noted Symbol Parameter Value Units 50 V V_{CBO} Collector-Base Voltage V_{CEO} Collector-Emitter Voltage 50 V 10 V V_{EBO} Emitter-Base Voltage Collector Current 100 mΑ I_C °C T_{STG} Storage Temperature Range -55~150 150 °C TJ **Junction Temperature** Collector Power Dissipation, by $R_{\theta JA}$ 200 mW P_C

* These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Electrical Characteristics^{*} $T_{C} = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	MIN	Тур	MAX	Units
V(BR)CBO	Collector-Emitter Breakdown Voltage	Ic = 10 uA, IE = 0	50			V
V(BR)CEO	Collector-Base Breakdown Voltage	Ic = 100 uA, I _B = 0	50			V
Ісво	Collector-Cutoff Current	$V_{CB} = 40 V, I_E = 0$			0.1	uA
hfe	DC Current Gain	Vce = 5 V, Ic = 5 mA	68			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	Ic = 10 mA, I _B = 0.5 mA			0.3	V
f⊤	Current Gain - Bandwidth Product	Vce = 10V, Ic = 5 mA		250		MHz
Ccb	Output Capacitance	Vcb = 10 V, IE = 0, f = 1.0 MHz		3.7		pF
VI(off)	Input Off Voltage	Vce = 5 V, Ic = 100uA	0.5			V
VI(on)	Input On Voltage	Vce = 0.2V, Ic = 5mA			1.3	V
R1	Input Resistor		3.2	4.7	6.2	KΩ
R1/R2	Resistor Ratio		0.09	0.1	0.11	

* Pulse Test: PW≤300µs, Duty Cycle≤2%

November 2006

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Typical Performance Characteristics

Figure 1. DC current Gain

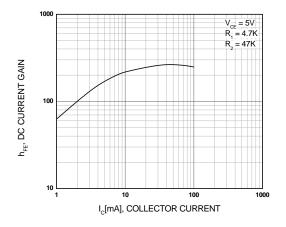
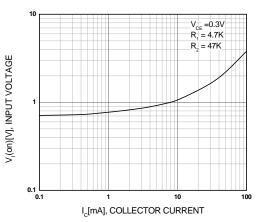
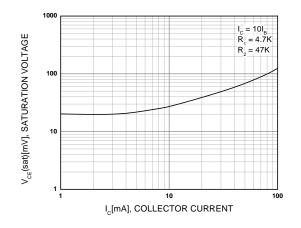


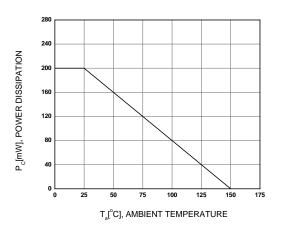
Figure 2. Input On Voltage

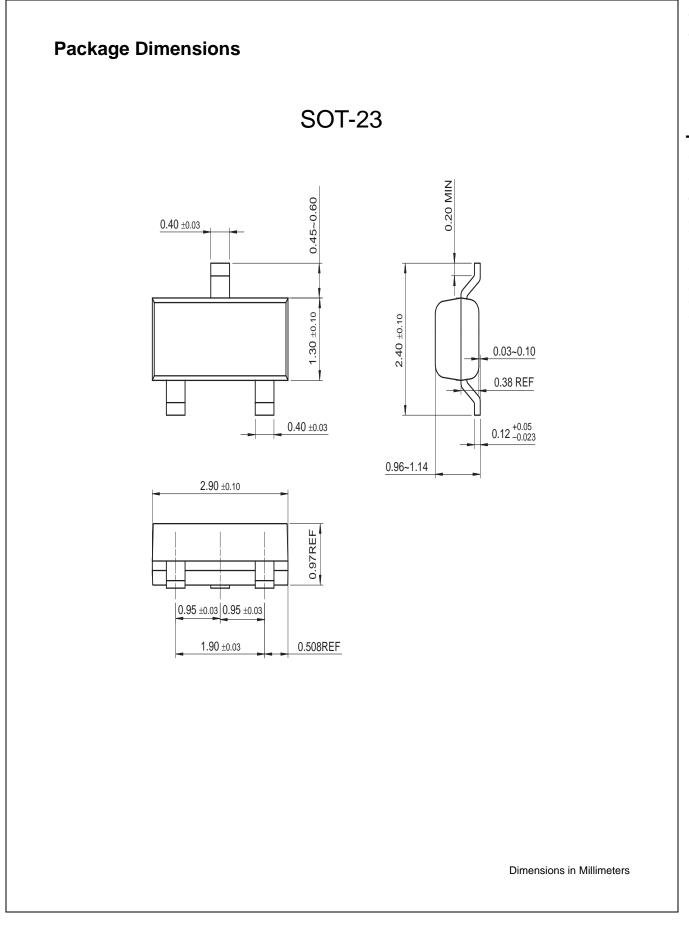














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