

• High Current Gain - Bandwidth Product: $f_T = 2MHz$ (MIN), $I_C = -500mA$

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

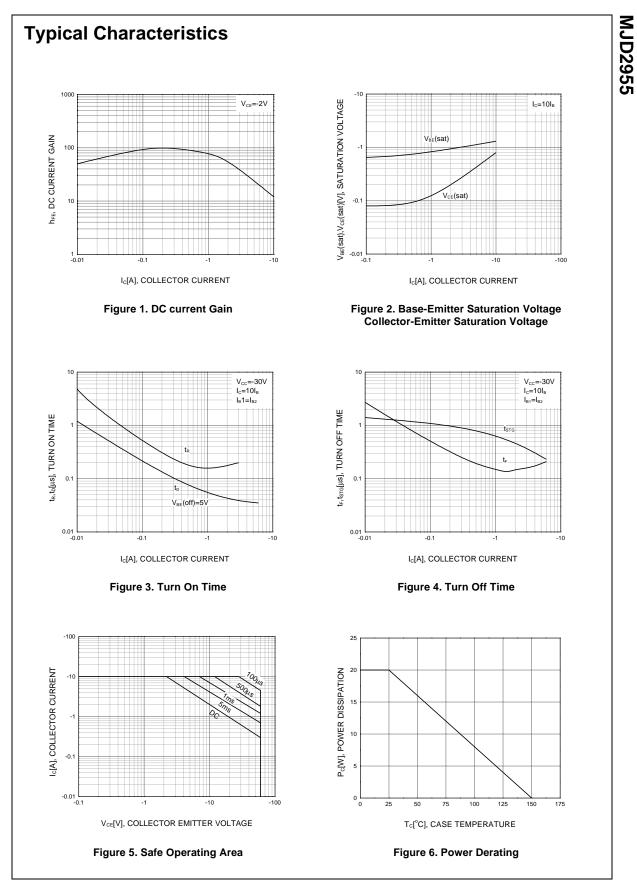
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	- 70	V
V _{CEO}	Collector-Emitter Voltage	- 60	V
V _{EBO}	Emitter-Base Voltage	- 5	V
I _C	Collector Current	- 10	А
I _B	Base Current	- 6	А
P _C	Collector Dissipation (T _C =25°C)	20	W
	Collector Dissipation (T _a =25°C)	1.75	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage	I _C = - 30mA, I _B = 0	-60		V
I _{CEO}	Collector Cut-off Current	$V_{CE} = -30V, I_E = 0$		- 50	μΑ
I _{CBO}	Collector Cut-off Current	$V_{CB} = -70V, I_E = 0$		- 2	mA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 0.5	mA
h _{FE}	* DC Current Gain	$V_{CE} = -4V, I_{C} = -4A$	20	100	
		$V_{CE} = -4V, I_{C} = -10A$	5		
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	$I_{\rm C} = -4A, I_{\rm B} = -0.4A$		- 1.1	V
		I _C = - 10A, I _B = - 3.3A		- 8	V
V _{BE} (on)	* Base-Emitter ON Voltage	V _{CE} = - 4V, I _C = - 4A		-1.8	V
f _T	Current Gain Bandwidth Product	V _{CE} = - 10V, I _C = - 500mA	2		MHz

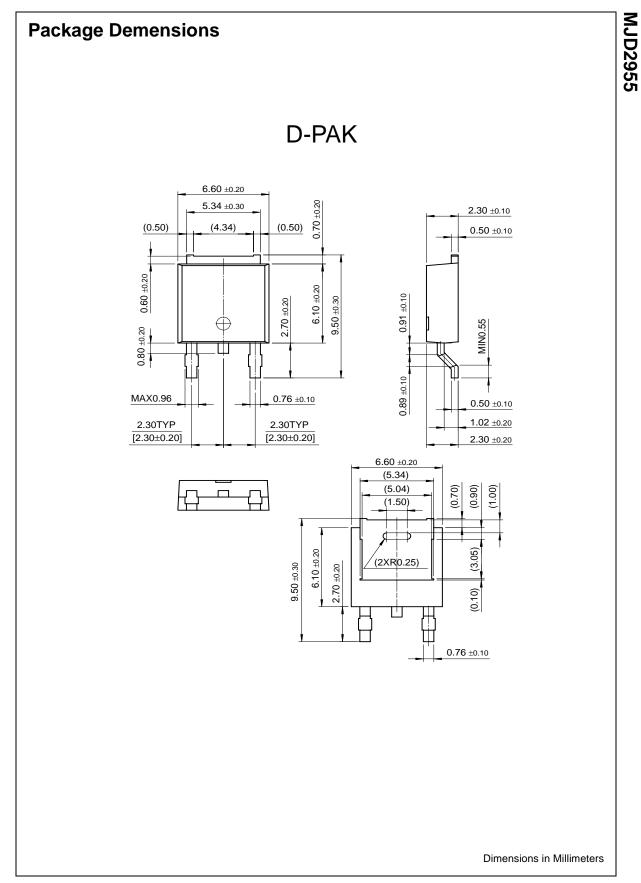
* Pulse Test: PW≤300ms, Duty Cycle≤2%

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