# FAIRCHILD

SEMICONDUCTOR TM

# **MJE210**

### Feature

- Low Collector-Emitter Saturation Voltage
- High Current Gain Bandwidth Product : f<sub>T</sub>=65MHz@I<sub>C</sub>= -100mA (Min.)
- Complement to MJE200



## **PNP Epitaxial Silicon Transistor**

Absolute	Maximum Ratings	T <sub>C</sub> =25°C unless otherw	ise noted

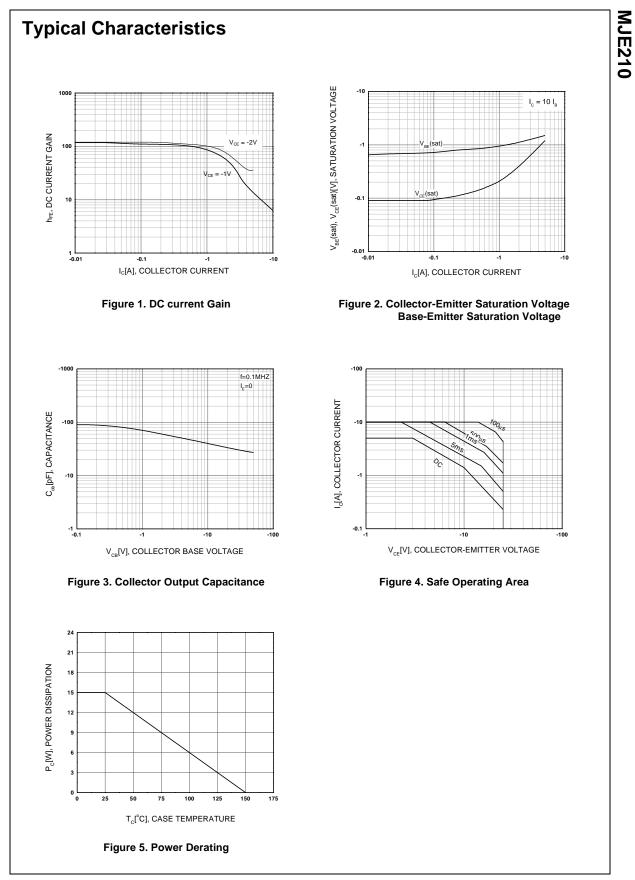
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	- 40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	- 25	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 8	V
I <sub>C</sub>	Collector Current	- 5	A
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	15	W
Τ <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

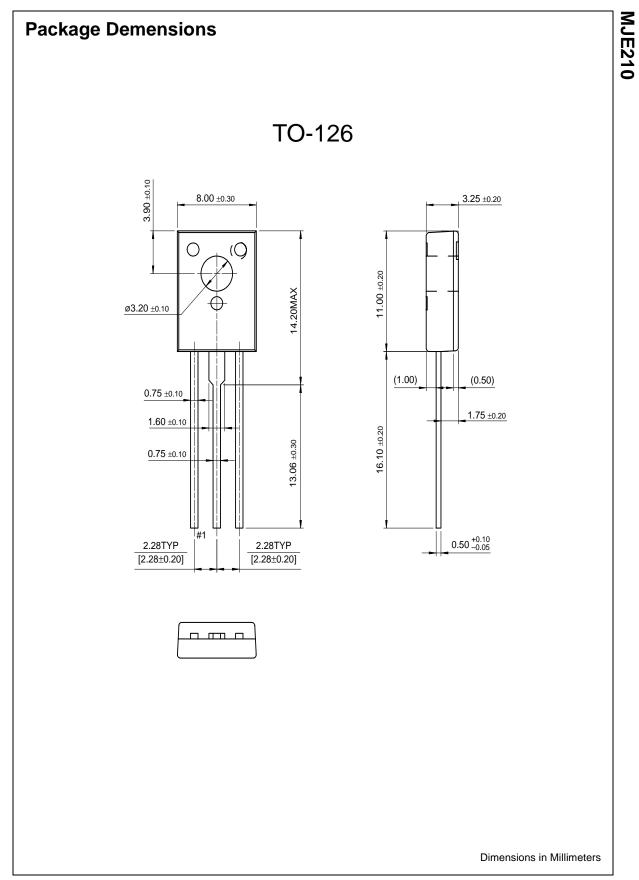
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = - 10mA, I <sub>B</sub> = 0	-25		V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -40V, I_{E} = 0$		-100	nA
		$V_{CB} = -40V, I_E = 0 @ T_J = 125^{\circ}C$		-100	μA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{BE} = -8V, I_{C} = 0$		-100	nA
h <sub>FE1</sub>	DC Current Gain	V <sub>CE</sub> = - 1V, I <sub>C</sub> = - 500mA	70		
h <sub>FE2</sub>		V <sub>CE</sub> = - 1V, I <sub>C</sub> = - 2A	45	180	
h <sub>FE3</sub>		V <sub>CE</sub> = - 2V, I <sub>C</sub> = - 5A	10		
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = - 500mA, I <sub>B</sub> = - 50mA		-0.3	V
		I <sub>C</sub> = - 2A, I <sub>C</sub> = - 200mA		-0.75	V
		I <sub>C</sub> = - 5A, I <sub>B</sub> = - 1A		-1.8	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = - 5A, I <sub>B</sub> = - 1A		-2.5	V
V <sub>BE</sub> (on)	Base-Emitter ON Voltage	V <sub>CE</sub> = - 1V, I <sub>C</sub> = - 2A		-1.6	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = - 10V, I <sub>C</sub> = - 100mA	65		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = - 10V, I <sub>E</sub> = 0, f = 1MHz		120	pF

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