

SCHOTTKY BARRIER RECTIFIER

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{RRM}	Maximum Repetitive Reverse Voltage	60	V
V _R	Maximum DC Reverse Voltage	60	V
I _{F(AV)}	Average Rectified Forward Current @ $T_C = 134^{\circ}C$	20	A
I _{FSM}	Non-repetitive Peak Surge Current (per diode) 60Hz Single Half-Sine Wave	200	A
T _{J,} T _{STG}	Operating Junction and Storage Temperature	-65 to +150	°C

Thermal Characteristics

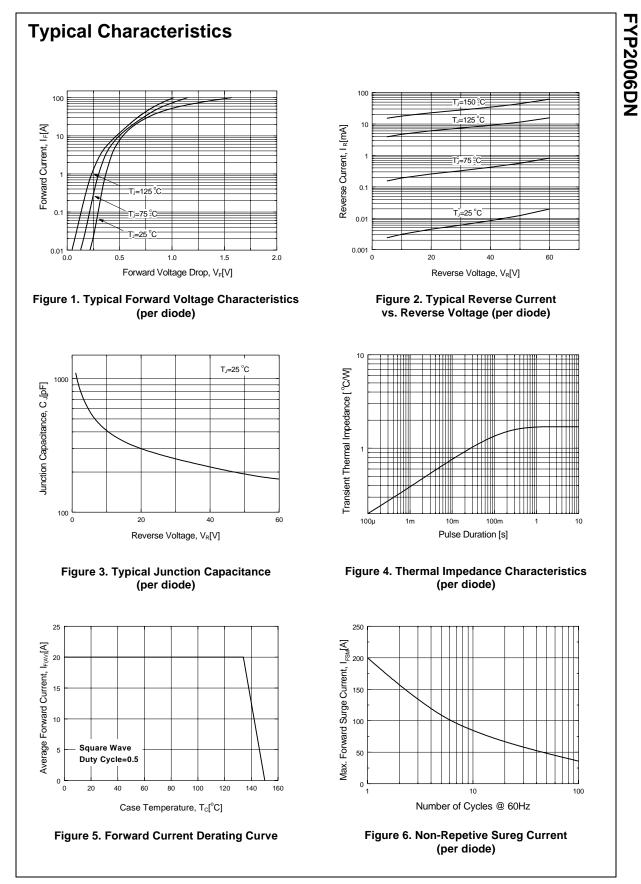
Symbol	Parameter	Value	Units
$R_{ extsf{ heta}JC}$	Maximum Thermal Resistance, Junction to Case (per diode)	1.7	°C/W

Electrical Characteristics (per diode)

Symbol	Parameter		Value	Units
V _{FM} *	Maximum Instantaneous Forward Voltage			V
	I _F = 10A	T _C = 25 °C	0.58	
	I _F = 10A	T _C = 25 °C T _C = 125 °C	0.52	
	I _F = 20A	T _C = 25 °C	0.71	
	I _F = 20A	T _C = 125 °C	0.65	
I _{RM} *	Maximum Instantaneous Reverse Current			mA
	@ rated V _R	T _C = 25 °C	1	
		T _C = 25 °C T _C = 125 °C	50	

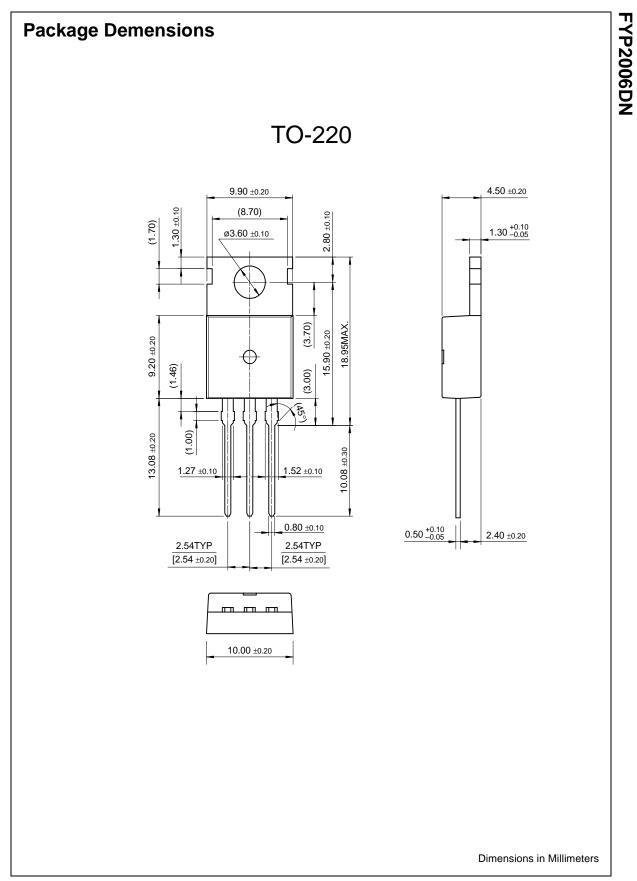
* Pulse Test: Pulse Width=300µs, Duty Cycle=2%

©2001 Fairchild Semiconductor Corporation



©2001 Fairchild Semiconductor Corporation

Rev. A, March 2001



©2001 Fairchild Semiconductor Corporation

Rev. A, March 2001

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST [®]	PACM
Bottomless™	FASTr™	POP™
CoolFET™	GlobalOptoisolator™	Power
CROSSVOLT™	GTO™	QFET
DenseTrench™	HiSeC™	QS™
DOME™	ISOPLANAR™	QT Op
EcoSPARK™	LittleFET™	Quiet S
E ² CMOS™	MicroFET™	SLIEN [®]
EnSigna™	MICROWIRE™	SMAR
FACT™	OPTOLOGIC™	Star* F
FACT Quiet Series™	OPTOPLANAR™	Stealth

PACMAN[™] POP[™] PowerTrench[®] QFET[™] QS[™] QT Optoelectronics[™] Quiet Series[™] SLIENT SWITCHER[®] SMART START[™] Star* Power[™] Stealth[™] SuperSOT[™]-3 SuperSOT[™]-6 SuperSOT[™]-8 SyncFET[™] TinyLogic[™] UHC[™] UHC[™] UltraFET[®] VCX[™]

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTENAPPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2001 Fairchild Semiconductor Corporation