

10V Drive Nch MOSFET

RCX330N25

Structure

Silicon N-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage V_{GSS} garanteed to be $\pm 30 \text{V}$.
- 4) High package power.

• Dimensions (Unit : mm) TO-220FM TO-220FM TO-220FM TO-220FM TO-220FM TO-220FM

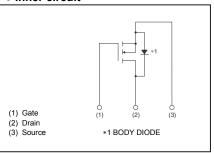
Application

Switching

Packaging specifications

	Package	Bulk	
Type	Code	ı	
	Basic ordering unit (pieces)	500	
RCX330N2	0		

• Inner circuit



● Absolute maximum ratings (Ta = 25°C)

Paramet	Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	250	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	Continuous	I _D *3	±33	Α
Diam current	Pulsed	I _{DP} *1	±132	Α
Source current (Body Diode)	Continuous	l _S *3	33	Α
	Pulsed	I _{SP} *1	132	Α
Avalanche Current		I _{AS} *2	16.5	Α
Avalanche Energy		E _{AS} *2	74.8	mJ
Power dissipation (Tc=25°C)		P_D	40	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw≤10µs, Duty cycle≤1%

Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	Rth (ch-c)	3.13	°C/W

^{*2} L \doteqdot 500 μ H, V_{DD}=50V, Rg=25 Ω , starting Tch=25°C

^{*3} Limited only by maximum temperature allowed.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	=	-	±100	nA	V_{GS} =±30V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	250	-	-	٧	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	=	-	10	μΑ	V _{DS} =250V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	3	-	5	٧	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS (on)}	-	77	105	mΩ	I _D =16.5A, V _{GS} =10V
Forward transfer admittance	I Y _{fs} I*	10	-	-	S	I _D =16.5A, V _{DS} =10V
Input capacitance	C _{iss}	=	4500	-	pF	V _{DS} =25V
Output capacitance	C _{oss}	=	220	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	=	130	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	50	-	ns	I _D =16.5A, V _{DD} ≒125V
Rise time	t _r *	=	200	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	=	120	-	ns	R_L =7.6 Ω
Fall time	t _f *	=	140	-	ns	R_G =10 Ω
Total gate charge	Q _g *	=	80	-	nC	I _D =33A,
Gate-source charge	Q _{gs} *	-	25	-	nC	V _{DD} ≒125V
Gate-drain charge	Q _{gd} *	-	27	-	nC	V _{GS} =10V

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.5	V	I_s =33A, V_{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

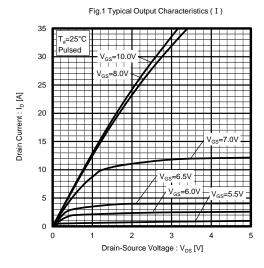


Fig.3 Typical Transfer Characteristics

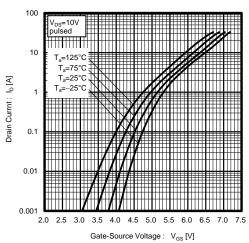


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

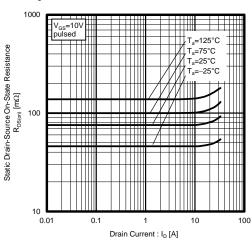


Fig.2 Typical Output Characteristics (${\rm I\hspace{-.1em}I}$)

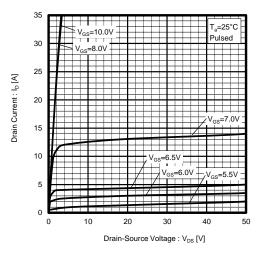


Fig.4 Gate Threshold Voltage vs. Channel Temperature

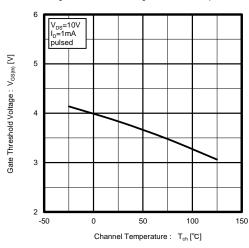


Fig.6 Static Drain-Source On-State Resistance vs. Channel Temperature

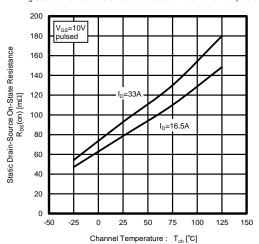


Fig.7 Forward Transfer Admittance vs. Drain Current

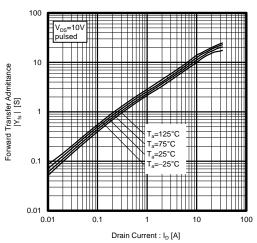


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

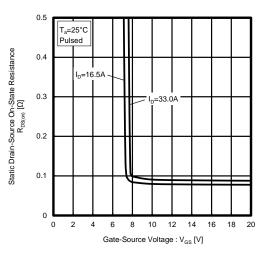


Fig.11 Dynamic Input Characteristics

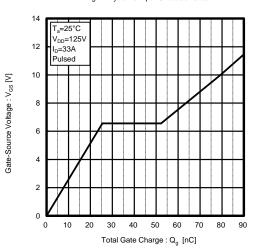


Fig.8 Source Current vs. Source-Drain Voltage

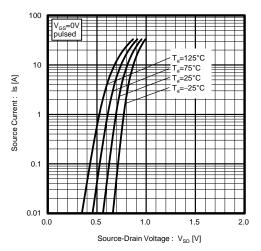


Fig.10 Switching Characteristics

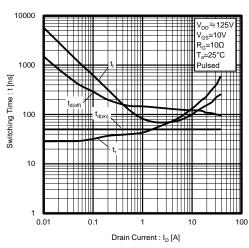
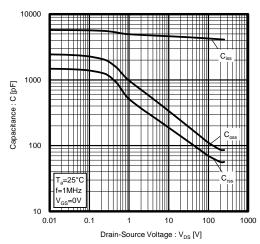


Fig.12 Typical Capacitance vs. Drain-Source Voltage



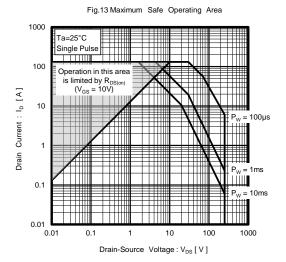
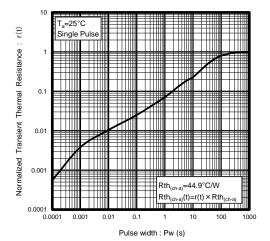


Fig.14 Normalized Transient Thermal Resistance v.s. Pulse Width



Measurement circuits

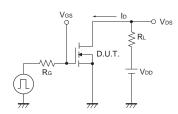


Fig.1-1 Switching Time Measurement Circuit

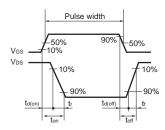


Fig.1-2 Switching Waveforms

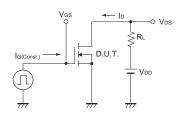


Fig.2-1 Gate Charge Measurement Circuit

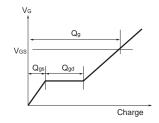


Fig.2-2 Gate Charge Waveform

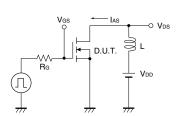


Fig.3-1 Avalanche Measurement Circuit

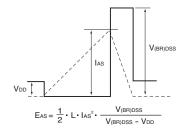


Fig.3-2 Avalanche Waveform

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