

# Preliminary

# SIDC03D60F6

# Fast switching diode chip in EMCON-Technology

### **FEATURES:**

- 600V EMCON technology 70 µm chip
- · soft , fast switching
- low reverse recovery charge
- small temperature coefficient

# This chip is used for:

EUPEC power modules and discrete devices



## Applications:

SMPS, resonant applications, drives

Chip Type	$V_R$	I <sub>F</sub>	Die Size	Package	Ordering Code	
SIDC03D60F6	600V	6A	1.2 x 2.25 mm <sup>2</sup>	sawn on foil	Q67050-A4037-	
0.2 0002 00. 0	000 1 0	0/1	1.2 x 2.20 11111	oawii oii ioii	A001	

## **MECHANICAL PARAMETER:**

Raster size	1.2 x 2.25				
Area total / active	2.7 / 1.45	mm <sup>2</sup>			
Anode pad size	1.77 x 0.72				
Thickness	70	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	5650 pcs				
Passivation frontside	Photoimide				
Anode metallisation	3200 nm AlSiCu				
Cathode metallisation	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤250μm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
store in original container, in dry nitrogen,  Recommended Storage Environment < 6 month at an ambient temperature of 23°0					

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# **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Continuous forward current limited by	$I_{F}$		6	
_T <sub>jmax</sub>	/F		0	
Single pulse forward current	$I_{FSM}$	$t_P = 10 \text{ ms sinusoidal}$	tbd	Α
(depending on wire bond configuration)	, L 2 IVI	tp = 10 me omaceidar	3	] ``
Maximum repetitive forward current				
limited by T <sub>jmax</sub>	I <sub>FRM</sub>		12	
(depending on wire bond configuration)				
Operating junction and storage temperature	$T_{\rm j}$ , $T_{\rm stg}$		-55+150	°C

# Static Electrical Characteristics (tested on chip), $T_j$ =25 °C, unless otherwise specified

Parameter	Symbol	Condi	Value			Unit	
raiailletei	Symbol	Conditions		min.	Тур.	max.	John
Reverse leakage current	$I_{R}$	V <sub>R</sub> =600V	<i>T<sub>j</sub></i> =25°C			27	μΑ
Cathode-Anode breakdown Voltage	V <sub>Br</sub>	I <sub>R</sub> =500μA	<i>T<sub>j</sub></i> =25°C	600			V
Forward voltage drop	V <sub>F</sub>	I <sub>F</sub> =6A	<i>T<sub>j</sub></i> =25°C		1.45		V

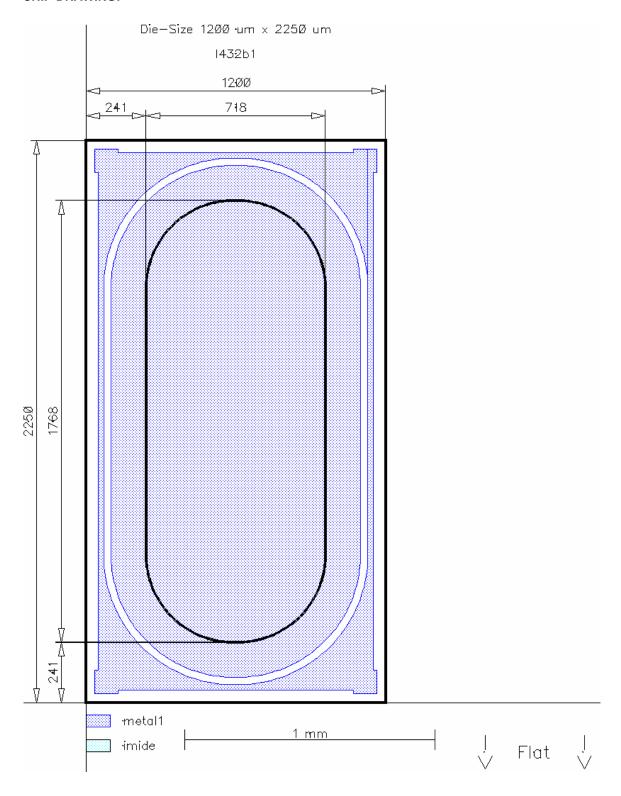
# **Dynamic Electrical Characteristics**, at $T_j = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol Cond			Value			Unit
Parameter			itions	min.	Тур.	max.	Unit
Reverse recovery time	t <sub>rr1</sub>	I <sub>F</sub> =6A	$T_j = 25$ °C		70		
	t <sub>rr2</sub>	$di/dt=550A/ms$ $V_R=400V$	$T_j = 150$ °C		105		ns
Peak recovery current	I <sub>RRM1</sub>	$I_F=6A$ $di/dt=550A/ms$ $V_R=400V$	$T_j = 25$ °C		6.5		^
	I <sub>RRM2</sub>		$T_j = 150$ °C		7.9		A
Reverse recovery charge	Q <sub>rr1</sub>	$I_F=6A$ $di/dt=550A/ms$ $V_R=400V$	T <sub>j</sub> =25°C		240		n C
	Q <sub>rr2</sub>		T <sub>j</sub> =150°C		400		
Peak rate of fall of reverse recovery current	di <sub>rr1</sub> /dt	I <sub>F</sub> =6A	T <sub>j</sub> = 25 °C		tbd		Δ /
	di <sub>rr2</sub> /dt	di/dt=550A/ms $V_R=400V$	T <sub>j</sub> =150°C				- A/μs
Softness	S1	I <sub>F</sub> =6A	<i>T<sub>j</sub></i> =25 °C		4		1
	S2	$V_{R} = 400 V$	T <sub>j</sub> =150°C		4.9		



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# **CHIP DRAWING:**



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# **Preliminary**

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# This chip data sheet refers to the device data sheet Description: AQL 0,65 for visual inspection according to failure catalog Electrostatic Discharge Sensitive Device according to MIL-STD 883 Test-Normen Villach/Prüffeld

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