

Innovator in Electronics

Murata Manufacturing Co., Ltd.

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for EU RoHS Compliant

- · All the products in this catalog comply with EU RoHS.
- EU RoHS is "the European Directive 2002/95/EC on the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment".
- · For more details, please refer to our website 'Murata's Approach for EU RoHS' (http://www.murata.com/info/rohs.html).



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Part Numbering

Chip Monolithic Ceramic Capacitors

(Part Number) GC M 18 8 R7 1H 102 K A37 D

Product ID

2 Series

Product ID	Code	Series
GC M		Power-train, Safety Equipment

3Dimension (LXW)

Code	Dimension (L×W)	EIA		
03	0.6×0.3mm	0201		
15	1.0×0.5mm	0402		
18	1.6×0.8mm	0603		
21	2.0×1.25mm	0805		
31	3.2×1.6mm	1206		
32	3.2×2.5mm	1210		

4 Dimension (T)

Code	Dimension (T)					
3	0.3mm					
5	0.5mm					
6	0.6mm					
8	0.8mm					
9	0.85mm					
Α	1.0mm					
В	1.25mm					
С	1.6mm					
D	2.0mm					
E	2.5mm					
М	1.15mm					
N	1.35mm					
R	1.8mm					
X	Depends on individual standards.					

5Temperature Characteristics

Temperature	Characteristic Co	odes		Operating		
Code	Public STD (Code	Reference Temperature	Temperature Range	Capacitance Change or Temperature Coefficient	Temperature Range
5C	COG EIA		25°C	25 to 125°C	0±30ppm/°C	-55 to 125°C
7U	U2J EIA		25°C	25 to 125°C	-750±120ppm/°C	-55 to 125°C
C7	C7 X7S EIA		25°C	-55 to 125°C	±22%	-55 to 125°C
R7 X7R EIA		25°C	-55 to 125°C	±15%	-55 to 125°C	

●Capacitance Change from each temperature

	Capacitance Change from 25°C (%)							
Murata Code	−55°C		-30)°C	−10°C			
	Max.	Min.	Max.	Min.	Max.	Min.		
5C	0.58	-0.24	0.40	-0.17	0.25	-0.11		
7 U	8.78	5.04	6.04	3.47	3.84	2.21		

6 Rated Voltage

Code	Rated Voltage
0J	DC6.3V
1A	DC10V
1C	DC16V
1E	DC25V
YA	DC35V
1H	DC50V
2A	DC100V
2E	DC250V
2J	DC630V

Capacitance

Expressed by three-digit alphanumerics. The unit is pico-farad $% \left(1\right) =\left(1\right) \left(1$ (pF). The first and second figures are significant digits, and the third figure expresses the number of zeros which follow the two numbers.

If there is a decimal point, it is expressed by the capital letter "R". In this case, all figures are significant digits.

Ex.)	Code	Capacitance
	R50	0.5pF
	1R0	1.0pF
	100	10pF
	103	10000pF

Continued on the following page.





Ontinued from the preceding page.

8 Capacitance Tolerance

Code	Capacitance Tolerance	TC	Series	Capacitance Step	
С	±0.25pF	COG	GCM	≦5pF	E12, 1pF Step *
D	±0.5pF	COG	GCM	6.0 to 9.0pF E12, 1pF Step	
	±5%	COG	GCM	≧10pF	E12 Step
J		U2J	GCM	E	6 Step
K	±10%	X7S, X7R	GCM	E6 Step	
М	±20%	X7S, X7R	GCM	E6 Step	

^{*} E24 series is also available.

Individual Specification Code

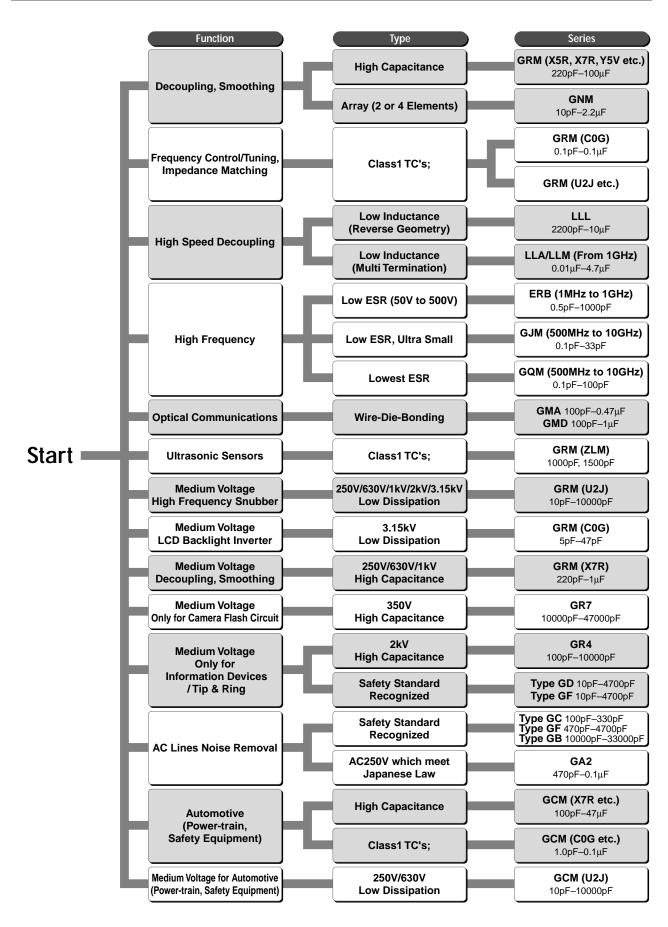
Expressed by three figures.

Packaging

Code	Packaging				
L	ø180mm Embossed Taping				
D	ø180mm Paper Taping				
K	ø330mm Embossed Taping				
J	ø330mm Paper Taping				
В	Bulk				
С	Bulk Case				



Selection Guide of Chip Monolithic Ceramic Capacitors



Chip Monolithic Ceramic Capacitors for Automotive



for Automotive GCM Series

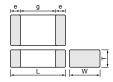
■ Features

- 1. The GCM series meet AEC-Q200 requirements.
- Higher resistance of solder-leaching due to the Ni-barriered termination, applicable for reflow-soldering, and flow-soldering (GCM18/21/31 type only).
- 3. The operating temperature range of R7/C7/5C series: -55 to 125 degree C.
- A wide selection of sizes is available, from miniature LxWxT:0.6x0.3x0.3mm to LxWxT: 3.2x2.5x2.5mm.
- 5. The GCM series is available in paper or embossed tape and reel packaging for automatic placement.
- 6. The GCM series is lead free product.

■ Applications

Automotive electronic equipment (Power-train, safety equipment)





			. ,	,				
Part Number	Dimensions (mm)							
rait Nullibel	Ĺ	W	T	е	g min.			
GCM033	0.6 ±0.03	0.3 ±0.03	0.3 ±0.03	0.1 to 0.2	0.2			
GCM155	1.0 ±0.05	0.5 ±0.05	0.5 ±0.05	0.15 to 0.35	0.3			
GCM188*	1.6 ±0.1	0.8 ±0.1	0.8 ±0.1	0.2 to 0.5	0.5			
GCM216			0.6 ±0.1					
GCM219	2.0 ±0.15	1.25 ±0.15	0.85 ±0.1	0.2 to 0.7	0.7			
GCM21B			1.25 ±0.15					
GCM319	2 2 40 15	1 4 10 15	0.85 ±0.1					
GCM31M	3.2 ±0.15	1.0 ±0.15	1.15 ±0.1	0.3 to 0.8	1.5			
GCM31C	3.2 ±0.2	1.6 ±0.2	1.6 ±0.2					
GCM32N			1.35 ±0.15					
GCM32R	3.2 +0.3	2.5 +0.2	1.8 ±0.2	0.3 min.	1.0			
GCM32D	3.2 ±0.3	2.5 ±0.2	2.0 ±0.2	0.3 11111.	1.0			
GCM32E			2.5 ±0.2					

^{*} Bulk Case: 1.6 ±0.07(L) × 0.8 ±0.07(W) × 0.8 ±0.07(T)

Capacitance Table

Temperature Compensating Type COC(5C)

Temperature Con					ype Part			
	_	1.0x0.5			2.0x			
LxW [mm]	(03)	(15)	(1	8)	(2	1)	(3	1)
		<0402>		03>	<08		<12	
Rated Voltage	25	50	100		100		100	
Capacitance [Vdc]			, ,		(2A)	(1H)	(ZA)	(1 H)
1.0pF(1R0)	3	5	8	8				
2.0pF(2R0) 3.0pF(3R0)	3	5	8	8				
4.0pF(4R0)	3	5	8	8				
5.0pF(5R0)	3	5	8	8				
6.0pF(6R0)	3	5	8	8				
7.0pF(7R0)	3	5	8	8				
8.0pF(8R0)	3	5	8	8				
9.0pF(9R0)	3	5	8	8				
10pF(100)	3	5	8	8				
12pF(120)	3	5	8	8				
15pF(150)	3	5	8	8				
18pF(180) 22pF(220)	3	5	8	8				
27pF(270)	3	5	8	8			! !	
33pF(330)	3	5	8	8			! !	
39pF(390)	3	5	8	8			 	
47pF(470)	3	5	8	8				
56pF(560)	3	5	8	8				
68pF(680)	3	5	8	8				
82pF(820)	3	5	8	8				
100pF(101)	3	5	8	8	6			
120pF(121)		5	8	8	6		 	
150pF(151)		5	8	8	6		 	
180pF(181) 220pF(221)		5	8	8	6		 	
270pF(271)		5	8	8	6			
330pF(331)		5	8	8	6			
390pF(391)		5	8	8	6			
470pF(471)		5	8	8	6			
560pF(561)			8	8	6	6		
680pF(681)			8	8	6	6		
820pF(821)			8	8	6	6		
1000pF(102)			8	8	6	6		
1200pF(122)			8	8	6	6		
1500pF(152) 1800pF(182)			8	8	6	6	9	
2200pF(222)				8	6	6	9	
2700pF(272)				8	6	6	9	
3300pF(332)				8	6	6	9	
3900pF(392)				8		6	9	
4700pF(472)						6	9	9
5600pF(562)						9	9	9
6800pF(682)						9	9	9
8200pF(822)						9	9	9
10000pF(103) 12000pF(123)						9	9	9
15000pF(123)						9		9
18000pF(183)						B		9
22000pF(223)						В		9
27000pF(273)					'			9
33000pF(333)								9
39000pF(393)								9
47000pF(473)								M
56000pF(563)								M
68000pF(683)								С
82000pF(823)								С
0.10μF(104)								С

The part numbering code is shown in () and Unit is shown in []. <>: EIA [inch] Code

muRata

Capacitance Table

High Dielectric Constant Type X7R(R7)/X7S(C7)

				•		`			`											6		e	x.6: T	Dim	ensio	n Par	t Num	nber (Code
LxW	0	.6x0	.3		1.0)	(0.5				x0.8 (8)				2.0x	1.25 1)					3.2	(1.6					3.2	(2.5		
[mm]	<	(03) 0201	>		(1 <04	02>			<06	03>				(2)	05>					<12	1) 06>					(3)	<u>10></u>		
Rated Voltage								100	50	25	16	100	50	35	25	16	10			25				100					
Capacitance [Vdc]	(1E)	(1C)	(1A)	(2A)	(1H)	(1E)	(1C)	(2A)	(1H)	(1E)	(1C)	(2A)	(1H)	(YA)	(1E)	(1C)	(1A)	(2A)	(1H)	(1E)	(1C)	(1A)	(0J)	(2A)	(1H)	(1E)	(1C)	(1 A)	(0J)
100pF(101)	3			į				i				į																	
150pF(151)	3							1 1 1				1												1					
220pF(221)	3			5	5			1				1						-						-					
330pF(331)	3			5	5			1				1						-						-					
470pF(471)	3			5	5							-						-						-					
680pF(681)	3			5	5			! !				<u> </u>												ļ					
1000pF(102)	3			5	5			8	8																				
1500pF(152)	3			5	5			8	8																				
2200pF(222)		3		5	5			8	8			1																	
3300pF(332)		3		5	5			8	8			1						-						-					
4700pF(472)			3	5	5			8	8			-						-						-					
6800pF(682)			3		5			8	8			6																	
10000pF(103)	T		3	Ī	5	5	Ī	8	8			6												ļ					
15000pF(153)					5	5		8	8			6																	
22000pF(223)				į	5	5		8	8	Ī		6																	
33000pF(333)				1 1 1		5	5		8	8		9	9					-						1					
47000pF(473)				1 1 1		5	5		8	8	1	В	В					-											
68000pF(683)				-			5		8	8	1	В	В																
0.10μF(104)	† ·						5	8	8	8	8	В	В					9	ļ										
0.15μF(154)				i					8	8			В		В			М											
0.22μF(224)				i !				i !	8	8	1		В		В			М											
0.33μF(334)				1 1				1 1 1		•	8		9		В				М					1					
0.47μF(474)				1 1 1				1 1 1		8	8	1	В		9			-	М	Ì				1					
0.68μF(684)								1			8	1		В	В	9			М										
1.0μF(105)	†			 !						8	8			В	В	9			M					† !	E				
2.2μF(225)	1														В	В	В	ĺ	С	М				D					
4.7μF(475)	1							i !				1		ı			В	1		С	С				Е	D			
10μF(106)	†											;						+ :			С	С]	+		Е	D		
22μF(226)	1			 				1 1 1				 						-					С				Е	E	
47μF(476)	1			!				1				1																	E
	_											1												1					

Temperature Compensating Type

L x W [mm]		0.6x0.3(03)<0201>	1.0x0.5(15)<0402>	1.6x0.8(1	8)<0603>
Rated Volt. [Vdc]	25(1E)	50(1H)	100(2A)	50(1H)
TC			COC	G(5C)	
Capacitance	Tolerance		Part N	lumber	
1.0pF(1R0)	±0.25pF(C)	GCM0335C1E1R0CD03D	GCM1555C1H1R0CZ13D	GCM1885C2A1R0CZ13D	GCM1885C1H1R0CZ13D
2.0pF(2R0)	±0.25pF(C)	GCM0335C1E2R0CD03D	GCM1555C1H2R0CZ13D	GCM1885C2A2R0CZ13D	GCM1885C1H2R0CZ13D
3.0pF(3R0)	±0.25pF(C)	GCM0335C1E3R0CD03D	GCM1555C1H3R0CZ13D	GCM1885C2A3R0CZ13D	GCM1885C1H3R0CZ13D
4.0pF(4R0)	±0.25pF(C)	GCM0335C1E4R0CD03D	GCM1555C1H4R0CZ13D	GCM1885C2A4R0CZ13D	GCM1885C1H4R0CZ13D
5.0pF(5R0)	±0.25pF(C)	GCM0335C1E5R0CD03D	GCM1555C1H5R0CZ13D	GCM1885C2A5R0CZ13D	GCM1885C1H5R0CZ13D
6.0pF(6R0)	±0.5pF(D)	GCM0335C1E6R0DD03D	GCM1555C1H6R0DZ13D	GCM1885C2A6R0DZ13D	GCM1885C1H6R0DZ13D
7.0pF(7R0)	±0.5pF(D)	GCM0335C1E7R0DD03D	GCM1555C1H7R0DZ13D	GCM1885C2A7R0DZ13D	GCM1885C1H7R0DZ13D
8.0pF(8R0)	±0.5pF(D)	GCM0335C1E8R0DD03D	GCM1555C1H8R0DZ13D	GCM1885C2A8R0DZ13D	GCM1885C1H8R0DZ13D
9.0pF(9R0)	±0.5pF(D)	GCM0335C1E9R0DD03D	GCM1555C1H9R0DZ13D	GCM1885C2A9R0DZ13D	GCM1885C1H9R0DZ13D
10pF(100)	±5%(J)	GCM0335C1E100JD03D	GCM1555C1H100JZ13D	GCM1885C2A100JA16D	GCM1885C1H100JA16D
12pF(120)	±5%(J)	GCM0335C1E120JD03D	GCM1555C1H120JZ13D	GCM1885C2A120JA16D	GCM1885C1H120JA16D
15pF(150)	±5%(J)	GCM0335C1E150JD03D	GCM1555C1H150JZ13D	GCM1885C2A150JA16D	GCM1885C1H150JA16D
18pF(180)	±5%(J)	GCM0335C1E180JD03D	GCM1555C1H180JZ13D	GCM1885C2A180JA16D	GCM1885C1H180JA16D
22pF(220)	±5%(J)	GCM0335C1E220JD03D	GCM1555C1H220JZ13D	GCM1885C2A220JA16D	GCM1885C1H220JA16D
27pF(270)	±5%(J)	GCM0335C1E270JD03D	GCM1555C1H270JZ13D	GCM1885C2A270JA16D	GCM1885C1H270JA16D
33pF(330)	±5%(J)	GCM0335C1E330JD03D	GCM1555C1H330JZ13D	GCM1885C2A330JA16D	GCM1885C1H330JA16D
39pF(390)	±5%(J)	GCM0335C1E390JD03D	GCM1555C1H390JZ13D	GCM1885C2A390JA16D	GCM1885C1H390JA16D
47pF(470)	±5%(J)	GCM0335C1E470JD03D	GCM1555C1H470JZ13D	GCM1885C2A470JA16D	GCM1885C1H470JA16D
56pF(560)	±5%(J)	GCM0335C1E560JD03D	GCM1555C1H560JZ13D	GCM1885C2A560JA16D	GCM1885C1H560JA16D
68pF(680)	±5%(J)	GCM0335C1E680JD03D	GCM1555C1H680JZ13D	GCM1885C2A680JA16D	GCM1885C1H680JA16D
82pF(820)	±5%(J)	GCM0335C1E820JD03D	GCM1555C1H820JZ13D	GCM1885C2A820JA16D	GCM1885C1H820JA16D
100pF(101)	±5%(J)	GCM0335C1E101JD03D	GCM1555C1H101JZ13D	GCM1885C2A101JA16D	GCM1885C1H101JA16D
120pF(121)	±5%(J)		GCM1555C1H121JA16D	GCM1885C2A121JA16D	GCM1885C1H121JA16D
150pF(151)	±5%(J)		GCM1555C1H151JA16D	GCM1885C2A151JA16D	GCM1885C1H151JA16D
180pF(181)	±5%(J)		GCM1555C1H181JA16D	GCM1885C2A181JA16D	GCM1885C1H181JA16D
220pF(221)	±5%(J)		GCM1555C1H221JA16D	GCM1885C2A221JA16D	GCM1885C1H221JA16D
270pF(271)	±5%(J)		GCM1555C1H271JA16D	GCM1885C2A271JA16D	GCM1885C1H271JA16D
330pF(331)	±5%(J)		GCM1555C1H331JA16D	GCM1885C2A331JA16D	GCM1885C1H331JA16D
390pF(391)	±5%(J)		GCM1555C1H391JA16D	GCM1885C2A391JA16D	GCM1885C1H391JA16D
470pF(471)	±5%(J)		GCM1555C1H471JA16D	GCM1885C2A471JA16D	GCM1885C1H471JA16D
560pF(561)	±5%(J)			GCM1885C2A561JA16D	GCM1885C1H561JA16D
680pF(681)	±5%(J)			GCM1885C2A681JA16D	GCM1885C1H681JA16D
820pF(821)	±5%(J)			GCM1885C2A821JA16D	GCM1885C1H821JA16D
1000pF(102)	±5%(J)			GCM1885C2A102JA16D	GCM1885C1H102JA16D
1200pF(122)	±5%(J)			GCM1885C2A122JA16D	GCM1885C1H122JA16D
1500pF(152)	±5%(J)			GCM1885C2A152JA16D	GCM1885C1H152JA16D
1800pF(182)	±5%(J)				GCM1885C1H182JA16D
2200pF(222)	±5%(J)				GCM1885C1H222JA16D
2700pF(272)	±5%(J)				GCM1885C1H272JA16D
3300pF(332)	±5%(J)				GCM1885C1H332JA16D
3900pF(392)	±5%(J)				GCM1885C1H392JA16D

The part numbering code is shown in () and Unit is shown in []. <>: EIA [inch] Code

1 Product ID2 Series3 Temperature Characteristics8 Capacitance Tolerance

③Dimension (L×W)⑥Rated Voltage⑨Individual Specification Code

Dimension (T)CapacitancePackaging

Packaging Code in Part Number is a code shows STD 180mm Reel Taping.

Temperature Compensating Type

L x W [mm]		2.0x1.25(2	2.0x1.25(21)<0805> 3.2x1.6(31)<1206>		
Rated Volt. [Vdc	:]	100(2A)	50(1H)	100(2A)	50(1H)
TC			COG	G(5C)	1
Capacitance	Tolerance		Part N	lumber	
100pF(101)	±5%(J)	GCM2165C2A101JA16D			
120pF(121)	±5%(J)	GCM2165C2A121JA16D			
150pF(151)	±5%(J)	GCM2165C2A151JA16D			
180pF(181)	±5%(J)	GCM2165C2A181JA16D			
220pF(221)	±5%(J)	GCM2165C2A221JA16D			
270pF(271)	±5%(J)	GCM2165C2A271JA16D			
330pF(331)	±5%(J)	GCM2165C2A331JA16D			
390pF(391)	±5%(J)	GCM2165C2A391JA16D			
470pF(471)	±5%(J)	GCM2165C2A471JA16D			
560pF(561)	±5%(J)	GCM2165C2A561JA16D	GCM2165C1H561JA16D		
680pF(681)	±5%(J)	GCM2165C2A681JA16D	GCM2165C1H681JA16D		
820pF(821)	±5%(J)	GCM2165C2A821JA16D	GCM2165C1H821JA16D		
1000pF(102)	±5%(J)	GCM2165C2A102JA16D	GCM2165C1H102JA16D		
1200pF(122)	±5%(J)	GCM2165C2A122JA16D	GCM2165C1H122JA16D		
1500pF(152)	±5%(J)	GCM2165C2A152JA16D	GCM2165C1H152JA16D		
1800pF(182)	±5%(J)	GCM2165C2A182JA16D	GCM2165C1H182JA16D	GCM3195C2A182JA16D	
2200pF(222)	±5%(J)	GCM2165C2A222JA16D	GCM2165C1H222JA16D	GCM3195C2A222JA16D	
2700pF(272)	±5%(J)	GCM2165C2A272JA16D	GCM2165C1H272JA16D	GCM3195C2A272JA16D	
3300pF(332)	±5%(J)	GCM2165C2A332JA16D	GCM2165C1H332JA16D	GCM3195C2A332JA16D	
3900pF(392)	±5%(J)		GCM2165C1H392JA16D	GCM3195C2A392JA16D	
4700pF(472)	±5%(J)		GCM2165C1H472JA16D	GCM3195C2A472JA16D	GCM3195C1H472JA16D
5600pF(562)	±5%(J)		GCM2195C1H562JA16D	GCM3195C2A562JA16D	GCM3195C1H562JA16D
6800pF(682)	±5%(J)		GCM2195C1H682JA16D	GCM3195C2A682JA16D	GCM3195C1H682JA16D
8200pF(822)	±5%(J)		GCM2195C1H822JA16D	GCM3195C2A822JA16D	GCM3195C1H822JA16D
10000pF(103)	±5%(J)		GCM2195C1H103JA16D	GCM3195C2A103JA16D	GCM3195C1H103JA16D
12000pF(123)	±5%(J)		GCM2195C1H123JA16D		GCM3195C1H123JA16D
15000pF(153)	±5%(J)		GCM2195C1H153JA16D		GCM3195C1H153JA16D
18000pF(183)	±5%(J)		GCM21B5C1H183JA16L		GCM3195C1H183JA16D
22000pF(223)	±5%(J)		GCM21B5C1H223JA16L		GCM3195C1H223JA16D
27000pF(273)	±5%(J)				GCM3195C1H273JA16D
33000pF(333)	±5%(J)				GCM3195C1H333JA16D
39000pF(393)	±5%(J)				GCM3195C1H393JA16D
47000pF(473)	±5%(J)				GCM31M5C1H473JA16L
56000pF(563)	±5%(J)				GCM31M5C1H563JA16L
68000pF(683)	±5%(J)				GCM31C5C1H683JA16L
82000pF(823)	±5%(J)				GCM31C5C1H823JA16L
0.10μF(104)	±5%(J)				GCM31C5C1H104JA16L

High Dielectric Constant Type

L x W [mm]			0.6x0.3(03)<0201>					
Rated Volt. [Vdc]	25(1E)	16(1C)	10(1A)				
TC			X7R(R7)					
Capacitance	Tolerance		Part Number					
100pF(101)	±10%(K)	GCM033R71E101KA03D						
150pF(151)	±10%(K)	GCM033R71E151KA03D						
220pF(221)	±10%(K)	GCM033R71E221KA03D						
330pF(331)	±10%(K)	GCM033R71E331KA03D						
470pF(471)	±10%(K)	GCM033R71E471KA03D						
680pF(681)	±10%(K)	GCM033R71E681KA03D						
1000pF(102)	±10%(K)	GCM033R71E102KA03D						
1500pF(152)	±10%(K)	GCM033R71E152KA03D						
2200pF(222)	±10%(K)		GCM033R71C222KA55D					
3300pF(332)	±10%(K)		GCM033R71C332KA55D					
4700pF(472)	±10%(K)			GCM033R71A472KA03D				
6800pF(682)	±10%(K)			GCM033R71A682KA03D				
10000pF(103)	±10%(K)			GCM033R71A103KA03D				

L x W [mm]			1.0x0.5(1	5)<0402>				
Rated Volt. [Vdc]	100(2A)	50(1H)	25(1E)	16(1C)			
TC			X7R	2(R7)				
Capacitance	Tolerance		Part Number					
220pF(221)	±10%(K)	GCM155R72A221KA37D	GCM155R71H221KA37D					
330pF(331)	±10%(K)	GCM155R72A331KA37D	GCM155R71H331KA37D					
470pF(471)	±10%(K)	GCM155R72A471KA37D	GCM155R71H471KA37D					
680pF(681)	±10%(K)	GCM155R72A681KA37D	GCM155R71H681KA37D					
1000pF(102)	±10%(K)	GCM155R72A102KA37D	GCM155R71H102KA37D					
1500pF(152)	±10%(K)	GCM155R72A152KA37D	GCM155R71H152KA37D					
2200pF(222)	±10%(K)	GCM155R72A222KA37D	GCM155R71H222KA37D					
3300pF(332)	±10%(K)	GCM155R72A332KA37D	GCM155R71H332KA37D					
4700pF(472)	±10%(K)	GCM155R72A472KA37D	GCM155R71H472KA37D					
6800pF(682)	±10%(K)		GCM155R71H682KA55D					
10000pF(103)	±10%(K)		GCM155R71H103KA55D	GCM155R71E103KA37D				
15000pF(153)	±10%(K)		GCM155R71H153KA55D	GCM155R71E153KA55D				
22000pF(223)	±10%(K)		GCM155R71H223KA55D	GCM155R71E223KA55D				
33000pF(333)	±10%(K)			GCM155R71E333KA55D	GCM155R71C333KA37D			
47000pF(473)	±10%(K)			GCM155R71E473KA55D	GCM155R71C473KA37D			
68000pF(683)	±10%(K)				GCM155R71C683KA55D			
0.10μF(104)	±10%(K)				GCM155R71C104KA55D			

The part numbering code is shown in () and Unit is shown in []. <>: EIA [inch] Code

Product IDSeriesTemperature CharacteristicsCapacitance Tolerance

③Dimension (LXW)⑥Rated Voltage⑨Individual Specification Code

4 Dimension (T)7 Capacitance10 Packaging

High Dielectric Constant Type

L x W [mm]			1.6x0.8(1	8)<0603>	
Rated Volt. [Vdc]	100(2A)	50(1H)	25(1E)	16(1C)
TC			X7R(R7)	/X7S(C7)	
Capacitance	Tolerance		Part N	lumber	
1000pF(102)	±10%(K)	GCM188R72A102KA37D	GCM188R71H102KA37D		
1500pF(152)	±10%(K)	GCM188R72A152KA37D	GCM188R71H152KA37D		
2200pF(222)	±10%(K)	GCM188R72A222KA37D	GCM188R71H222KA37D		
3300pF(332)	±10%(K)	GCM188R72A332KA37D	GCM188R71H332KA37D		
4700pF(472)	±10%(K)	GCM188R72A472KA37D	GCM188R71H472KA37D		
6800pF(682)	±10%(K)	GCM188R72A682KA37D	GCM188R71H682KA37D		
10000pF(103)	±10%(K)	GCM188R72A103KA37D	GCM188R71H103KA37D		
15000pF(153)	±10%(K)	GCM188R72A153KA37D	GCM188R71H153KA37D		
22000pF(223)	±10%(K)	GCM188R72A223KA37D	GCM188R71H223KA37D		
33000pF(333)	±10%(K)		GCM188R71H333KA55D	GCM188R71E333KA37D	
47000pF(473)	±10%(K)		GCM188R71H473KA55D	GCM188R71E473KA37D	
68000pF(683)	±10%(K)		GCM188R71H683KA57D	GCM188R71E683KA57D	
0.10μF(104)	±10%(K)	GCM188R72A104KA64D	GCM188R71H104KA57D	GCM188R71E104KA57D	GCM188R71C104KA37D
0.15μF(154)	±10%(K)		GCM188R71H154KA64D	GCM188R71E154KA37D	
0.22μF(224)	±10%(K)		GCM188R71H224KA64D	GCM188R71E224KA55D	
0.33μF(334)	±10%(K)				GCM188R71C334KA37D
0.47μF(474)	±10%(K)			GCM188R71E474KA64D	GCM188R71C474KA55D
0.68μF(684)	±10%(K)				GCM188C71C684KA64D
1.0μF(105)	±10%(K)			GCM188R71E105KA64D	GCM188R71C105KA64D
L v M [mana]		I	2.0v1.25/	04) .000F.	
L x W [mm]		100(2A)	50(1H)	21)<0805> 35(YA)	25(4 E)
Rated Volt. [Vdc	J	100(2A)	, ,	(R7)	25(1E)
Capacitance	Tolerance			lumber	
6800pF(682)	±10%(K)	GCM216R72A682KA37D	raitiv		
10000pF(103)	±10%(K)	GCM216R72A103KA37D			
	· · ·	GCM216R72A153KA37D			
15000pF(153)	±10%(K)	GCM216R72A153KA37D			

TC			X7R(R7)				
Capacitance	Tolerance		Part Number				
6800pF(682)	±10%(K)	GCM216R72A682KA37D					
10000pF(103)	±10%(K)	GCM216R72A103KA37D					
15000pF(153)	±10%(K)	GCM216R72A153KA37D					
22000pF(223)	±10%(K)	GCM216R72A223KA37D					
33000pF(333)	±10%(K)	GCM219R72A333KA37D	GCM219R71H333KA37D				
47000pF(473)	±10%(K)	GCM21BR72A473KA37L	GCM21BR71H473KA37L				
68000pF(683)	±10%(K)	GCM21BR72A683KA37L	GCM21BR71H683KA37L				
0.10μF(104)	±10%(K)	GCM21BR72A104KA37L	GCM21BR71H104KA37L				
0.15μF(154)	±10%(K)		GCM21BR71H154KA37L		GCM21BR71E154KA37L		
0.22μF(224)	±10%(K)		GCM21BR71H224KA37L		GCM21BR71E224KA37L		
0.33μF(334)	±10%(K)		GCM219R71H334KA55D		GCM21BR71E334KA37L		
0.47μF(474)	±10%(K)		GCM21BR71H474KA55L		GCM219R71E474KA55D		
0.68μF(684)	±10%(K)			GCM21BR7YA684KA55L	GCM21BR71E684KA55L		
1.0μF(105)	±10%(K)			GCM21BR7YA105KA55L	GCM21BR71E105KA56L		
2.2μF(225)	±10%(K)				GCM21BR71E225KA73L		

L x W [mm]		2.0x1.25(21)<0805>		
Rated Volt. [Vdc]	16(1C)	10(1A)	
TC		X7R(R7)/X7S(C7)		
Capacitance	Tolerance	Part Number		
0.68μF(684)	±10%(K)	GCM219R71C684KA37D		
1.0μF(105)	±10%(K)	GCM219R71C105KA37D		
2.2μF(225)	±10%(K)	GCM21BR71C225KA64L	GCM21BR71A225KA37L	
4.7μF(475)	±10%(K)		GCM21BC71A475KA73L	

High Dielectric Constant Type

L x W [mm]			3.2x1.6(31)<1206>				
Rated Volt. [Vdc	:]	100(2A)	50(1H)	25(1E)	16(1C)		
TC		X7R(R7)					
Capacitance	Tolerance		Part Number				
0.10μF(104)	±10%(K)	GCM319R72A104KA37D					
0.15μF(154)	±10%(K)	GCM31MR72A154KA37L					
0.22μF(224)	±10%(K)	GCM31MR72A224KA37L					
0.33μF(334)	±10%(K)		GCM31MR71H334KA37L				
0.47μF(474)	±10%(K)		GCM31MR71H474KA37L				
0.68μF(684)	±10%(K)		GCM31MR71H684KA55L				
1.0μF(105)	±10%(K)		GCM31MR71H105KA55L				
2.2μF(225)	±10%(K)		GCM31CR71H225KA55L	GCM31MR71E225KA57L			
4.7μF(475)	±10%(K)			GCM31CR71E475KA55L	GCM31CR71C475KA37L		
10μF(106)	±10%(K)				GCM31CR71C106KA64L		

L x W [mm]		3.2x1.6(31)<1206>		
Rated Volt. [Vdc]	10(1A)	6.3 (0J)	
TC		X7R(R7)		
Capacitance	Tolerance	Part N	umber	
10μF(106)	±10%(K)	GCM31CR71A106KA64L		
22μF(226)	±20%(M)		GCM31CR70J226ME23L	

L x W [mm]			3.2x2.5(32)<1210>			
Rated Volt. [Vdc	:]	100(2A)	50(1H)	25(1E)	16(1C)	
TC		X7R(R7)				
Capacitance	Tolerance	Part Number				
1.0μF(105)	±10%(K)		GCM32ER71H105KA37L			
2.2μF(225)	±10%(K)	GCM32DR72A225KA64L				
4.7μF(475)	±10%(K)		GCM32ER71H475KA55L	GCM32DR71E475KA55L		
10μF(106)	±10%(K)			GCM32ER71E106KA57L	GCM32DR71C106KA37L	
22μF(226)	±20%(M)				GCM32ER71C226ME19L	

L x W [mm]		3.2x2.5 (32)<1210>			
Rated Volt. [Vdc]	10(1A)	6.3(0J)		
TC		X7R(R7)			
Capacitance	Tolerance	Part N	umber		
22μF(226)	±20%(M)	GCM32ER71A226ME12L			
47μF(476)	±20%(M)		GCM32ER70J476ME19L		

Product IDSeriesTemperature CharacteristicsCapacitance Tolerance

③Dimension (LXW)⑥Rated Voltage④Individual Specification Code

4 Dimension (T)7 Capacitance10 Packaging

Packaging Code in Part Number is a code shows STD 180mm Reel Taping.

	Test Item		Specifi	cations		
No.			Temperature Compensating Type	High Dielectric Type	AEC-Q200 Test Method	
1	Pre-and Pe				_	
	High Tem Exposure		The measured and observed ch specifications in the following ta	•		
		Appearance	No marking defects			
2		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Within ±10.0%	Sit the capacitor for 1000±12 hours at 150±3°C. Let sit for 24±2	
2		Q/D.F.	30pFmin.: Q≥1000 30pFmax.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.03 max. W.V.: 16V: 0.05 max.	hours at room temperature, then measure.	
		I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	F *1		
	Temperat Cycle	ture	The measured and observed ch specifications in the following ta		Fix the capacitor to the supporting jig in the same manner and under the same conditions as (18). Perform the 1000 cycles	
		Appearance	No marking defects		according to the four heat treatments listed in the following table. Let sit for 24±2 hours at room temperature, then measure	
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Within ±10.0%	Step 1 2 3 4	
3		Q/D.F.	30pFmin.: Q≧1000 30pFmax.: Q≧400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.03 max. W.V.: 16V: 0.05 max.	Temp. (C) -55+0/-3 Temp. Tem	
		I.R.	More than 10,000M Ω or 500 Ω · F (Whichever is smaller)		• Initial measurement for high dielectric constant type Perform a heat treatment at 150 + o o for one hour and then let sit for 24±2 hours at room temperature. Perform the initial measurement.	
4	Destructive Physical Analysis		No defects or abnormalities		Per EIA-469	
	Moisture Resistant	ce	The measured and observed ch specifications in the following ta	•	Apply the 24-hour heat (25 to 65°C) and humidity (80 to 98%) treatment shown below, 10 consecutive times.	
		Appearance	No marking defects		Let sit for 24±2 hours at room temperature, then measure.	
		Capacitance Change	Within ±3.0% or ±0.30pF (Whichever is larger)	Within ±12.5%	Humidity Humidity Humidity Humidity °C 90-98% 80-98% 90-98% 80-98% 90-98% 70	
5		Q/D.F.	30pFmin.: Q≥350 10pF and over, 30pF and below: Q≥275+ ½ C 10pFmax.: Q≥200+10C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.03 max. W.V.: 16V: 0.05 max.	65 60 65 50 45 84 0 18 35 19 30 410 25 80 25	
		I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	*1 F	One cycle 24 hours 0 1 2 3 4 5 6 7 8 9 101112131415161718192021222324 Hours	
	Biased H	umidity	The measured and observed ch specifications in the following ta	•		
		Appearance	No marking defects			
6		Capacitance Change	Within ±3.0% or ±0.30pF (Whichever is larger)	Within ±12.5%	Apply the rated voltage and 1.3+0.2/-0Vdc (add 6.8k Ω resistor) at 85±3°C and 80 to 85% humidity for 1000±12 hours. Remove and let sit for 24±2 hours at room temperature, then	
J		Q/D.F.	30pF and over: Q≥200 30pF and below: Q≥100+ ¹⁰ / ₃ C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.035 max. W.V.: 16V: 0.05 max.	measure. The charge/discharge current is less than 50mA.	
		I.R.	More than 1,000M Ω or $50\Omega \cdot F$ (Whichever is smaller)	*1		

Continued on the following page.





_	Continued fr	.,				
lo.	AEC-	Q200	Specifi	cations	AEC-Q200 Test Method	
•0.	Test	Item	Temperature Compensating Type	High Dielectric Type	ALO QZOO FOST MOUROU	
	Operation	nal Life	The measured and observed ch specifications in the following ta	,		
		Appearance	No marking defects		Apply 200% of the rated voltage for 1000±12 hours at	
		Capacitance Change	Within ±3.0% or ±0.30pF (Whichever is larger)	Within ±12.5%	125±3°C. Let sit for 24±2 hours at room temperature, then measure. *2 The charge/discharge current is less than 50mA.	
7		Q/D.F.	30pFmin.: Q≥350 10pF and over, 30pF and below: Q≥275+ ½ C 10pFmax.: Q≥200+10C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.035 max. W.V.: 16V: 0.05 max.	Initial measurement for high dielectric constant type. Apply 200% of the rated DC voltage for one hour at the maximu operating temperature ±3°C. Remove and let sit for 24±2 hours at room temperature. Perform initial measurement. *2	
		I.R.	More than 1,000M Ω or $50\Omega \cdot F$ *1 (Whichever is smaller)			
8	External	Visual	No defects or abnormalities		Visual inspection	
9	Physical [Dimension	Within the specified dimensions		Using calipers	
		Appearance	No marking defects		Per MIL-STD-202 Method 215	
		Capacitance Change	Within the specified tolerance		Solvent 1: 1 part (by volume) of isopropyl alcohol 3 parts (by volume) of mineral spirits	
10	Resistance to Solvents	Q/D.F.	30pFmin.: Q≧1000 30pFmax.: Q≧400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	Solvent 3: 42 parts (by volume) of water 1 part (by volume) of propylene glycol	
		I.R.	More than 10,000M Ω or 500 Ω · F *1 (Whichever is smaller)		monomethyl ether 1 part (by volume) of monoethanolamine	
		Appearance	No marking defects			
		Capacitance Change Within the specified tolerance			Three shocks in each direction should be applied along 3	
11	Mechanical Shock	Q/D.F.	30pFmin.: Q≥1000 30pFmax.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	mutually perpendicular axes of the test specimen (18 shocks). The specified test pulse should be Half-sine and should have a duration: 0.5ms, peak value: 1500g and velocity change: 4.7m/s	
		I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	F *1		
		Appearance	No defects or abnormalities		Solder the capacitor to the test jig (glass epoxy board) in the	
		Capacitance Change	Within the specified tolerance		same manner and under the same conditions as (19). The capacitor should be subjected to a simple harmonic motion beginning to the large state of the same conditions.	
12	Vibration	Q/D.F.	30pFmin.: Q≧1000 30pFmax.: Q≧400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 2000Hz. Th frequency range, from 10 to 2000Hz and return to 10Hz, should be traversed in approximately 20 minutes. This motion should be	
		I.R.	More than 10,000M Ω or 500 Ω · F *1 (Whichever is smaller)		applied for 12 items in each 3 mutually perpendicular directions (total of 36 times).	
	Resistand Soldering		The measured and observed ch specifications in the following ta	-		
		Appearance	No marking defects		Immerse the capacitor in a eutectic solder solution at 260±5°C f 10±1 seconds. Let sit at room temperature for 24±2 hours, then	
13		Capacitance Change	Within the specified tolerance		measure.	
, 3		Q/D.F.	30pFmin.: Q≧1000 30pFmax.: Q≥400+20C	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max	 Initial measurement for high dielectric constant type Perform a heat treatment at 150⁺⁰/₁₀ °C for one hour and then least for 24±2 hours at room temperature. 	

Continued on the following page.

Perform the initial measurement.





W.V.: 16V: 0.035 max.

C: Nominal Capacitance (pF)

(Whichever is smaller)

More than 10,000M Ω or $500\Omega \cdot F$

I.R.

Continued from the preceding page.

le.	AEC-	-Q200	Specifi	cations	AEC-Q200 Test Method		
No.	Test	Item	Temperature Compensating Type	High Dielectric Type	ALC-0200 rest Method		
	Thermal	Shock	The measured and observed ch specifications in the following ta		Fix the capacitor to the supporting jig in the same manner and under the same conditions as (18). Perform the 300 cycles		
		Appearance	No marking defects		according to the two heat treatments listed in the following table (Maximum transfer time is 20 seconds). Let sit for 24±2 hours a		
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Within ±10.0%	room temperature, then measure.		
14		Q/D.F.	30pF min.: Q≥1000 30pF max.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	Temp. (°C) -55+0/-3 125+3/-0 (5C, C7, R7) Time (min.) 15±3 15±3 Initial measurement for high dielectric constant type		
		I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	*1	Perform a heat treatment at 150±9 °C for one hour and ther let sit for 24±2 hours at room temperature. Perform the initial measurement.		
		Appearance	No marking defects				
		Capacitance Change	Within the specified tolerance				
15	ESD	Q/D.F.	30pF min.: Q≧1000 30pF max.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	Per AEC-Q200-004		
		I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	F *1			
16	5 Solderability		95% of the terminations is to be continuously.	soldered evenly and	 (a) Preheat at 155°C for 4 hours. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5°C. (b) Should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in 		
			editindeday.		weight proportion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5°C. (c) Should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in eutectic solder solution for 120±5 seconds at 260±5°C.		
		Appearance	No defects or abnormalities		Visual inspection.		
		Capacitance Change	Within the specified tolerance		The capacitance/Q/D.F. should be measured at 25°C at the frequency and voltage shown in Tourist Tourist (1) Tourist To		
17	Electrical Characteri-	Q/D.F.	30pF min.: Q≥1000 30pF max.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25V min.: 0.025 max. W.V.: 16V: 0.035 max	(1) Temperature Compensating Type		
	zation	I.R.	25°C More than 100,000MΩ or 1,000Ω · F (Whichever is smaller) Max. Operating Temperature···125°C More than 10,000MΩ or 100Ω · F (Whichever is smaller)	25°C *1 25°C More than 10,000M Ω or 500 Ω · F (Whichever is smaller) Max. Operating Temperature125°C More than 1,000M Ω or 10 Ω · F (Whichever is smaller)	The insulation resistance should be measured with a DC voltag not exceeding the rated voltage at 25°C and 125°C and within 2 minutes of charging.		
		Dielectric Strength	No failure		No failure should be observed when 250% of the rated voltage applied between the terminations for 1 to 5 seconds, provided to charge/discharge current is less than 50mA.		

Continued on the following page.





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\overline{A}	Continued ii	om the prec	eding page.					
No.		Q200 Item	·	cations	AEC-Q200 Test Method			
	1031		Temperature Compensating Type	High Dielectric Type	Solder the connector on the test iig (along appay hours) shown in			
		Appearance Capacitance Change	No marking defects Within ±5.0% or ±0.5pF (Whichever is larger) 30pF min.: Q≥1000	Within ±10.0%	Solder the capacitor on the test jig (glass epoxy board) shown in Fig. 1 using a eutectic solder. Then apply a force in the direction shown in Fig. 2 for 5±1sec. The soldering should be done by the reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock.			
		Q/D.F.	30pF max.: Q≧400+20C C: Nominal Capacitance (pF)	W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	Type a b c GCM03 0.3 0.9 0.3			
18	Board Flex	I.R.	*1 More than 10,000MΩ or 500Ω · F (Whichever is smaller)	t: 1.6mm (GCM03/15: 0.8mm)	GCM15 0.5 1.5 0.6 GCM18 0.6 2.2 0.9 GCM21 0.8 3.0 1.3 GCM31 2.0 4.4 1.7 GCM32 2.0 4.4 2.6 (in mm) Capacitance meter 45 45 Flexure: ≤2 (High Dielectric Type) Flexure: ≤3 (High Dielectric Ty			
		Appearance	No marking defects		Solder the capacitor to the test jig (glass epoxy board) shown in			
		Capacitance Change	Within the specified tolerance		Fig. 3 using a eutectic solder. Then apply *18N force in parallel with the test jig for 60sec. The soldering should be done either with an iron or using the			
		Q/D.F.	30pF min.: Q≥1000 30pF max.: Q≥400+20C C: Nominal Capacitance (pF)	*1 W.V.: 25Vmin.: 0.025 max. W.V.: 16V: 0.035 max.	reflow method and should be conducted with care so that the soldering is uniform and free of defects such as heat shock. *2N (GCM03/15)			
19	Terminal Strength	I.R.	More than 10,000M Ω or 500 Ω · (Whichever is smaller)	*1 F	Type a b c GCM03 0.3 0.9 0.3 GCM15 0.4 1.5 0.5 GCM18 1.0 3.0 1.2 GCM21 1.2 4.0 1.65 GCM31 2.2 5.0 2.0 GCM32 2.2 5.0 2.9 (in mm) Fig. 3			
20	20 Beam Load Test		The chip endure following force. < Chip L dimension: 2.5mm max. > Chip thickness > 0.5mm rank: 20N Chip thickness ≤ 0.5mm rank: 8N < Chip L dimension: 3.2mm min. > Chip thickness < 1.25mm rank: 15N Chip thickness ≥ 1.25mm rank: 54.5N		Place the capacitor in the beam load fixture as Fig. 4. Apply a force. < Chip Length: 2.5mm max. > Iron Board Speed supplied the Stress Load: 0.5mm / sec. < Chip Length: 3.2mm min. > Speed supplied the Stress Load: 2.5mm / sec. Fig. 4			

\subseteq	Continued fr	om the prec	eding page.		
	AEC-	·Q200	Specifi	cations	450 0000 5 144 11 1
No	Test	Item	Temperature Compensating Type	High Dielectric Type	AEC-Q200 Test Method
		Capacitance Change	Within the specified tolerance (Table A)	C7: Within ±22% (-55°C to +125°C) R7: Within ±15% (-55°C to +125°C)	The capacitance change should be measured after 5 min. at each specified temperature stage. (1) Temperature Compensating Type The temperature coefficient is determined using the capacitance
		Temperature Coefficient	Within the specified tolerance (Table A)		measured in step 3 as a reference. When cycling the temperature sequentially from step1 through 5 (ΔC: +25°C to +125°C: other temp. coeffs.: +25°C to +85°C) the capacitance
2*	Capacitance Temperature Character- istics	Capacitance Drift	Within ±0.2% or ±0.05 pF (Whichever is larger) * Do not apply to 1X/25V		should be within the specified tolerance for the temperature coefficient and capacitance change as shown in Table A. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in steps 1, 3 and 5 by the capacitance value in step 3. Step

^{*1:} The figure indicates typical inspection. Please refer to individual specifications.

Table A

		Capacitance Change from 25°C (%)					
Char.	Nominal Values (ppm/°C) Note1	-55		-30		-10	
		Max.	Min.	Max.	Min.	Max.	Min.
5C	0±30	0.58	-0.24	0.40	-0.17	0.25	-0.11

Note 1: Nominal values denote the temperature coefficient within a range of 25°C to 125°C (for 5C).

^{*2:} Some of the parts are applicable in rated voltage x 150%. Please refer to individual specifications.

Package

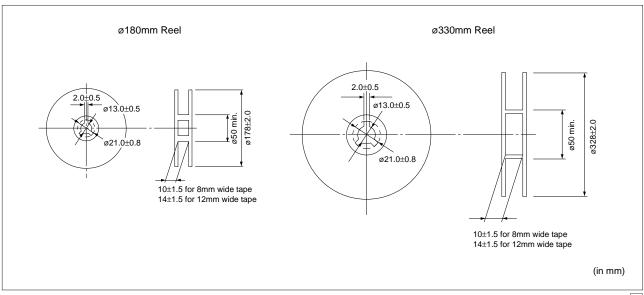
■ Minimum Quantity Guide

	Dimensions (mm)			Quantity (pcs.)					
Part Number	Dimensions (mm)		ø180mm reel		ø330mm reel			Dulle Dan	
Fait Number	L	w	Т	Paper Tape Packaging Code: D	Embossed Tape Packaging Code: L	Paper Tape Packaging Code: J	Embossed Tape Packaging Code: K	Bulk Case Packaging Code: C	Bulk Bag Packaging Code: B
GCM03	0.6	0.3	0.3	15,000	-	50,000	-	-	1,000
GCM15	1.0	0.5	0.5	10,000	-	50,000	-	50,000	1,000
GCM18	1.6	0.8	0.8	4,000	-	10,000	-	15,000 ¹⁾	1,000
			0.6	4,000	-	10,000	-	10,000	1,000
GCM21	2.0	1.25	0.85	4,000	-	10,000	-	-	1,000
			1.25	-	3,000	-	10,000	5,000 ¹⁾	1,000
			0.85	4,000	-	10,000	-	-	1,000
GCM31	3.2	1.6	1.15	-	3,000	-	10,000	-	1,000
			1.6	-	2,000	-	6,000	-	1,000
			1.15	-	3,000	-	10,000	-	1,000
GCM32	2.0	2.5	1.35	-	2,000	-	8,000	-	1,000
GCIVI32	3.2		1.6	-	2,000	-	6,000	-	1,000
			1.8/2.0/2.5	-	1,000	-	4,000	-	1,000

¹⁾ There are parts number without bulk case.

■ Tape Carrier Packaging

1. Dimensions of Reel



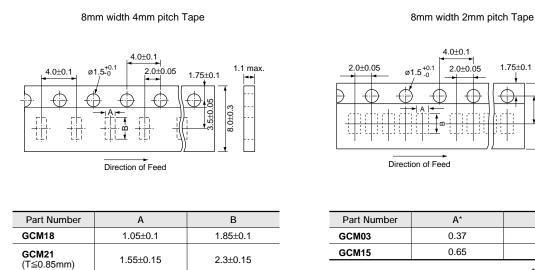
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Package

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2. Dimensions of Paper Tape



3.6±0.2

3.6±0.2

2.0±0.05 Ø1.5 ±0.1 2.0±0.05 1.75±0.1 0.8 max.		
Ø1.5.0	4.0±0.1	
33,540,00	2.0±0.05 ø1.5 ^{+0.1} 2.0±0.05 1.75±0.1 0.8 max.	
3.5+0.0		
<u> </u>		
	± € 69	

Part Number	A*	B*
GCM03	0.37	0.67
GCM15	0.65	1.15

*Nominal Value

(in mm)

3. Dimensions of Embossed Tape

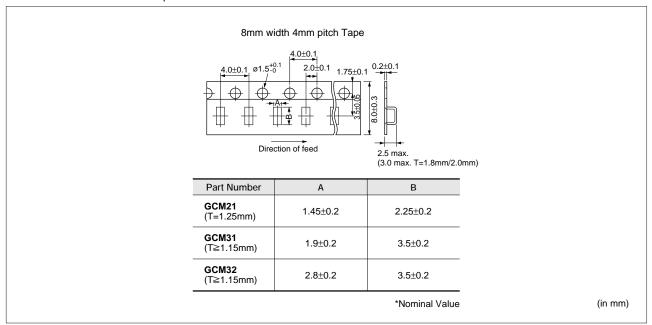
2.0±0.2

 2.8 ± 0.2

GCM31

(T≦0.85mm) GCM32

(T=0.85mm)



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Package

Continued from the preceding page.

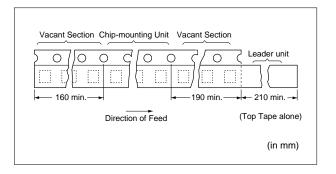
4. Taping Method

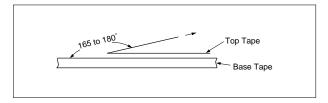
- (1) Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.
- (2) Part of the leader and part of the empty tape should be attached to the end of the tape as follows.
- (3) The top tape and base tape are not attached at the end of the tape for a minimum of 5 pitches.
- (4) Missing capacitors number within 0.1% of the number per reel or 1 pc, whichever is greater, and are not
- (5) The top tape and bottom tape should not protrude beyond the edges of the tape and should not cover sprocket holes.
- (6) Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm.
- (7) Peeling off force: 0.1 to 0.6N* in the direction shown below.

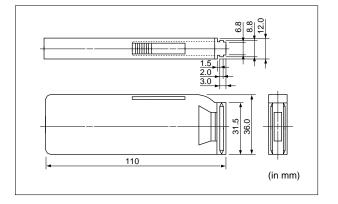
*GCM03: 0.05 to 0.5N

■ Dimensions of Bulk Case Packaging

The bulk case uses antistatic materials. Please contact Murata for details.







■ **(**Caution (Storage and Operating Condition)

Chip monolithic ceramic capacitors (chips) can experience degradation of termination solderability when subjected to high temperature or humidity, or if exposed to sulfur or chlorine gases.

Storage environment must be at an ambient temperature of 5-40 degree C and an ambient humidity of 20-70%RH. Use chip within 6 months. If 6 months or more have elapsed, check solderability before use.

Insulation Resistance should be deteriorated on specific condition of high humidity or incorrosion gas such as hydrogen sulfide, sulfurous acid gas, chlorine. Those condition are not suitable for use.

■ ①Caution (Handling)

1. Inspection

Thrusting force of the test probe can flex the PCB, resulting in cracked chips or open solder joints. Provide support pins on the back side of the PCB to prevent warping or flexing.

- 2. Board Separation (or depanalization)
- (1) Board flexing at the time of separation causes cracked chips or broken solder.
- (2) Severity of stresses imposed on the chip at the time of board break is in the order of: Pushback<Slitter<V Slot<Perforator.
- (3) Board separation must be performed using special jigs, not with hands.

Use of Sn-Zn based solder will deteriorate reliability of MLCC.

Please contact murata factory for the use of Sn-Zn based solder in advance.

Do not use under the condition that causes condensation. Use dampproof countermeasure if using under the condition that causes condensation.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT. WORST CASE. IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

3. Reel and bulk case

In the handling of reel and case, please be careful and do not drop it.

Do not use chips from a case which has been dropped.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT. WORST CASE. IN A SHORT CIRCUIT AND FUMING WHEN THE PRODUCTS IS USED.



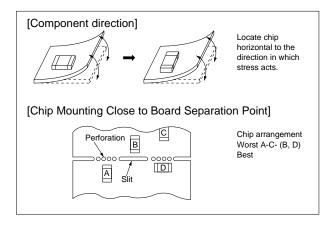
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⚠Caution

■ ①Caution (Soldering and Mounting)

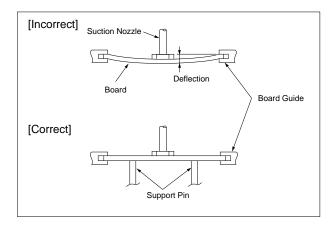
1. Mounting Position

Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.



2. Chip Placing

- An excessively low bottom dead point of the suction nozzle imposes great force on the chip during mounting, causing cracked chips. So adjust the suction nozzle's bottom dead point by correcting warp in the board. Normally, the suction bottom dead point must be set on the upper surface of the board. Nozzle pressure for chip mounting must be a 1 to 3N static load.
- Dirt particles and dust accumulated between the suction nozzle and the cylinder inner wall prevent the nozzle from moving smoothly. This imposes great force on the chip during mounting, causing cracked chips. And the locating claw, when worn out, imposes uneven forces on the chip when positioning, causing cracked chips. The suction nozzle and the locating claw must be maintained, checked and replaced periodically.



Continued on the following page.



- Continued from the preceding page.
- 3. Reflow Soldering
- When the sudden heat is given to the components, the mechanical strength of the components should go down because remarkable temperature change causes deformity of components inside. In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in table 1. It is required to keep temperature differential between the soldering and the components surface (ΔT) as small as possible.
- Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used.
 - Please confirm the solderability of Tin plating termination chip before use.
- When components are immersed in solvent after mounting, be sure to maintain the temperature difference (ΔT) between the component and solvent within the range shown in the table 1.

Table 1

Part Number	Temperature Differential		
GCM03/15/18/21/31	ΔT≦190°C		
GCM32	ΔT≦130°C		

Recommended Conditions

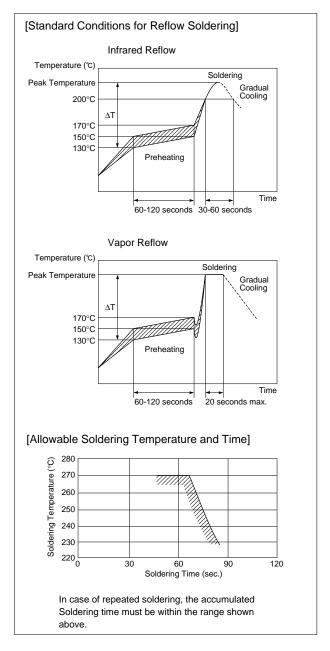
	Pb-Sn S	Lead Free Solder	
	Infrared Reflow	Vapor Reflow	Lead Free Solder
Peak Temperature	230-250°C	230-240°C	240-260°C
Atmosphere	Air	Air	Air or N2

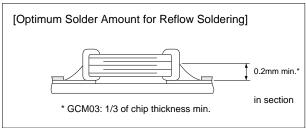
Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

- Optimum Solder Amount for Reflow Soldering
 - Overly thick application of solder paste results in excessive fillet height solder.
 - This makes the chip more susceptible to mechanical and thermal stress on the board and may cause cracked chips.
 - Too little solder paste results in a lack of adhesive strength on the outer electrode, which may result in chips breaking loose from the PCB.
 - Make sure the solder has been applied smoothly to the end surface to a height of 0.2mm min.

Inverting the PCB

Make sure not to impose an abnormal mechanical shock on the PCB.





⚠Caution

Continued from the preceding page.

4. Leaded Component Insertion

If the PCB is flexed when leaded components (such as transformers and ICs) are being mounted, chips may crack and solder joints may break.

Before mounting leaded components, support the PCB using backup pins or special jigs to prevent warping.

5. Flow Soldering

- When the sudden heat is given to the components, the mechanical strength of the components should go down because remarkable temperature change causes deformity of components inside. And an excessively long soldering time or high soldering temperature results in leaching of the outer electrodes, causing poor adhesion or a reduction in capacitance value due to loss of contact between electrodes and end termination.
- In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in table 2. It is required to keep temperature differential between the soldering and the components surface (ΔT) as small as possible. When components are immersed in solvent after mounting, be sure to maintain the temperature difference between the component and solvent within the range shown in Table 2.

Don't apply flow soldering to chips not listed in Table 2.

Table 2

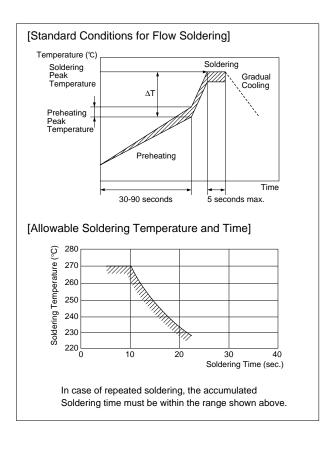
Part Number	Temperature Differential
GCM18/21/31	ΔT≦150°C

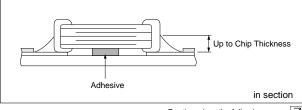
Recommended Conditions

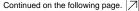
	Pb-Sn Solder	Lead Free Solder
Preheating Peak Temperature	90-110°C	100-120°C
Soldering Peak Temperature	240-250°C	250-260°C
Atmosphere	Air	N ₂

Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

Optimum Solder Amount for Flow Soldering
 The top of the solder fillet should be lower than the
 thickness of components. If the solder amount is
 excessively big, the risk of cracking is higher during
 board bending or under any other stressful conditions.









⚠Caution

- Continued from the preceding page.
- 6. Correction with a Soldering Iron
- (1) For Chip Type Capacitors
- When sudden heat is applied to the components by use of a soldering iron, the mechanical strength of the components will go down because the extreme temperature change causes deformations inside the components. In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB board. Preheating conditions, (The "Temperature of the Soldering Iron tip", "Preheating Temperature", "Temperature Differential" between the iron tip and the components and the PCB), should be within the conditions of table 3. It is required to keep the temperature differential between the soldering Iron and the components surface (ΔT) as small as possible. After soldering, do not allow the component/PCB to cool down rapidly. The operating time for the re-working should be as short as possible. When re-working time is too long, it may cause solder leaching, and that will cause a reduction of the adhesive strength of the terminations.
- Optimum Solder Amount when re-working Using a Soldering Iron In case of smaller size than 0603, (GCM03/15/18), the top of the solder fillet should be lower than 2/3's of the thickness of the component or 0.5mm whichever is smaller. In case of 0805 and larger sizes, (GCM21/31/32), the top of the solder fillet should be lower than 2/3's of the thickness of the components. If the solder amount is excessive, the risk of cracking is higher during board bending or under any other stressful conditions. A Soldering iron ø3mm or smaller should be used. It is also necessary to keep the soldering iron from touching the components during the re-work. Solder wire with ø0.5mm or smaller is required for soldering.
- Please carry it out after having confirmed that there is not abnormality such as product cracks by mounter beforehand.
- 7. Washing
- Excessive output of ultrasonic oscillation during cleaning causes PCBs to resonate, resulting in cracked chips or broken solder. Take note not to vibrate PCBs.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND FUMING WHEN THE PRODUCTS IS USED.

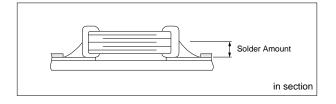
Table 3

Part Number	Temperature of Soldering Iron tip	Preheating Temperature	Temperature Differential (∆T)	Atomosphere				
GCM03/15/18/21/31	350°C max.	150°C min.	ΔT≦190°C	Air				
GCM32	280°C max.	150°C min.	ΔT≦130°C	Air				

*Applicable for both Pb-Sn and Lead Free Solder.

Pb-Sn Solder: Sn-37Pb

Lead Free Solder: Sn-3.0Ag-0.5Cu





Notice

■ Notice (Soldering and Mounting)

- 1. PCB Design
- (1) Notice for Pattern Forms

Unlike leaded components, chip components are susceptible to flexing stresses since they are mounted directly on the substrate.

They are also more sensitive to mechanical and thermal stresses than leaded components.

Excess solder fillet height can multiply these stresses and cause chip cracking. When designing substrates, take land patterns and dimensions into consideration to eliminate the possibility of excess solder fillet height. It causes breaking a chip to mount on metal substrate when heat stress increased, because there are different of thermal expansion coefficient between metal substrate and chip. Please contact us in the case of mounting metal substrate beforehand.

Pattern Forms

	Placing Close to Chassis	Placing of Chip Components and Leaded Components	Placing of Leaded Components after Chip Component	Lateral Mounting
Prohibited	Chassis Solder (Ground) Electrode Pattern in section	Lead Wire in section	Soldering Iron Lead Wire in section	
Correct	Solder Resist in section	Solder Resist in section	Solder Resist in section	Solder Resist

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Notice

Continued from the preceding page.

(2) Land Dimensions

Excessive amount of solder gives much stress to the components. Appropriate land pattern size can reduce the amount of solder and the mechanical stress to the components. Recommended land pattern dimension for flow and reflow are shown in Table 1 and Table 2 respectively.

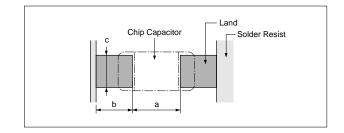


Table 1 Flow Soldering Method

Dimensions Part Number	Dimensions (LXW)	a	b	С
GCM18	1.6×0.8	0.6-1.0	0.8-0.9	0.6-0.8
GCM21	2.0×1.25	1.0-1.2	0.9-1.0	0.8-1.1
GCM31	3.2×1.6	2.2-2.6	1.0-1.1	1.0-1.4

Table 2 Reflow Soldering Method

(in mm)

Dimensions Part Number	Dimensions (LXW)	a	b	С
GCM03	0.6×0.3	0.2-0.3	0.2-0.35	0.2-0.4
GCM15	1.0×0.5	0.3-0.5	0.35-0.45	0.4-0.6
GCM18	1.6×0.8	0.6-0.8	0.6-0.7	0.6-0.8
GCM21	2.0×1.25	1.0-1.2	0.6-0.7	0.8-1.1
GCM31	3.2×1.6	2.2-2.4	0.8-0.9	1.0-1.4
GCM32	3.2×2.5	2.0-2.4	1.0-1.2	1.8-2.3

(in mm)

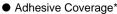
2. Adhesive Application

 Thin or insufficient adhesive causes chips to loosen or become disconnected when flow soldered.

The amount of adhesive must be more than dimension c shown in the drawing below to obtain enough bonding strength.

The chip's electrode thickness and land thickness must be taken into consideration.

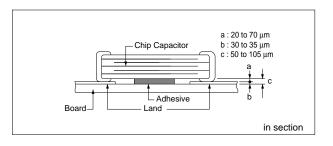
 Low viscosity adhesive causes chips to slip after mounting. Adhesive must have a viscosity of 5000Pa·s (500ps) min. (at 25°C)



- / taileeire eerelage						
Part Number	Adhesive Coverage*					
GCM18	0.05mg min.					
GCM21	0.1mg min.					
GCM31	0.15mg min.					

*Nominal Value

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3. Adhesive Curing

Insufficient curing of the adhesive causes chips to disconnect during flow soldering and causes deteriorated insulation resistance between outer electrodes due to moisture absorption.

Control curing temperature and time in order to prevent insufficient hardening.

Inverting the PCB

Make sure not to impose an abnormal mechanical shock on the PCB.

Notice

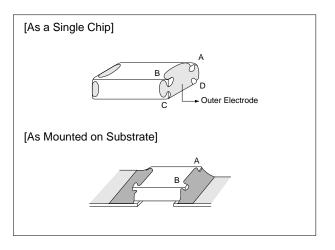
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4. Flux Application

- An excessive amount of flux generates a large quantity of flux gas, causing deteriorated solderability.
 So apply flux thinly and evenly throughout.
 (A foaming system is generally used for flow soldering.)
- Flux containing too high percentage of halide may cause corrosion of the outer electrodes unless sufficient cleaning. Use flux with a halide content of 0.2% max.
- Do not use strong acidic flux.
- Do not use water-soluble flux*.
 (*Water-soluble flux can be defined as non rosin type flux including wash-type flux and non-wash-type flux.)

5. Flow Soldering

 Set temperature and time to ensure that leaching of the outer electrode does not exceed 25% of the chip end area as a single chip (full length of the edge A-B-C-D shown below) and 25% of the length A-B shown below as mounted on substrate.



■ Notice (Other)

Resin Coating
 When selecting resin materials, select those with low contraction.

Circuit Design GCM Series capacitors in this catalog are not safety recognized products.

3. Remarks

The above notices are for standard applications and conditions. Contact us when the products are used in special mounting conditions. Select optimum conditions for operation as they determine the reliability of the product after assembly.

The data herein are given in typical values, not guaranteed ratings.



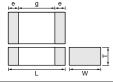
Chip Monolithic Ceramic Capacitors for Automotive



Medium Voltage for Automotive GCM Series Low Dissipation Factor

■ Features

- 1. The GCM series meet AEC-Q200 requirements.
- 2. Low-loss and suitable for high frequency circuits
- 3. Murata's original internal electrode structure realizes high flash-over voltage.
- 4. A new monolithic structure for small, surfacemountable devices capable of operating at high voltage levels.
- 5. Sn-plated external electrodes realize good solderability.
- 6. Use the GCM21/31 type with flow or reflow soldering, and other types with reflow soldering only.



Part Number	Dimensions (mm)					
Part Number	L	W	Т	e min.	g min.	
GCM21A	2.0 ±0.2	1.25 ±0.2	1.0 +0,-0.3		0.7	
GCM31A	3.2 ±0.2	1.6 +0.2	1.0 +0,-0.3	0.3		
GCM31B	3.2 ±0.2	1.0 ±0.2	1.25 +0,-0.3	0.3	1.5	
GCM32A	3.2 ±0.2	2.5 ±0.2	1.0 +0,-0.3			

■ Applications

Ideal for use on high frequency pulse circuits such as snubber circuits for DC-DC converters.

Part Number	Rated Voltage (V)	TC Code (Standard)	Capacitance (pF)	Length L (mm)	Width W (mm)	Thickness T (mm)	Electrode g min. (mm)	Electrode e (mm)
GCM21A7U2E101JX01D	DC250	U2J (EIA)	100 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E151JX01D	DC250	U2J (EIA)	150 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E221JX01D	DC250	U2J (EIA)	220 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E331JX01D	DC250	U2J (EIA)	330 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E471JX01D	DC250	U2J (EIA)	470 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E681JX01D	DC250	U2J (EIA)	680 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E102JX01D	DC250	U2J (EIA)	1000 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E152JX01D	DC250	U2J (EIA)	1500 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM21A7U2E222JX01D	DC250	U2J (EIA)	2200 ±5%	2.0	1.25	1.0	0.7	0.3 min.
GCM31A7U2E332JX01D	DC250	U2J (EIA)	3300 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2E472JX01D	DC250	U2J (EIA)	4700 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31B7U2E682JX01L	DC250	U2J (EIA)	6800 ±5%	3.2	1.6	1.25	1.5	0.3 min.
GCM31B7U2E103JX01L	DC250	U2J (EIA)	10000 ±5%	3.2	1.6	1.25	1.5	0.3 min.
GCM31A7U2J100JX01D	DC630	U2J (EIA)	10 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J150JX01D	DC630	U2J (EIA)	15 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J220JX01D	DC630	U2J (EIA)	22 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J330JX01D	DC630	U2J (EIA)	33 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J470JX01D	DC630	U2J (EIA)	47 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J680JX01D	DC630	U2J (EIA)	68 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J101JX01D	DC630	U2J (EIA)	100 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J151JX01D	DC630	U2J (EIA)	150 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J221JX01D	DC630	U2J (EIA)	220 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J331JX01D	DC630	U2J (EIA)	330 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J471JX01D	DC630	U2J (EIA)	470 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J681JX01D	DC630	U2J (EIA)	680 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM31A7U2J102JX01D	DC630	U2J (EIA)	1000 ±5%	3.2	1.6	1.0	1.5	0.3 min.
GCM32A7U2J152JX01D	DC630	U2J (EIA)	1500 ±5%	3.2	2.5	1.0	1.5	0.3 min.
GCM32A7U2J222JX01D	DC630	U2J (EIA)	2200 ±5%	3.2	2.5	1.0	1.5	0.3 min.

No.	AEC- Test		Specifications	AEC-Q200 Test Method
1	Pre-and Post-Stress Electrical Test High Temperature			_
	High Tem Exposure	•	The measured and observed characteristics should satisfy the specifications in the following table.	
		Appearance	No marking defects	
2		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Sit the capacitor for 1000±12 hours at 150±3°C. Let sit for 24±2 hours at room temperature, then measure.
		Q	Q≥1000	
		I.R.	More than 10,000M Ω or 500M $\Omega \cdot \mu F$ (Whichever is smaller)	
	Temperat Cycle	ture	The measured and observed characteristics should satisfy the specifications in the following table.	Fix the capacitor to the supporting jig in the same manner and
		Appearance	No marking defects	under the same conditions as (19). Perform the 1000 cycles according to the 4 heat treatments listed in the following table.
3		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)	Let sit for 24±2 hours at room temperature, then measure.
		Q	Q≧1000	Step 1 2 3 4
		I.R.	More than 10,000M Ω or 500M $\Omega \cdot \mu F$ (Whichever is smaller)	Temp. (°C) -55+0/-3 Room Temp. 125+3/-0 Room Temp. Time (min.) 15±3 1 15±3 1
4	Destructi Physical		No defects or abnormalities	Per EIA-469
	Moisture Resistant	ce	The measured and observed characteristics should satisfy the specifications in the following table.	Apply the 24 hour heat (25 to 65°C) and humidity (80 to 98%) treatment shown below, 10 consecutive times.
		Appearance	No marking defects	Let sit for 24±2 hours at room temperature, then measure.
		Capacitance Change	Within ±3.0% or ±0.3pF (Whichever is larger)	"C 90-98% 80-98% 90-98% 80-98% 90-98%
		Q	Q≧350	65 60 75 75 75 75 75 75 75 75 75 75 75 75 75
5		I.R.	More than 10,000M Ω or 500M Ω · μ F (Whichever is smaller)	50 40 80 80 80 90 15 10 10 10 10 10 10 10 10 10 10
	Biased H	umidity	The measured and observed characteristics should satisfy the specifications in the following table.	
		Appearance	No marking defects	Apply the rated voltage and DC1.3+0.2/-0V (add 6.8kΩ
6		Capacitance Change	Within ±3.0% or ±0.3pF (Whichever is larger)	resistor) at 85±3°C and 80 to 85% humidity for 1000±12 hours. Remove and let sit for 24±2 hours at room temperature, then measure.
		Q	Q≧200	The charge/discharge current is less than 50mA.
		I.R.	More than 1,000M Ω or 50M Ω \cdot μ F (Whichever is smaller)	
	Operation	nal Life	The measured and observed characteristics should satisfy the specifications in the following table.	
		Appearance	No marking defects	Apply 120% of the rated voltage for 1000±12 hours at
7		Capacitance Change	Within ±3.0% or ±0.3pF (Whichever is larger)	125±3°C. Let sit for 24±2 hours at room temperature, then measure.
		Q	Q≧350	The charge/discharge current is less than 50mA.
		I.R.	More than 1,000M Ω or 50M Ω \cdot μF (Whichever is smaller)	
8	External \	/isual	No defects or abnormalities	Visual inspection
9	Physical [Dimension	Within the specified dimensions	Using calipers

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Continued from the preceding page.

$ \mathcal{A} $	Continued fr	om the prec	eding page.		
No.	AEC-Q200 Test Item		Specifications	AEC-Q200 Test Method	
10	Resistance	Appearance Capacitance Change Q	No marking defects Within the specified tolerance Q≥1000	Per MIL-STD-202 Method 215 Solvent 1: 1 part (by volume) of isopropyl alcohol 3 parts (by volume) of mineral spirits Solvent 2: Terpene defluxer	
	to Solvents	I.R.	More than 10,000M Ω or 500M Ω · μF (Whichever is smaller)	Solvent 3: 42 parts (by volume) of water 1 part (by volume) of propylene glycol monomethyl ether 1 part (by volume) of monoethanolomine	
11	Mechanical	Appearance Capacitance Change	No marking defects Within the specified tolerance	Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen (18 shocks).	
	Shock	I.R.	Q≥1000 More than 10,000MΩ or 500MΩ · μF (Whichever is smaller)	The specified test pulse should be Half-sine and should have a duration: 0.5ms, peak value: 1500g and velocity change: 4.7m/s.	
12	Vibration	Appearance Capacitance Change	No defects or abnormalities Within the specified tolerance Q≥1000	Solder the capacitor to the test jig (glass epoxy board) in the same manner and under the same conditions as (19). The capacitor should be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 2000Hz. The	
12	VIDIALIOII	I.R.	More than 10,000M Ω or 500M $\Omega \cdot \mu F$ (Whichever is smaller)	frequency range, from 10 to 2000Hz and return to 10Hz, should be traversed in approximately 20 minutes. This motion should be applied for 12 items in each 3 mutually perpendicular directions (total of 36 times).	
	Resistand Soldering		The measured and observed characteristics should satisfy the specifications in the following table.		
13		Appearance Capacitance Change	No marking defects Within the specified tolerance	Immerse the capacitor in a eutectic solder solution at 260±5°C for 10±1 seconds. Let sit at room temperature for 24±2 hours, then measure.	
		Q I.R.	Q≥1000 More than 10,000MΩ or 500MΩ · μF (Whichever is smaller)		
	Thermal	Shock	The measured and observed characteristics should satisfy the specifications in the following table.	Fix the capacitor to the supporting jig in the same manner and under the same conditions as (19). Perform the 300 cycles	
14		Appearance Capacitance Change	No marking defects Within ±2.5% or ±0.25pF (Whichever is larger)	according to the two heat treatments listed in the following table (Maximum transfer time is 20 seconds). Let sit for 24±2 hours at room temperature, then measure.	
		Q I.R.	Q≥1000 More than 10,000MΩ or 500MΩ · μF (Whichever is smaller)	Step 1 2 Temp. (°C) -55+0/-3 125+3/-0 Time (min.) 15±3 15±3	
		Appearance Capacitance Change	No marking defects Within the specified tolerance		
15	ESD	Q	Q≥1000	Per AEC-Q200-004	
		I.R.	More than 10,000M Ω or 500M $\Omega \cdot \mu F$ (Whichever is smaller)		
			(a) Preheat at 155°C for 4 hours. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5°C.		
16	Solderab	ility	95% of the terminations is to be soldered evenly and continuously.	(b) Should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in eutectic solder solution for 5+0/-0.5 seconds at 235±5°C.	
				(c) Should be placed into steam aging for 8 hours±15 minutes. After preheating, immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Immerse in eutectic solder solution for 120 ±5 seconds at 260±5°C.	

Ontinued from the preceding page.

AEC-	Q200						
Test	Item	em Specifications		AEC-Q200 Test Method		Method	
	Appearance	No defects or abnormalities		Visual inspection.			
	Capacitance Change	Within the specified tolerance		The capacitance/Q should be measured at 25°C at the frequent and voltage shown in the table.			
	Q	Q≧1000		C<1000pF C≥1000pF	Frequency 1±0.1MHz 1±0.1kHz	Voltage AC0.5 to 5V(r.m.s.) AC1±0.2V(r.m.s.)	
Electrical Characteriation	I.R.	25°C More than $100,000\text{M}\Omega$ or $1,000\text{M}\Omega$ (Whichever is smaller) Max. Operating Temperature…125 More than $10,000\text{M}\Omega$ or $100\text{M}\Omega$ · (Whichever is smaller)	5°C			neasured with a DC voltage °C and 125°C and within 2	
	Dielectric Strength	No failure		No failure should be of between the termination charge/discharge current Rated Voltage DC250V DC630V	ons for 1 to 5 seent is less than		
	Appearance	No marking defects			Solder the capacitor on the test jig (glass epoxy board) shown in		
	Capacitance Change Within ±5.0% or ±0.5pF (Whichever is larger)		Fig. 1 using a eutectic solder. Then apply a force in the direction shown in Fig. 2 for 5±1 seconds. The soldering should be done by the reflow method and should be conducted with care so that				
	Q	Q≧1000		the soldering is uniform	n and free of de	efects such as heat shock.	
Board Flex	I.R.	More than 10,000M Ω or 500M Ω · μF (Whichever is smaller)	t 1.6mm	Type GCM21 GCM31 GCM32	pacitance meter	b c 3.0 1.3 4.4 1.7 4.4 2.6 (in mm) Pressunzing speed: 1.0mm/s Pressurize Flexure: ≤3	
	Appearance	No marking defects		· ·	, , ,	ass epoxy board) shown in	
	Capacitance Change	Within the specified tolerance		with the test jig for 60 s	seconds.	pply 18N force in parallel reflow method and should	
	Q	Q≧1000		be conducted with care of defects such as hea		ldering is uniform and free	
Terminal Strength	I.R.	More than 10,000M Ω or 500M Ω · (Whichever is smaller)	μF	Type GCM21 GCM31 GCM32	a 1.2 2.2 2.2	b c 4.0 1.65 5.0 2.0 5.0 2.9 (in mm)	
		ength	ength More than $10,000 \mathrm{M}\Omega$ or $500 \mathrm{M}\Omega$ ·	ength More than 10,000MΩ or 500MΩ · μF	minal ength $ \begin{array}{c c} \hline GCM21 \\ \hline GCM31 \\ \hline GCM32 \\ \hline \\ \hline \\ More than 10,000M\Omega \ or \ 500M\Omega \cdot \mu F \\ \hline \\$	GCM21 1.2 GCM31 2.2 GCM32 2.2	

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09.3.31

Specifications and Test Methods

Continued from the preceding page.

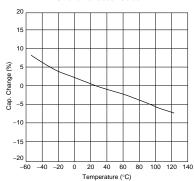
	Solution of the preceding page.					
No.	AEC-		Specifications	AEC-Q200 Test Method		
20	20 Beam Load Test		The chip endure following force. < Chip L dimension: 2.5mm max. > Chip thickness > 0.5mm rank: 20N Chip thickness ≤ 0.5mm rank: 8N < Chip L dimension: 3.2mm min. > Chip thickness < 1.25mm rank: 15N Chip thickness ≥ 1.25mm rank: 54.5N	Place the capacitor in the beam load fixture as Fig. 4. Apply a force. < Chip L dimension: 2.5mm max. > Iron Board < Chip L dimension: 3.2mm min. > Fig. 4 Speed supplied the Stress Load: 2.5mm / s		
21	Capacitance Temperature Character- istics	Capacitance Change	-750±120 ppm/°C (Temp. Range: +25 to +125°C) -750±120, -347 ppm/°C (Temp. Range: -55 to +25°C)	The capacitance change should be measured after 5 min. at each specified temperature stage. The temperature coefficient is determined using the capacitance measured in step 3 as a reference. When cycling the temperature sequentially from step1 through 5 the capacitance should be within the specified tolerance for the temperature coefficient. The capacitance drift is calculated by dividing the differences between the maximum and minimum measured		
		Capacitance Drift	Within ±0.5% or ±0.05 pF (Whichever is larger)	values in steps 1, 3 and 5 by the capacitance value in step 3. Step Temperature (°C) 1 25±2 2 -55±3 3 25±2 4 125±3 5 25±2		



Data

■ Capacitance - Temperature Characteristics





Package

Taping is standard packaging method.

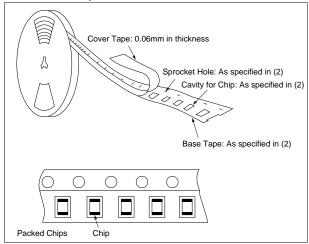
■ Minimum Quantity Guide

Part Number		Dimensions (mm)			Quantity (pcs.)		
					ø180mm Reel		
		L	W	Т	Paper Tape	Embossed Tape	
	GCM21	2.0	1.25	1.0	4,000	-	
Medium Voltage	GCM31	3.2 1.6	1.6	1.0	4,000	-	
wedidiii voitage			1.0	1.25	-	3,000	
	GCM32	3.2	2.5	1.0	4,000	-	

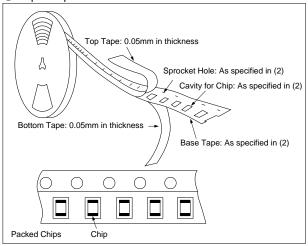
■ Tape Carrier Packaging

(1) Appearance of Taping

① Embossed Tape

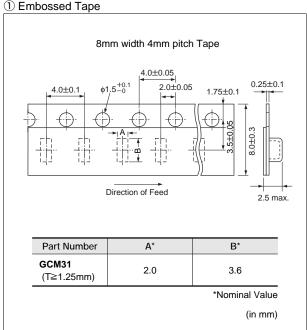




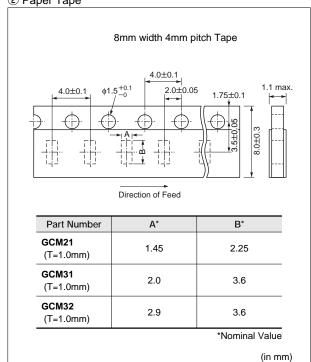


(2) Dimensions of Tape

① Embossed Tape



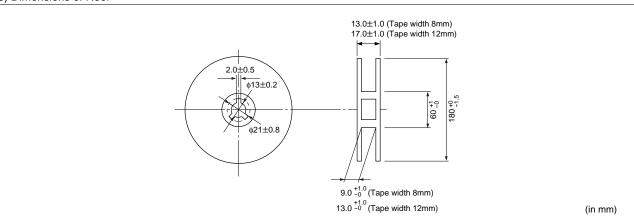
2 Paper Tape



Package

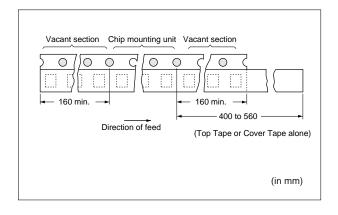
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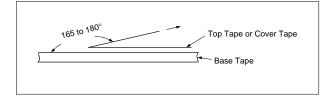
(3) Dimensions of Reel



(4) Taping Method

- 1 Tapes for capacitors are wound clockwise. The sprocket holes are to the right as the tape is pulled toward the user.
- 2 Part of the leader and part of the empty tape should be attached to the end of the tape as shown at right.
- 3 The top tape or cover tape and base tape are not attached at the end of the tape for a minimum of 5
- 4 Missing capacitors number within 0.1% of the number per reel or 1 pc, whichever is greater, and are not continuous.
- (5) The top tape or cover tape and bottom tape should not protrude beyond the edges of the tape and should not cover sprocket holes.
- 6 Cumulative tolerance of sprocket holes, 10 pitches: ±0.3mm.
- 7 Peeling off force: 0.1 to 0.6N in the direction shown at







■ Storage and Operating Conditions

Operating and storage environment Do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors

where the temperature and relative humidity do not exceed 5 to 40 degrees centigrade and 20 to 70%. Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

■ Handling

- 1. Vibration and impact Do not expose a capacitor to excessive shock or vibration during use.
- 2. Do not directly touch the chip capacitor, especially the ceramic body. Residue from hands/fingers may create a short circuit environment.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



Rating

1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

When DC-rated capacitors are to be used in input circuits from commercial power source (AC filter), be sure to use Safety Recognized Capacitors because various regulations on withstand voltage or impulse withstand established for each equipment should be taken into considerations.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

2. Operating Temperature, Self-generated Heat, and Load Reduction at High-frequency Voltage Condition Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a highfrequency voltage, pulse voltage, it may self-generate heat due to dielectric loss.

The frequency of the applied sine wave voltage should be less than 500kHz. The applied voltage should be less than the value shown in figure below.

In case of non-sine wave which includes a harmonic frequency, please contact our sales representatives or product engineers. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running.

Otherwise, accurate measurement cannot be ensured.)

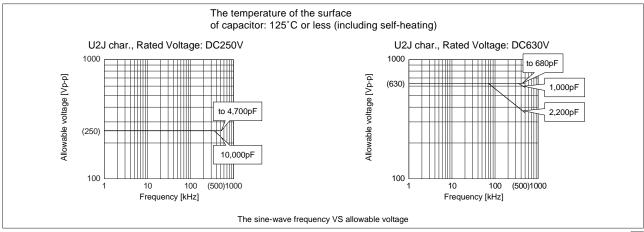
<Capacitor Selection Tool>

We are also offering free software the "capacitor selection tool: Murata Medium Voltage Capacitors Selection Tool by Voltage Form (*)" which will assist you in selecting a suitable capacitor.

The software can be downloaded from Murata's Internet Web site.

(http://www.murata.com/designlib/mmcsv e.html) By inputting capacitance values and applied voltage waveform of the specific capacitor series, this software will calculate the capacitor's power consumption and list suitable capacitors. (non-sine wave is also available).

- * Subject series are below.
- · Temperature Characteristics U2J





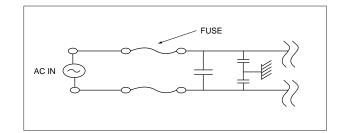
⚠Caution

Continued from the preceding page.

3. Fail-safe

Failure of a capacitor may result in a short circuit. Be sure to provide an appropriate fail-safe function such as a fuse on your product to help eliminate possible electric shock, fire, or fumes.

Please consider using fuses on each AC line if the capacitors are used between the AC input lines and earth (line bypass capacitors), to prepare for the worst case, such as a short circuit.



4. Test Condition for AC Withstanding Voltage

(1) Test Equipment

Tests for AC withstanding voltage should be made with equipment capable of creating a wave similar to a 50/60 Hz sine wave.

If the distorted sine wave or overload exceeding the specified voltage value is applied, a defect may be caused.

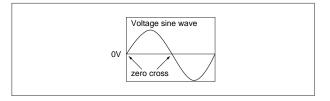
(2) Voltage Applied Method

The capacitor's leads or terminals should be firmly connected to the output of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage. If the test voltage is applied directly to the capacitor without raising it from near zero, it should be applied with the zero cross*. At the end of the test time, the test voltage should be reduced to near zero, and then the capacitor's leads or terminals should be taken off the output of the withstanding voltage test equipment. If the test voltage is applied directly to the capacitor without raising it from near zero, surge voltage may occur and cause a defect.

*ZERO CROSS is the point where voltage sine wave pass 0V.

- See the figure at right -

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



⚠Caution

2

■ Solder and Mounting

1. Vibration and Impact Do not expose a capacitor to excessive shock or vibration during use.

2. Circuit Board Material

In case that ceramic chip capacitor is soldered on the metal board, such as Aluminum board, the stress of heat expansion and contraction might cause the crack of ceramic capacitor, due to the difference of thermal expansion coefficient between metal board and ceramic chip.

3. Land Layout for Cropping PC Board

Choose a mounting position that minimizes the stress imposed on the chip during flexing or bending of the board.

[Component Direction]

Locate chip horizontal to the direction in which stress acts.

C Perforation В D Α

[Chip Mounting Close to Board Separation Point]

Chip arrangement Worst A>C>B~D Best

<Examples of improvements> <Examples to be avoided>





Continued from the preceding page.

4. Reflow Soldering

- When the sudden heat is given to the components, the mechanical strength of the components should go down because remarkable temperature change causes deformity of components inside. In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in Table 1. It is required to keep temperature differential between the soldering and the components surface (ΔT) as small as possible.
- Solderability of Tin plating termination chip might be deteriorated when low temperature soldering profile where peak solder temperature is below the Tin melting point is used. Please confirm the solderability of Tin plating termination chip before use.
- When components are immersed in solvent after mounting, be sure to maintain the temperature difference (ΔT) between the component and solvent within the range shown in the Table 1.

Table 1

Part Number	Temperature Differential	
G□□21/31	ΔΤ≦190℃	
G □□32	ΔΤ≦130℃	

Recommended Conditions

	Pb-Sn S	Lood Fron Coldon	
	Infrared Reflow	Vapor Reflow	Lead Free Solder
Peak Temperature	230-250°C	230-240°C	240-260°C
Atmosphere	Air	Air	Air or N ₂

Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

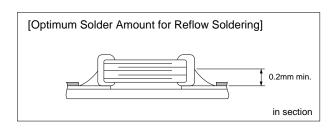
[Standard Conditions for Reflow Soldering] Infrared Reflow Temperature (°C) Soldering Peak Temperature Gradual Cooling 200°C Δ٦ 170°C 150°C 130°C Preheating Time 60-120 seconds 30-60 seconds Vapor Reflow Temperature (°C) Soldering Peak Temperature Gradual Cooling 170°C 150°C 130°C Preheating Time 60-120 seconds 20 seconds max [Allowable Soldering Temperature and Time] Soldering Temperature (°C) 270 260 250 230 0 90 Soldering Time (sec.) In case of repeated soldering, the accumulated soldering time must be within the range shown above.

Optimum Solder Amount for Reflow Soldering

- Overly thick application of solder paste results in excessive fillet height solder.
 - This makes the chip more susceptible to mechanical and thermal stress on the board and may cause cracked chips.
- Too little solder paste results in a lack of adhesive strength on the outer electrode, which may result in chips breaking loose from the PCB.
- Make sure the solder has been applied smoothly to the end surface to a height of 0.2mm min.

Inverting the PCB

Make sure not to impose an abnormal mechanical shock on the PCB.



⚠Caution

Continued from the preceding page.

5. Flow Soldering

- When the sudden heat is given to the components, the mechanical strength of the components should go down because remarkable temperature change causes deformity of components inside. And an excessively long soldering time or high soldering temperature results in leaching by the outer electrodes, causing poor adhesion or a reduction in capacitance value due to loss of contact between electrodes and end termination.
- In order to prevent mechanical damage in the components, preheating should be required for both of the components and the PCB board. Preheating conditions are shown in Table 2. It is required to keep temperature differential between the soldering and the components surface (ΔT) as small as possible.
- When components are immersed in solvent after mounting, be sure to maintain the temperature difference between the component and solvent within the range shown in Table 2.

Do not apply flow soldering to chips not listed in Table 2.

Table 2

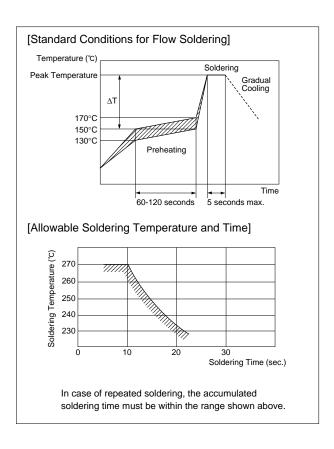
Part Number	Temperature Differential		
G□□21/31	ΔT≦150°C		

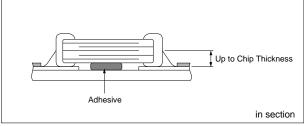
Recommended Conditions

	Pb-Sn Solder	Lead Free Solder	
Peak Temperature	240-250°C	250-260°C	
Atmosphere	Air	N ₂	

Ph-Sn Solder: Sn-37Ph Lead Free Solder: Sn-3.0Ag-0.5Cu

 Optimum Solder Amount for Flow Soldering The top of the solder fillet should be lower than the thickness of components. If the solder amount is excessively big, the risk of cracking is higher during board bending or under any other stressful conditions.







2



Continued from the preceding page.

6. Correction with a Soldering Iron

 When sudden heat is applied to the components by use of a soldering iron, the mechanical strength of the components will go down because the extreme temperature change causes deformations inside the components.

In order to prevent mechanical damage to the components, preheating is required for both the components and the PCB board.

Preheating conditions, (The "Temperature of the Soldering Iron tip", "Preheating Temperature",

"Temperature Differential" between iron tip and the

Table 3

Part Number	Temperature of Soldering Iron tip	Preheating Temperature		Atmosphere
G□□21/31	350°C max.	150°C min.	ΔΤ≦190℃	air
G□□32	280°C max.	150°C min.	ΔΤ≦130℃	air

*Applicable for both Pb-Sn and Lead Free Solder.

Pb-Sn Solder: Sn-37Pb Lead Free Solder: Sn-3.0Ag-0.5Cu

Optimum Solder Amount when re-working Using a Soldering Iron

In case of larger sizes than G□□21, the top of the solder fillet should be lower than 2/3's of the thickness of the component.

If the solder amount is excessive, the risk of cracking is higher during board bending or under any other stressful

A Soldering iron ø3mm or smaller should be used. It is also necessary to keep the soldering iron from touching the components during the re-work. Solder wire with Ø0.5mm or smaller is required for soldering.

7. Washing

Excessive output of ultrasonic oscillation during cleaning causes PCBs to resonate, resulting in cracked chips or broken solder. Take note not to vibrate PCBs.

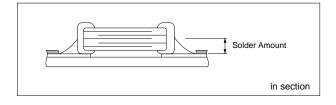
FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT. WORST CASE. IN A SHORT CIRCUIT AND FUMING WHEN THE PRODUCT IS USED.

components and the PCB), should be within the conditions of table 3.

It is required to keep the temperature differential between the soldering Iron and the components surface (ΔT) as small as possible.

After soldering, do not allow the component/PCB to cool down rapidly.

The operating time for the re-working should be as short as possible. When re-working time is too long, it may cause solder leaching, and that will cause a reduction of the adhesive strength of the terminations.



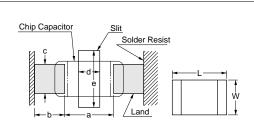
2

■ Solder and Mounting

1. Construction of Board Pattern

After installing chips, if solder is excessively applied to the circuit board, mechanical stress will cause destruction resistance characteristics to lower. To prevent this, be extremely careful in determining shape and dimension before designing the circuit board diagram.

Construction and Dimensions of Pattern (Example)



Preparing slit helps flux cleaning and resin coating on the back of the capacitor.

Flow Soldering

L×W	а	b	С
2.0×1.25	1.0-1.2	0.9-1.0	0.8-1.1
3.2×1.6	2.2-2.6	1.0-1.1	1.0-1.4

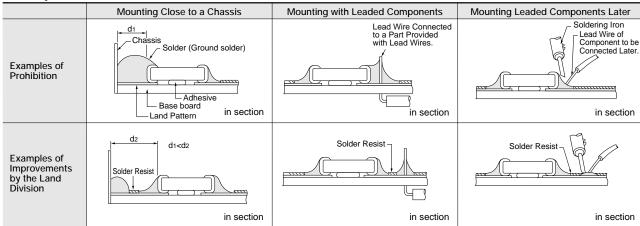
Flow soldering: 3.2×1.6 or less available.

Reflow Soldering

L×W	a	b	С	d	е
2.0×1.25	1.0-1.2	0.6-0.7	0.8-1.1	-	-
3.2×1.6	2.2-2.4	0.8-0.9	1.0-1.4	1.0-2.0	3.2-3.7
3.2×2.5	2.0-2.4	1.0-1.2	1.8-2.3	1.0-2.0	4.1-4.6

(in mm)

Land Layout to Prevent Excessive Solder







Continued from the preceding page.

2. Mounting of Chips

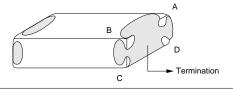
- Thickness of adhesives applied Keep thickness of adhesives applied (50-105μm or more) to reinforce the adhesive contact considering the thickness of the termination or capacitor (20-70µm) and the land pattern (30-35µm).
- Mechanical shock of the chip placer When the positioning claws and pick-up nozzle are worn, the load is applied to the chip while positioning is concentrated in one position, thus causing cracks, breakage, faulty positioning accuracy, etc. Careful checking and maintenance are necessary to prevent unexpected trouble. An excessively low bottom dead point of the suction nozzle imposes great force on the chip during mounting, causing cracked chips. Please set the suction nozzle's bottom dead point on the upper surface of the board.

3. Soldering

(1) Limit of losing effective area of the terminations and conditions needed for soldering.

Depending on the conditions of the soldering temperature and/or immersion (melting time), effective areas may be lost in some part of the terminations.

To prevent this, be careful in soldering so that any possible loss of the effective area on the terminations will securely remain at a maximum of 25% on all edge length A-B-C-D-A of part with A, B, C, D, shown in the Figure below.



(2) Flux Application

- An excessive amount of flux generates a large quantity of flux gas, causing deteriorated solderability. So apply flux thinly and evenly throughout. (A foaming system is generally used for flow soldering.)
- Flux containing too high percentage of halide may cause corrosion of the outer electrodes unless sufficient cleaning. Use flux with a halide content of 0.2% max.
- Do not use strong acidic flux.
- Do not use water-soluble flux*. (*Water-soluble flux can be defined as non rosin type flux including wash-type flux and non-wash-type flux.)





Continued from the preceding page.

4. Cleaning

Please confirm there is no problem in the reliability of the product beforehand when cleaning it with the intended

The residue after cleaning it might cause the decrease in the surface resistance of the chip and the corrosion of the electrode part, etc. As a result it might cause reliability to deteriorate. Please confirm beforehand that there is no problem with the intended equipment in ultrasonic cleansing.

5. Resin Coating

Please use it after confirming there is no influence on the product with a intended equipment beforehand when the resin coating and molding.

A cracked chip might be caused at the cooling/heating cycle by the amount of resin spreading and/or bias

The resin for coating and molding must be selected as the stress is small when stiffening and the hygroscopic is low as possible.

■ Rating

- 1. Capacitance change of capacitor Capacitance might change a little depending on the surrounding temperature or an applied voltage. Please contact us if you intend to use this product in a strict time constant circuit.
- 2. Performance check by equipment Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.



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