TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

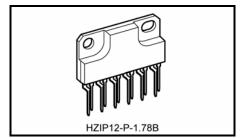
# TA8246AHQ

Dual Audio Power Amplifier 6 W × 2 Ch

TA8246AHQ is dual power amplifier for Consumer applications. This IC provides an output power of 6 watts per channel (at  $V_{CC} = 20 \text{ V}$ , f = 1 kHz, THD = 10%,  $R_L = 8 \Omega$ ). It is suitable for power amplifier of TV and home Stereo.

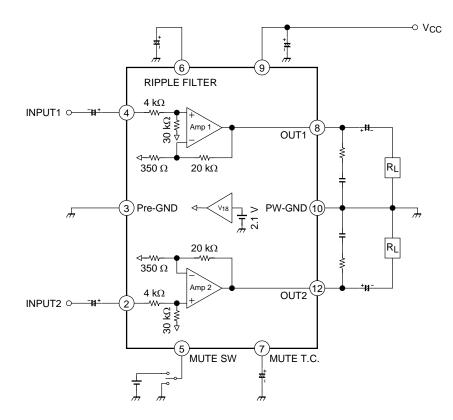
#### Features

- High output power: P<sub>out</sub> = 6 W (Typ.)
   (V<sub>CC</sub> = 20 V, R<sub>L</sub> = 8 Ω, f = 1 kHz, THD = 10%)
- Built-in audio muting circuit.
- NF terminal capacitor less
  - : Fixed gain ( $G_v = 34$ dB), needless external capacitor.
- Protectors
  - : Thermal shut down protection circuit, over voltage protection circuit
- Low poping noise
- High THD ratio
- High input dynamic range
- Available for using same PCB layout with 3 channel IC: TA8256BHQ
- Operating supply voltage range
  - :  $V_{CC}$  (opr) = 10~30 V (Ta = 25°C)



Weight: 4.04 g (typ.)

#### **Block Diagram**



# <u>TOSHIBA</u>

#### **Terminal Explanation**

Terminal No.	Symbol	Function	Equivalent Circuit		
2	IN2	- Input			
4	IN1	Input	2/4 G G G S G S G S G G S G G G G G G G G G G G G G		
3	Pre-GND	GND terminal			
5	MUTE SW	- MUTE control terminal	$V_{CC}$		
7	MUTE T.C.				
6	R/F	Ripple filter	6 CFX CFX CFX CFX CFX CFX CFX CFX		
8	OUT1	Quanta	V <sub>CC</sub>		
12	OUT2	Output	(8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8/12) (8		
9	V <sub>CC</sub>	Supply voltage terminal	—		
10	PW-GND	GND terminal	_		

1, 11: NC

#### Cautions

This IC is not proof enough against a strong E-M field by CRT which may cause malfunction such as leak. Please set the IC keeping the distance from CRT.

#### Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	30	V	
Output current (peak/ch)	I <sub>O (peak)</sub>	2	А	
Power dissipation	P <sub>D</sub> (Note)	25	W	
Operating temperature	T <sub>opr</sub>	-20~75	°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

Note: Derated above  $Ta = 25^{\circ}C$  in the proportion of 200 mW/°C.

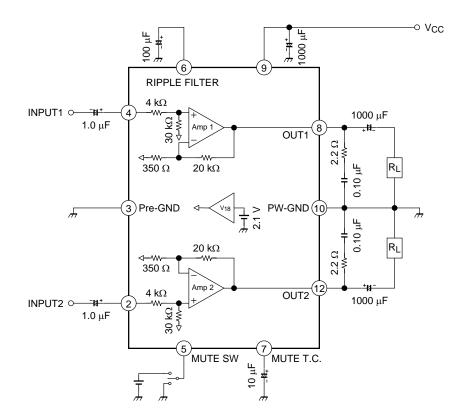
#### **Electrical Characteristics**

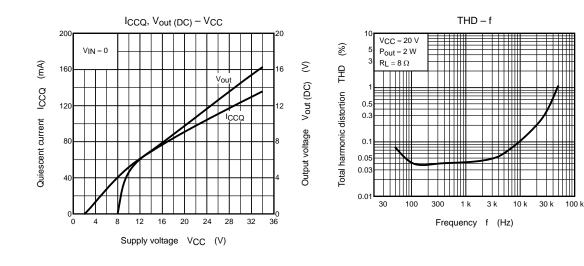
#### (Unless otherwise specified, V\_{CC} = 20 V, R\_L = 8 $\Omega, R_g$ = 620 $\Omega, f$ = 1 kHz, Ta = 25°C)

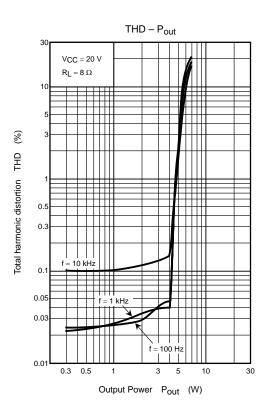
Characteristic	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max	Unit
Quiescent current	ICCQ	_	$V_{in} = 0$	50	85	130	mA
Output power	P <sub>out</sub> (1)	_	THD = 10%	5	6	_	w
	P <sub>out</sub> (2)	_	THD = 1%		4.5		
Total harmonic distortion	THD (1)	—	Pout = 2 W	_	0.04	0.2	%
	THD (2)	_	$P_{out} = 2 \text{ W}, f = 10 \text{ kHz},$	_	0.1	0.6	
Voltage gain	Gv	_	V <sub>out</sub> = 0.775 Vrms	32.5	34	35.5	dB
Input resistance	R <sub>IN</sub>	—	—	_	34	_	kΩ
Ripple rejection ratio	R.R.	_	f = 100 Hz	-40	-47		dB
Output noise voltage	V <sub>no</sub>	_	Rg = 10 kΩ, BW = 20 Hz~20 kHz		0.14	0.3	mVrms
Cross talk	C.T.	_	$\begin{array}{l} Rg=10 \ k\Omega, \\ V_{out}=0.775 \ Vrms \end{array}$	_	-60	_	dB
Mute control voltage	V <sub>th (ON)</sub>	—	MUTE ON	3.1	_	V <sub>CC</sub>	v
wate control voltage	Vth (OFF)	—	MUTE OFF	0	_	2.5	
Mute attenuation level	ATT	—	$V_{out} = 0.775 \text{ Vrms} \rightarrow \text{Mute}$	-52	-60		dB

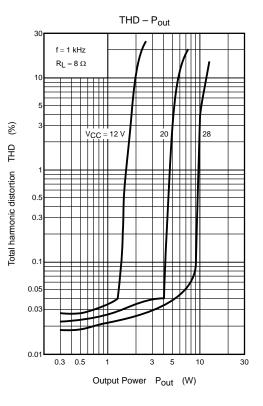
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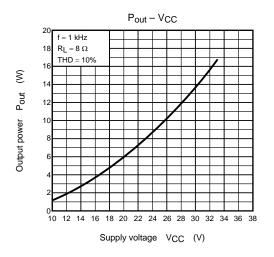
#### **Test Circuit**

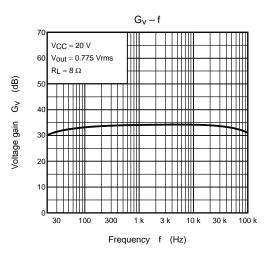


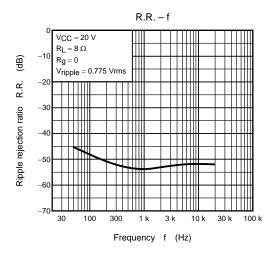


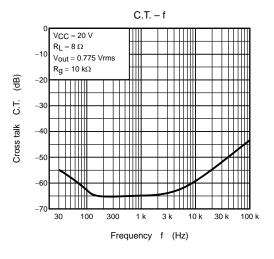


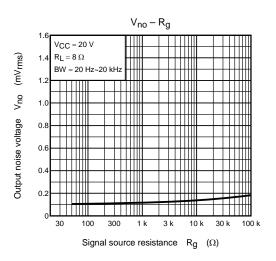


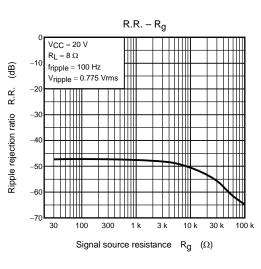


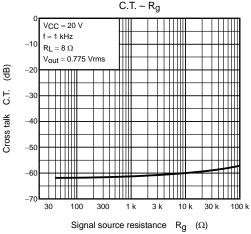


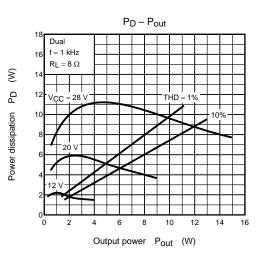


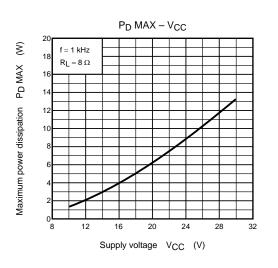


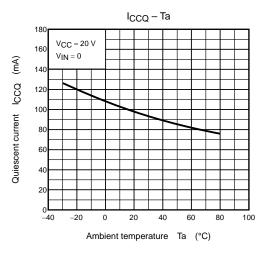


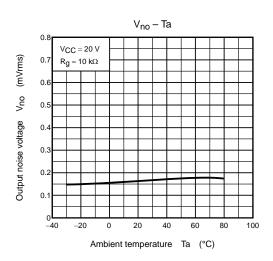


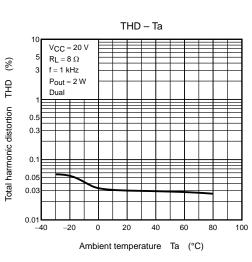




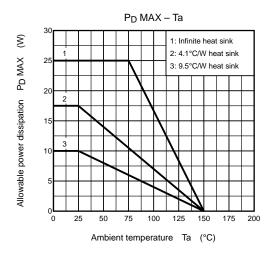








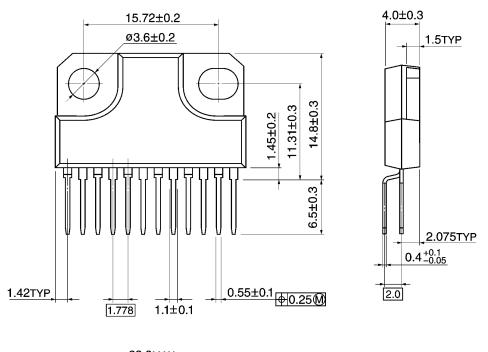
C.T. – Rg



#### Package Dimensions









Weight: 4.04 g (typ.)

About solderability, following conditions were confirmed
Solderability
<ul> <li>(1) Use of Sn-63Pb solder Bath <ul> <li>solder bath temperature = 230°C</li> <li>dipping time = 5 seconds</li> <li>the number of times = once</li> <li>use of R-type flux</li> </ul> </li> </ul>
<ul> <li>(2) Use of Sn-3.0Ag-0.5Cu solder Bath</li> <li>solder bath temperature = 245°C</li> <li>dipping time = 5 seconds</li> <li>the number of times = once</li> </ul>

• use of R-type flux

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   The product is often the final stage (the external output stage) of a circuit. Substandard performance or malfunction of the destination device to which the circuit supplies output may cause damage to the circuit or to the product.