

BAS16TT1

Preferred Device

Silicon Switching Diode

Features

- Pb-Free Package is Available*

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Rating	Symbol	Max	Unit
Continuous Reverse Voltage	V_R	75	V
Recurrent Peak Forward Current	I_F	200	mA
Peak Forward Surge Current Pulse Width = 10 μs	$I_{FM(\text{surge})}$	500	mA

THERMAL CHARACTERISTICS

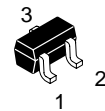
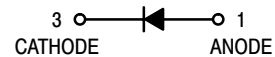
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	225	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	$^\circ\text{C}/\text{W}$
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	360	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	345	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

- FR-4 @ Minimum Pad
- FR-4 @ 1.0 x 1.0 Inch Pad



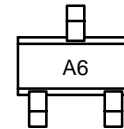
ON Semiconductor®

<http://onsemi.com>



CASE 463
SOT-416
STYLE 2

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping†
BAS16TT1	SOT-416	3000 / Tape & Reel
BAS16TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Preferred devices are recommended choices for future use and best overall value.

BAS16TT1

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Forward Voltage ($I_F = 1.0\text{ mA}$) ($I_F = 10\text{ mA}$) ($I_F = 50\text{ mA}$) ($I_F = 150\text{ mA}$)	V_F	– – – –	715 866 1000 1250	mV
Reverse Current ($V_R = 75\text{ V}$) ($V_R = 75\text{ V}, T_J = 150^\circ\text{C}$) ($V_R = 25\text{ V}, T_J = 150^\circ\text{C}$)	I_R	– – –	1.0 50 30	μA
Capacitance ($V_R = 0, f = 1.0\text{ MHz}$)	C_D	–	2.0	pF
Reverse Recovery Time ($I_F = I_R = 10\text{ mA}, R_L = 50\ \Omega$) (Figure 1)	t_{rr}	–	6.0	ns
Stored Charge ($I_F = 10\text{ mA}$ to $V_R = 6.0\text{ V}, R_L = 500\ \Omega$) (Figure 2)	QS	–	45	PC
Forward Recovery Voltage ($I_F = 10\text{ mA}, t_r = 20\text{ ns}$) (Figure 3)	V_{FR}	–	1.75	V

BAS16TT1

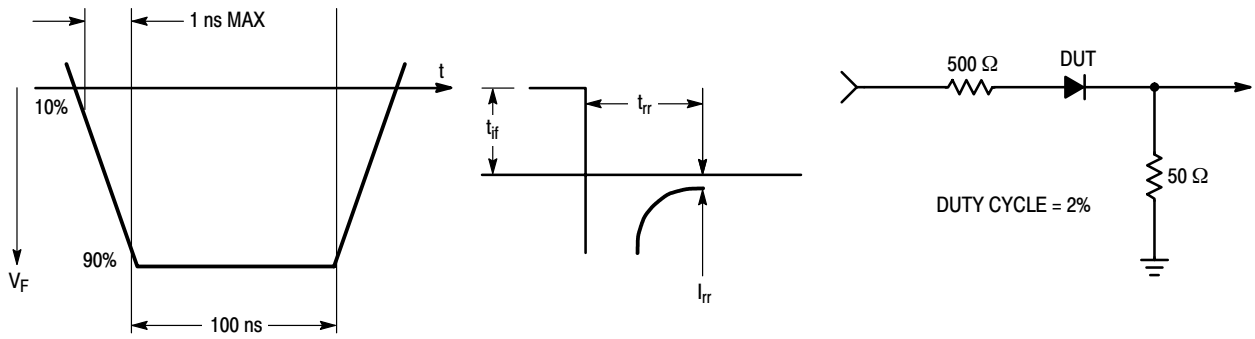


Figure 1. Reverse Recovery Time Equivalent Test Circuit

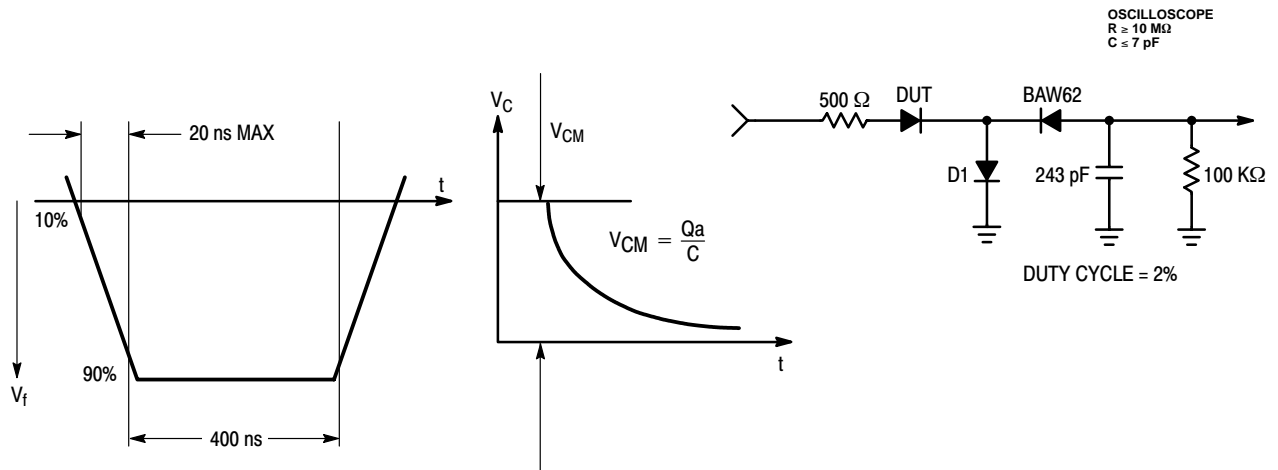


Figure 2. Stored Charge Equivalent Test Circuit

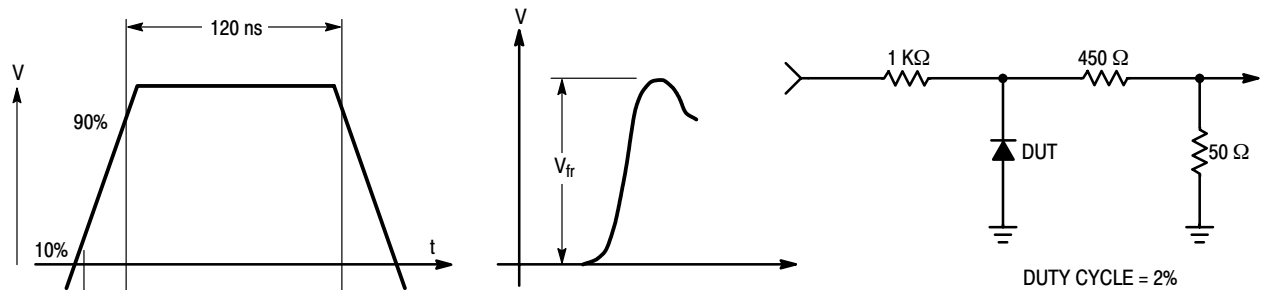


Figure 3. Forward Recovery Voltage Equivalent Test Circuit

BAS16TT1

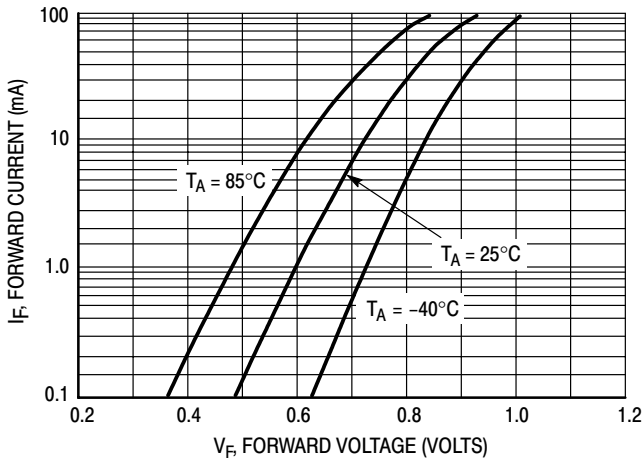


Figure 4. Forward Voltage

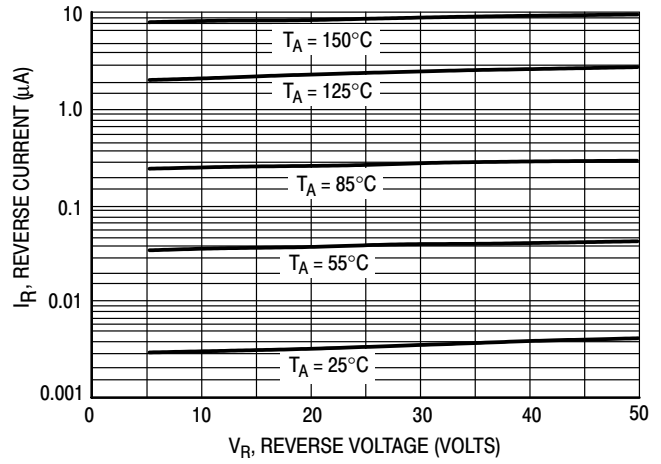


Figure 5. Leakage Current

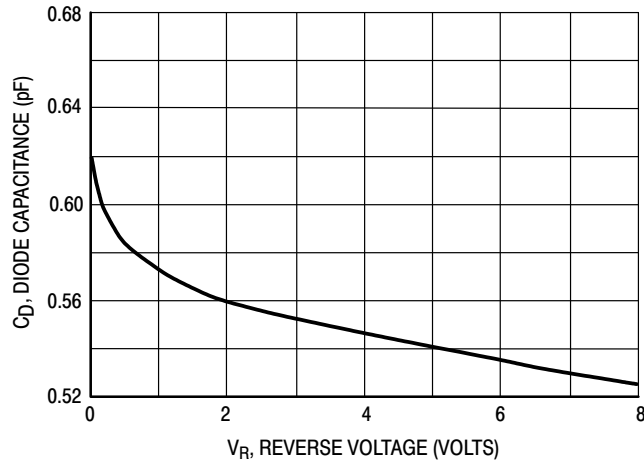


Figure 6. Capacitance

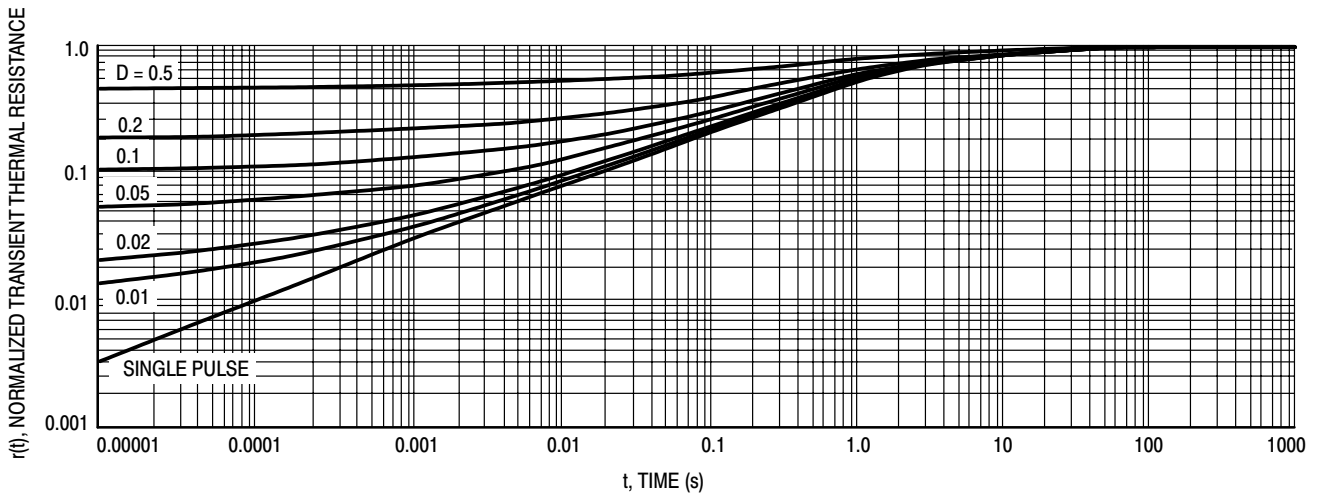
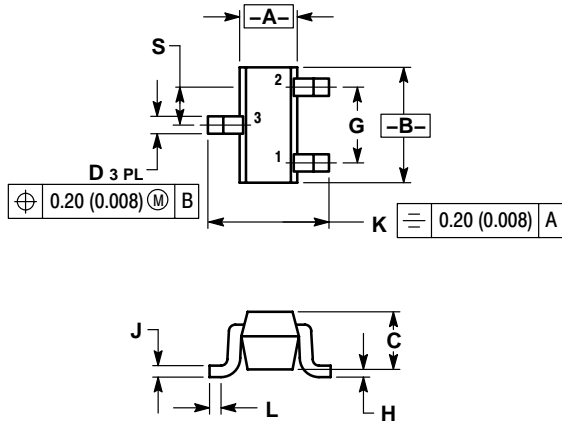


Figure 7. Normalized Thermal Response

BAS16TT1

PACKAGE DIMENSIONS

SC-416/SC-90/SOT-75
CASE 463-01
ISSUE C



NOTES:


1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
B	1.40	1.80	0.055	0.071
C	0.60	0.90	0.024	0.035
D	0.15	0.30	0.006	0.012
G	1.00 BSC		0.039 BSC	
H	---	0.10	---	0.004
J	0.10	0.25	0.004	0.010
K	1.45	1.75	0.057	0.069
L	0.10	0.20	0.004	0.008
S	0.50 BSC		0.020 BSC	

STYLE 2:

- PIN 1. ANODE
- N/C
- CATHODE

BAS16TT1

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