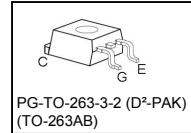
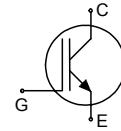


Fast IGBT in NPT-technology

- 75% lower E_{off} compared to previous generation combined with low conduction losses
- Short circuit withstand time – 10 μ s
- Designed for:
 - Motor controls
 - Inverter
- NPT-Technology for 600V applications offers:
 - very tight parameter distribution
 - high ruggedness, temperature stable behaviour
 - parallel switching capability
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	V_{CE}	I_C	$V_{CE(sat)}$	T_j	Marking	Package
SGB20N60	600V	20A	2.4V	150°C	G20N60	PG-T0-263-3-2

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CE}	600	V
DC collector current	I_C		A
$T_C = 25^\circ\text{C}$		40	
$T_C = 100^\circ\text{C}$		20	
Pulsed collector current, t_p limited by T_{jmax}	I_{Cpuls}	80	
Turn off safe operating area	-	80	
$V_{CE} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$	V_{GE}	± 20	V
Gate-emitter voltage	E_{AS}	115	mJ
Avalanche energy, single pulse $I_C = 20\text{ A}, V_{CC} = 50\text{ V}, R_{GE} = 25\Omega$, start at $T_j = 25^\circ\text{C}$			
Short circuit withstand time ² $V_{GE} = 15\text{V}, V_{CC} \leq 600\text{V}, T_j \leq 150^\circ\text{C}$	t_{SC}	10	μs
Power dissipation $T_C = 25^\circ\text{C}$	P_{tot}	179	W
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature (reflow soldering, MSL1)		245	

¹ J-STD-020 and JESD-022

² Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		0.7	K/W
Thermal resistance, junction – ambient ¹⁾	R_{thJA}		40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=500\mu\text{A}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=20\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.7 -	2 2.4	2.4 2.9	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=700\mu\text{A}, V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	- -	- -	40 2500	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}, I_C=20\text{A}$	-	14	-	S

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V},$	-	1100	1320	pF
Output capacitance	C_{oss}	$V_{GE}=0\text{V},$	-	107	128	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$	-	63	76	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}, I_C=20\text{A}$ $V_{GE}=15\text{V}$	-	100	130	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current ²⁾	$I_{C(\text{sc})}$	$V_{GE}=15\text{V}, t_{\text{sc}} \leq 10\mu\text{s}$ $V_{CC} \leq 600\text{V},$ $T_j \leq 150^\circ\text{C}$	-	200	-	A

¹⁾ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70μm thick) copper area for collector connection. PCB is vertical without blown air.

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25\text{ }^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=20\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=16\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=900\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	36	46	ns
Rise time	t_r		-	30	36	
Turn-off delay time	$t_{d(off)}$		-	225	270	
Fall time	t_f		-	54	65	
Turn-on energy	E_{on}		-	0.44	0.53	mJ
Turn-off energy	E_{off}		-	0.33	0.43	
Total switching energy	E_{ts}		-	0.77	0.96	

Switching Characteristic, Inductive Load, at $T_j=150\text{ }^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=150\text{ }^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=20\text{A}$, $V_{GE}=0/15\text{V}$, $R_G=16\Omega$, $L_\sigma^{(1)}=180\text{nH}$, $C_\sigma^{(1)}=900\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	36	46	ns
Rise time	t_r		-	30	36	
Turn-off delay time	$t_{d(off)}$		-	250	300	
Fall time	t_f		-	63	76	
Turn-on energy	E_{on}		-	0.67	0.81	mJ
Turn-off energy	E_{off}		-	0.49	0.64	
Total switching energy	E_{ts}		-	1.12	1.45	

¹⁾ Leakage inductance L_σ and Stray capacity C_σ due to dynamic test circuit in Figure E.

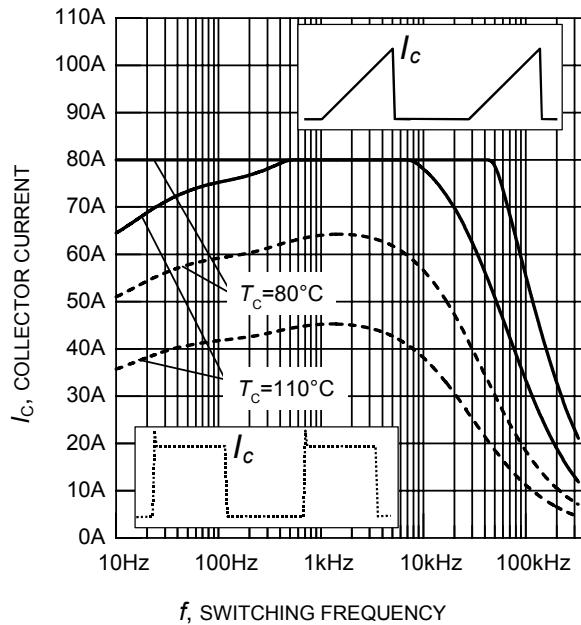


Figure 1. Collector current as a function of switching frequency

($T_j \leq 150^\circ\text{C}$, $D = 0.5$, $V_{\text{CE}} = 400\text{V}$,
 $V_{\text{GE}} = 0/+15\text{V}$, $R_G = 16\Omega$)

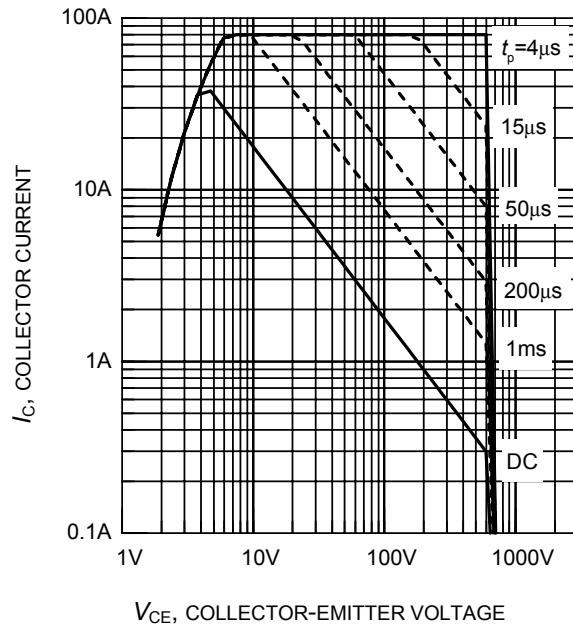


Figure 2. Safe operating area

($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 150^\circ\text{C}$)

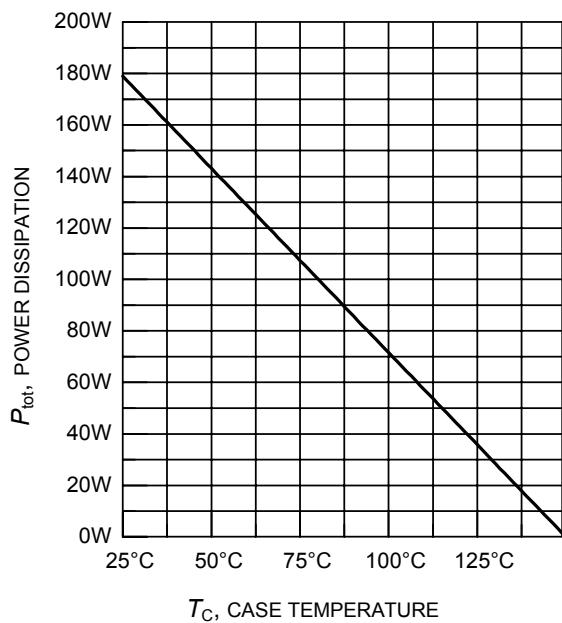


Figure 3. Power dissipation as a function of case temperature

($T_j \leq 150^\circ\text{C}$)

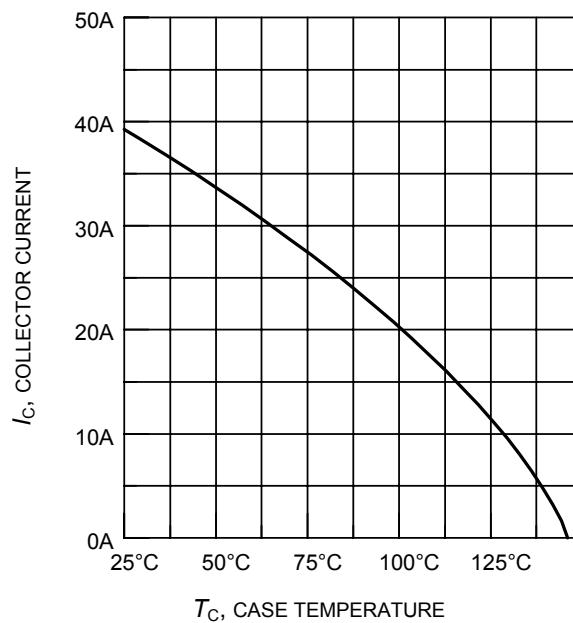
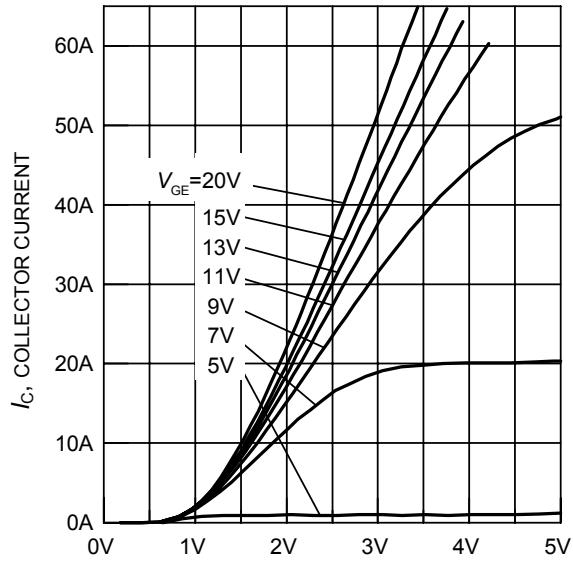


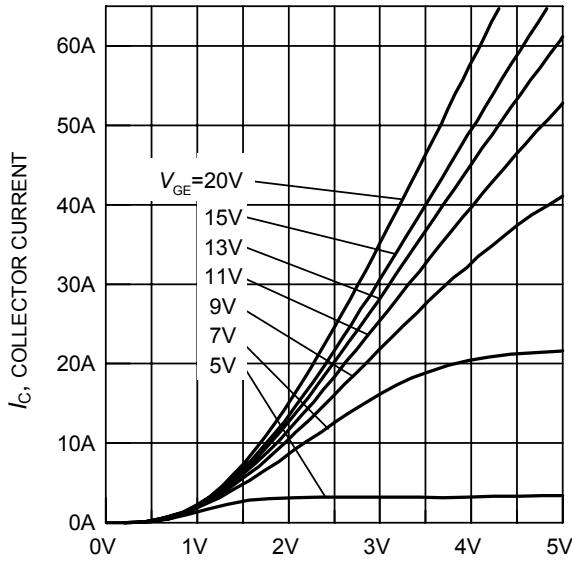
Figure 4. Collector current as a function of case temperature

($V_{\text{GE}} \leq 15\text{V}$, $T_j \leq 150^\circ\text{C}$)



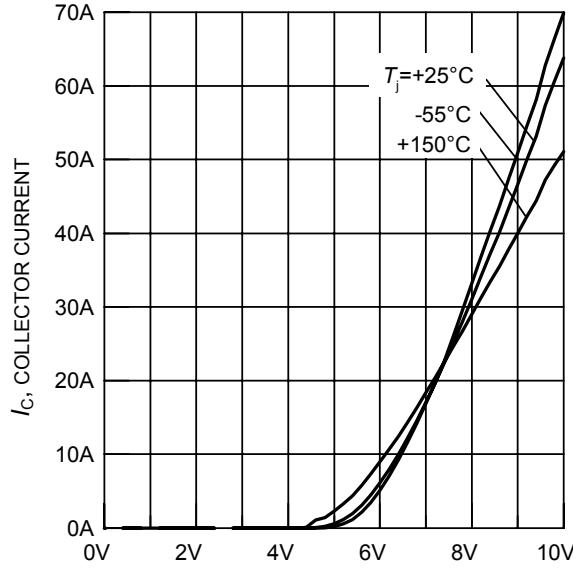
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 5. Typical output characteristics
($T_j = 25^\circ\text{C}$)



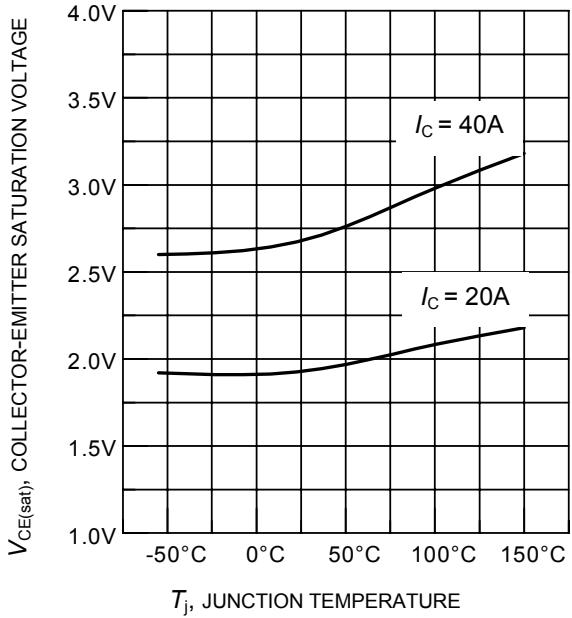
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 6. Typical output characteristics
($T_j = 150^\circ\text{C}$)



V_{GE} , GATE-EMITTER VOLTAGE

Figure 7. Typical transfer characteristics
($V_{CE} = 10\text{V}$)



T_j , JUNCTION TEMPERATURE

Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)

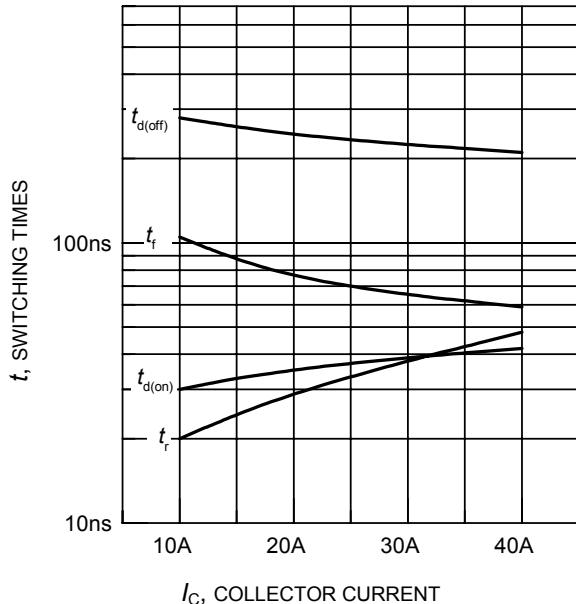


Figure 9. Typical switching times as a function of collector current

(inductive load, $T_j = 150^\circ\text{C}$, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/+15\text{V}$, $R_G = 16\Omega$, Dynamic test circuit in Figure E)

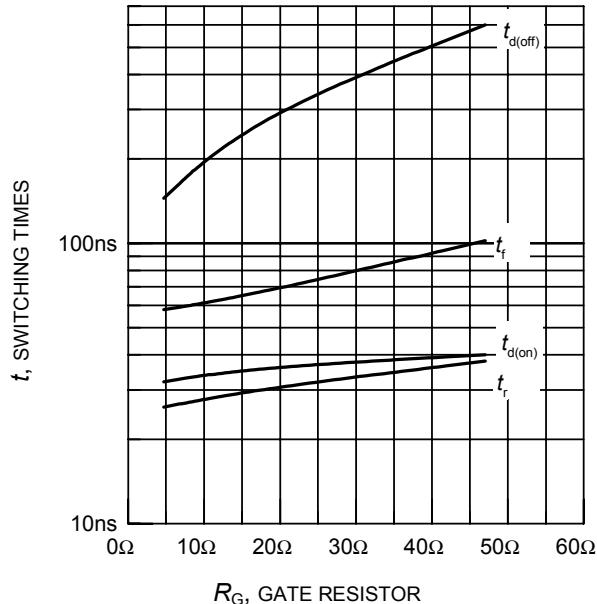


Figure 10. Typical switching times as a function of gate resistor

(inductive load, $T_j = 150^\circ\text{C}$, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/+15\text{V}$, $I_C = 20\text{A}$, Dynamic test circuit in Figure E)

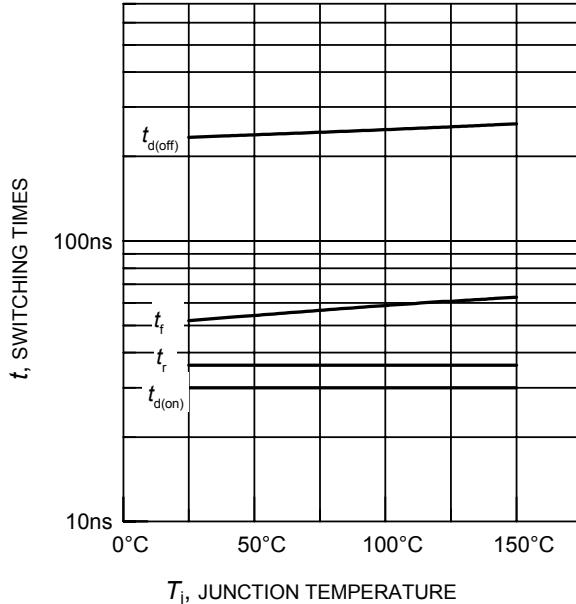


Figure 11. Typical switching times as a function of junction temperature

(inductive load, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/+15\text{V}$, $I_C = 20\text{A}$, $R_G = 16\Omega$, Dynamic test circuit in Figure E)

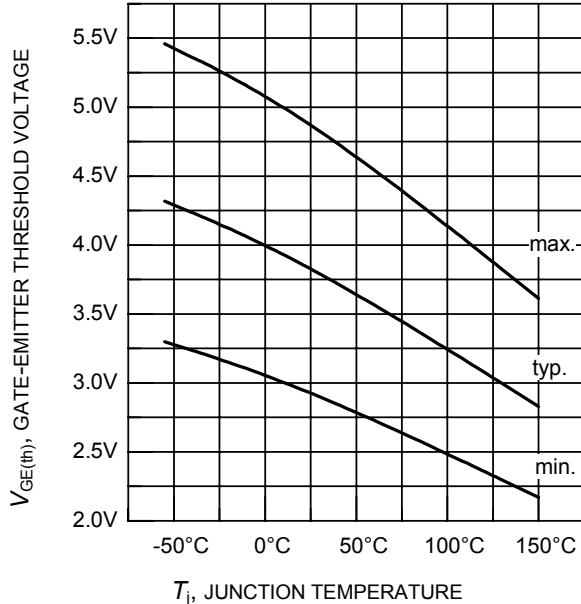


Figure 12. Gate-emitter threshold voltage as a function of junction temperature

($I_C = 0.7\text{mA}$)

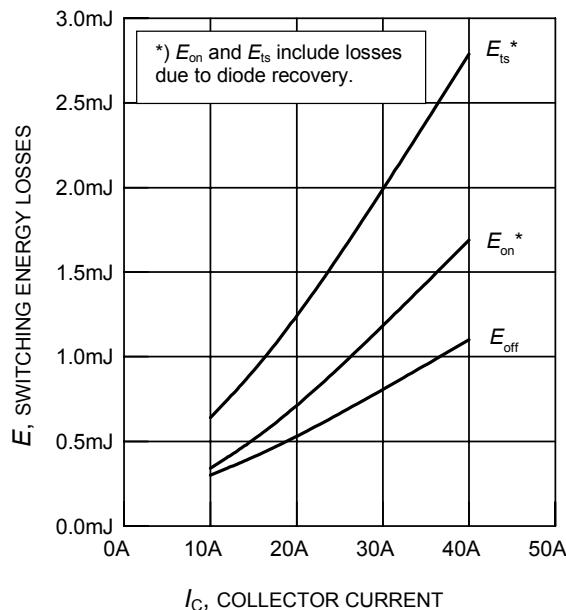


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, $T_j = 150^\circ\text{C}$, $V_{\text{CE}} = 400\text{V}$,
 $V_{\text{GE}} = 0/+15\text{V}$, $R_G = 16\Omega$,
Dynamic test circuit in Figure E)

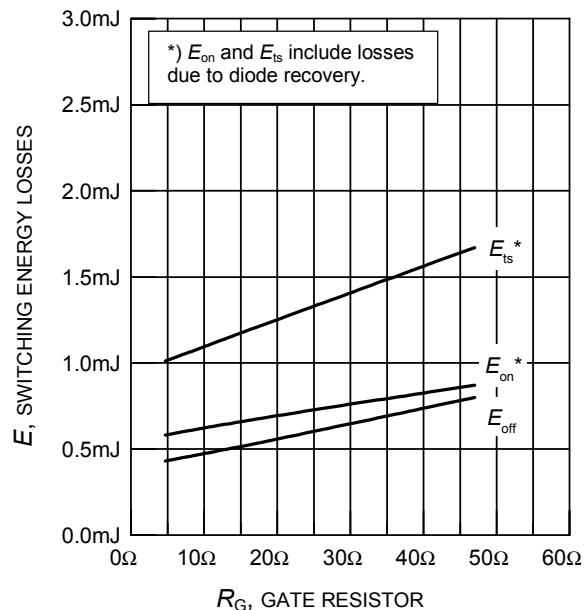


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, $T_j = 150^\circ\text{C}$, $V_{\text{CE}} = 400\text{V}$,
 $V_{\text{GE}} = 0/+15\text{V}$, $I_C = 20\text{A}$,
Dynamic test circuit in Figure E)

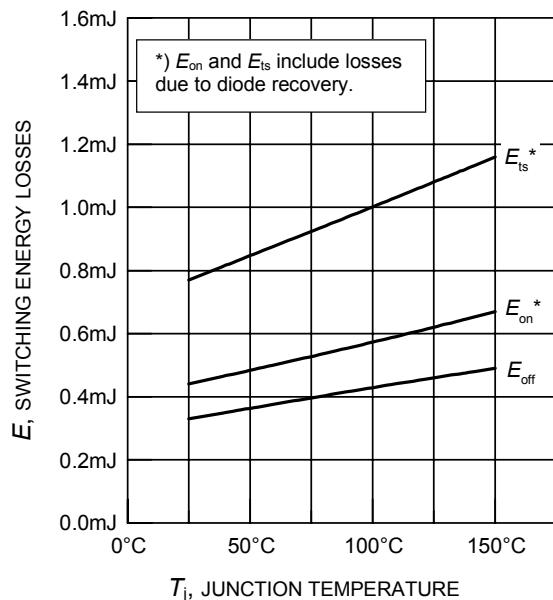


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/+15\text{V}$,
 $I_C = 20\text{A}$, $R_G = 16\Omega$,
Dynamic test circuit in Figure E)

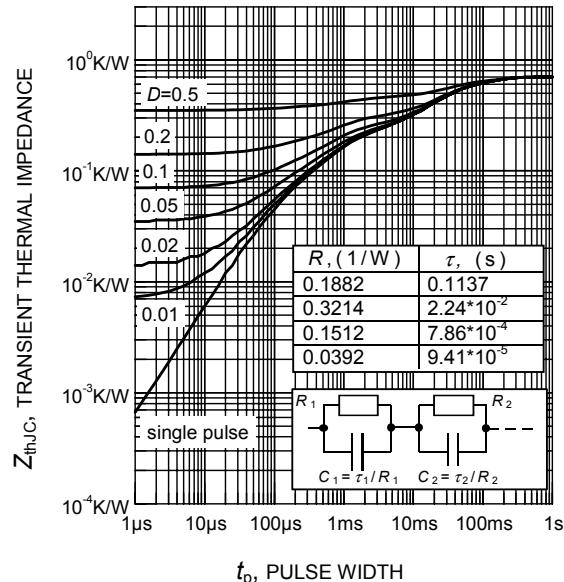


Figure 16. IGBT transient thermal impedance as a function of pulse width
($D = t_p / T$)

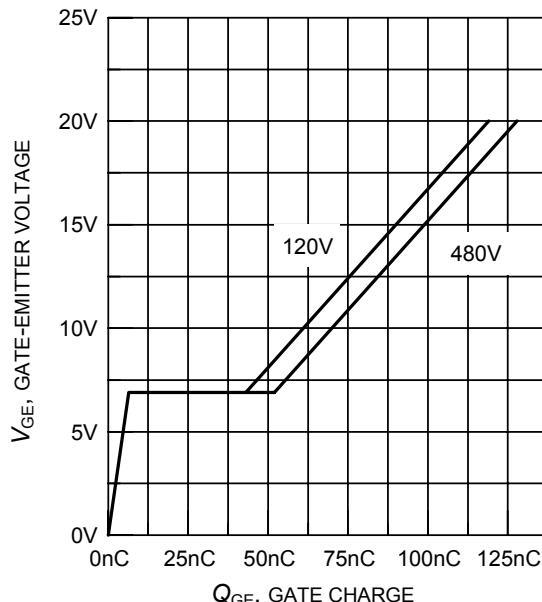


Figure 17. Typical gate charge
($I_C = 20A$)

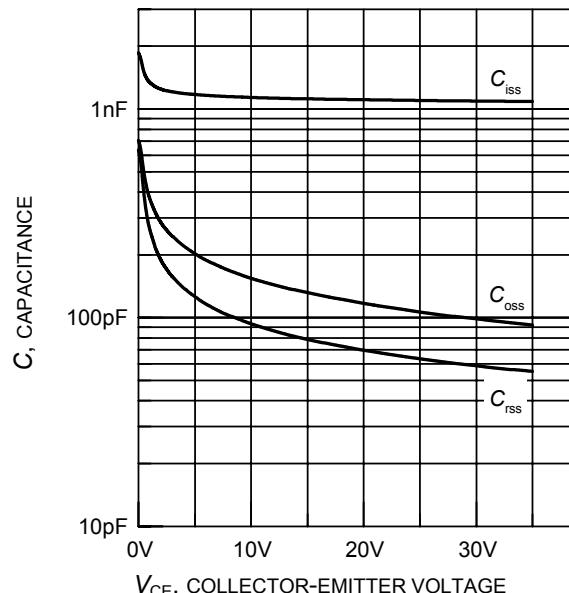


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE} = 0V, f = 1MHz$)

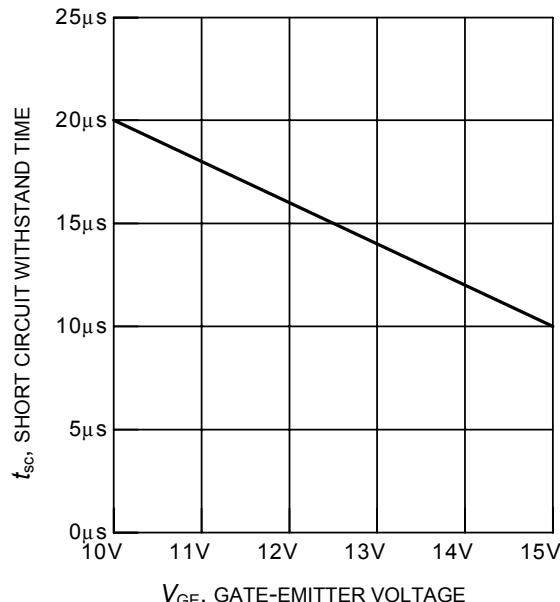


Figure 19. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE} = 600V$, start at $T_j = 25^{\circ}C$)

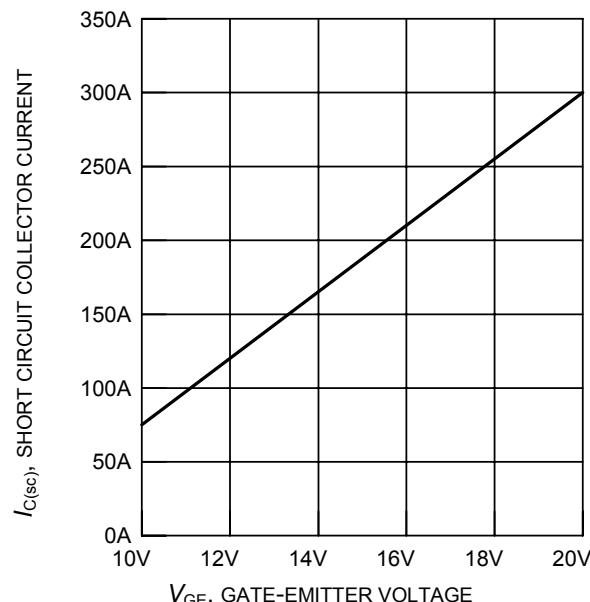
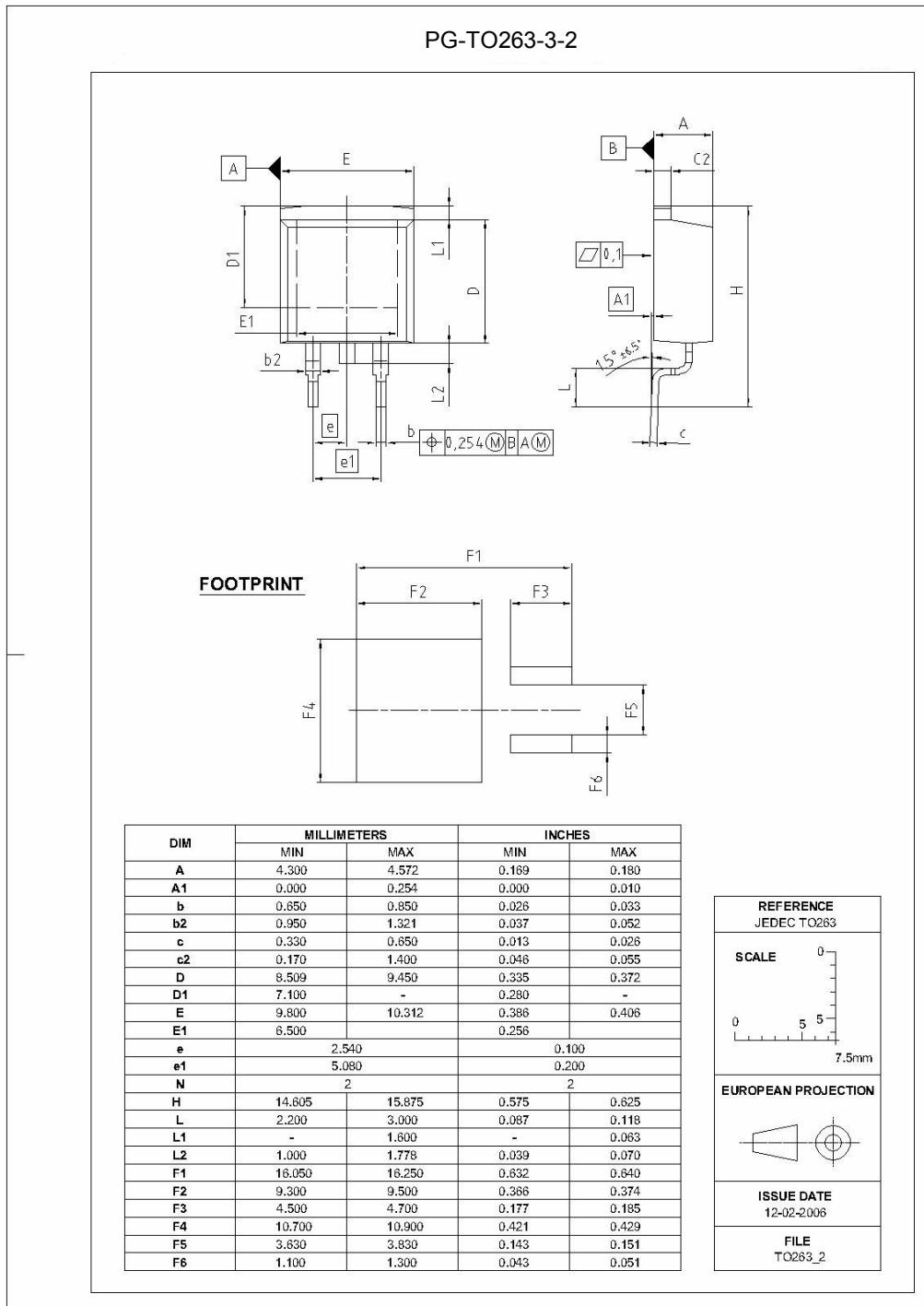
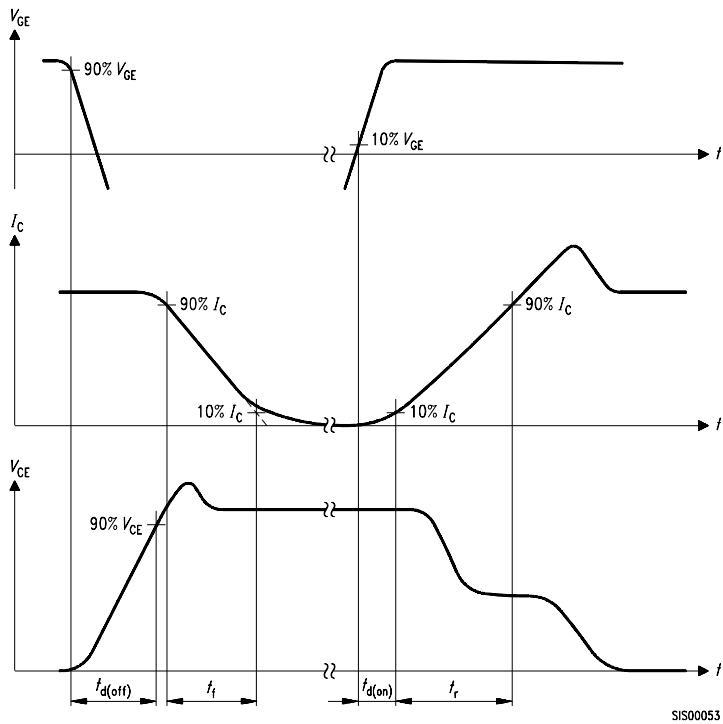
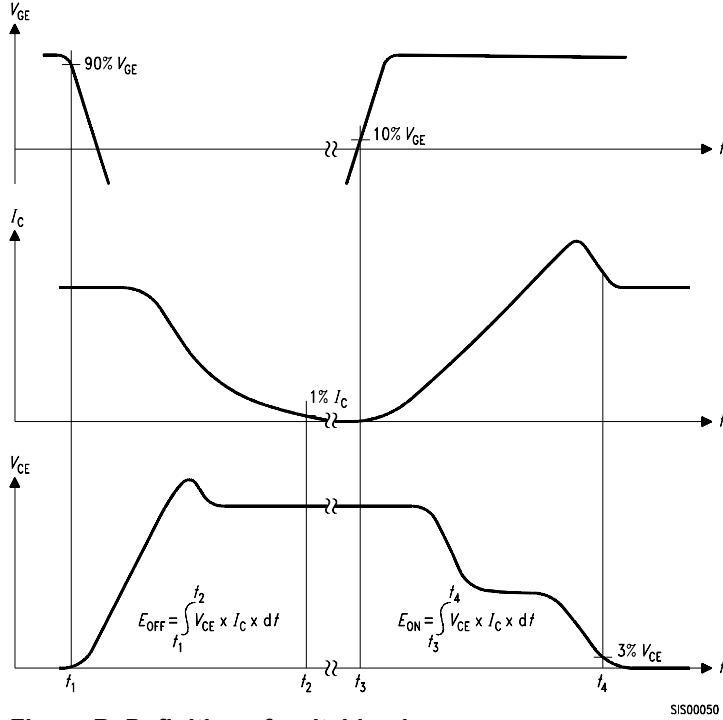
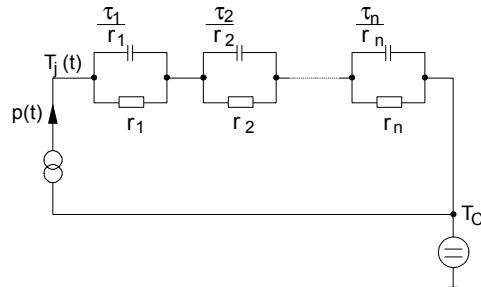
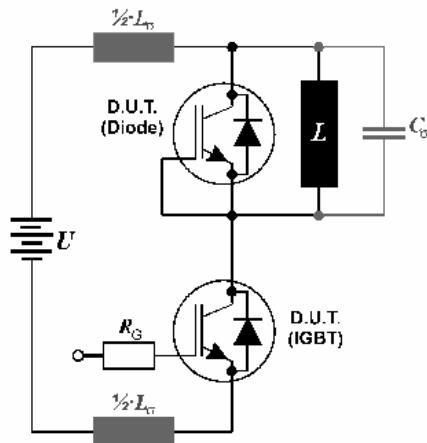


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 600V, T_j = 150^{\circ}C$)




Figure A. Definition of switching times

Figure B. Definition of switching losses

Figure D. Thermal equivalent circuit

Figure E. Dynamic test circuit
 Leakage inductance $L_\sigma = 180\text{nH}$ and Stray capacity $C_\sigma = 900\text{pF}$.



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