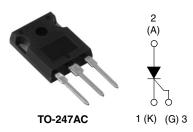


Vishay High Power Products

Phase Control SCR, 35 A



PRODUCT SUMMARY				
V _T at 40 A	< 1.45 V			
I _{TSM}	500 A			
V _{RRM}	800/1200 V			

DESCRIPTION/FEATURES

The 40TPS...A High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature. Low Igt parts available.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	35	А		
I _{RMS}		55	A		
V _{RRM} /V _{DRM}		800/1200	V		
I _{TSM}		500	А		
V _T	40 A, T _J = 25 °C	1.45	V		
dV/dt		1000	V/µs		
dl/dt		100	A/µs		
TJ		- 40 to 125	°C		

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} ∕I _{DRM} AT 125 °C mA		
40TPS08A	800	900			
40TPS12A	1200	1300	10		
40TPS08	800	900	10		
40TPS12	1200	1300			

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average on-state current	I _{T(AV)}	T _C = 79 °C, 180° c	conduction half sine w	vave	35	
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}			55	A	
Maximum peak, one-cycle	I	10 ms sine pulse,	rated V_{RRM} applied		500	
non-repetitive surge current	I _{TSM}	10 ms sine pulse, i	no voltage reapplied		600	
Maximum I ² t for fusing	l ² t	10 ms sine pulse,	rated V _{RRM} applied	Initial T _J = T _J maximum	1250	A ² s
Maximum i-t for fusing	1-1	10 ms sine pulse, i	no voltage reapplied	1 J Maximani	1760	
Maximum I ² √t for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied		12 500	A²√s	
Low level value of threshold voltage	V _{T(TO)1}	T ₁ = 125 °C			1.02	V
High level value of threshold voltage	V _{T(TO)2}				1.23	v
Low level value of on-state slope resistance	r _{t1}	- 1j = 125 °C		9.74		
High level value of on-state slope resistance	r _{t2}			7.50	mΩ	
Maximum peak on-state voltage	V _{TM}	110 A, T _J = 25 °C		1.85	V	
Maximum rate of rise of turned-on current	dl/dt	T _J = 25 °C		100	A/µs	
Maximum holding current	Ι _Η				150	
Maximum latching current	١L				300	4
	I _{RRM/} I _{DRM}	T _J = 25 °C	$V_{\rm R}$ = Rated $V_{\rm RRM}/V_{\rm DRM}$		0.5	mA
Maximum reverse and direct leakage current		T _J = 125 °C			10	
Maximum rate of rise of off-state voltage 40TPS08	d\//dt	$T_J = T_J$ maximum, linear to 80 % V_{DRM} , R_g -k = Open		500	V/µs	
Maximum rate of rise of off-state voltage 40TPS12	dV/dt			1000	V/µs	

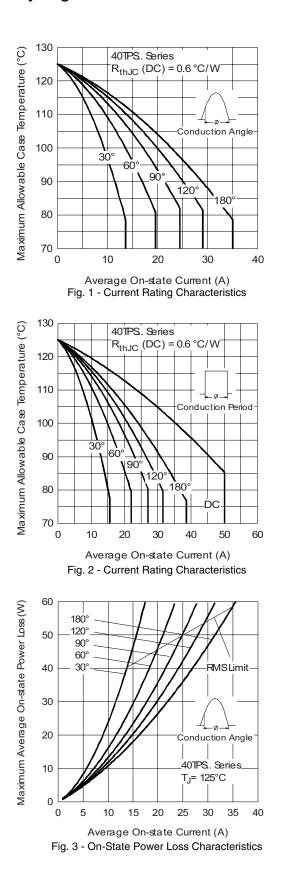
TRIGGERING					
PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P _{GM}			10	W
Maximum average gate power	P _{G(AV)}			2.5	vv
Maximum peak gate current	I _{GM}			2.5	А
Maximum peak negative gate voltage	- V _{GM}			10	V
Maximum required DC gate voltage to trigger	V _{GT}	T _J = - 40 °C		4.0	V
		T _J = 25 °C	Anode supply = 6 V resistive load	2.5	
		T _J = 125 °C		1.7	
	I _{GT}	T _J = - 40 °C		270	
Maximum required DC gate current to trigger		T _J = 25 °C		150	~ ^
		T _J = 125 °C		80 mA	
		$T_J = 25 \text{ °C}$, for 40TPS08A and 40TPS12A		40	
Maximum DC gate voltage not to trigger	V _{GD}	T _J = 125 °C, V _{DRM} = Rated value		0.25	V
Maximum DC gate current not to trigger	I _{GD}			6	mA

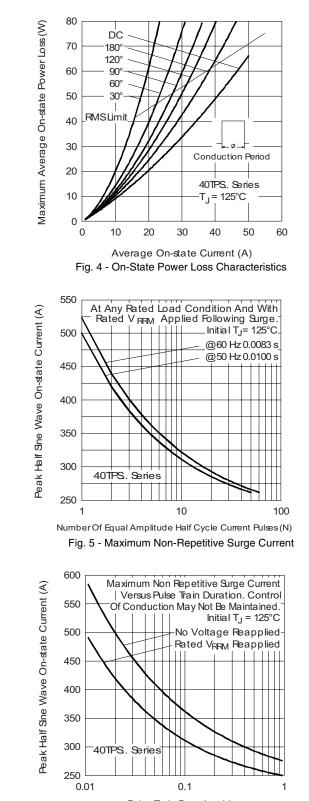


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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and sto temperature range	rage	T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case Maximum thermal resistance, junction to ambient Maximum thermal resistance, case to heatsink		R _{thJC}		0.6	
		R _{thJA}	- DC operation	40	°C/W
		R _{thCS}	Mounting surface, smooth and greased	0.2	
A many state to include				6	g
Approximate weight	Approximate weight			0.21	0Z.
Mounting torque minimum maximum				6 (5)	kgf ⋅ cm
				12 (10)	(lbf · in)
Marking device				40TPS08A	
				40TPS12A	
			Case style TO-247AC	40TPS08	
				40TPS12	

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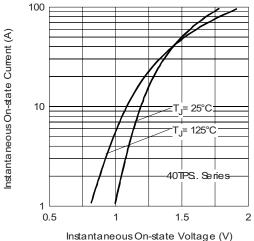


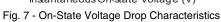


Pulse Train Duration (s) Fig. 6 - Maximum Non-Repetitive Surge Current



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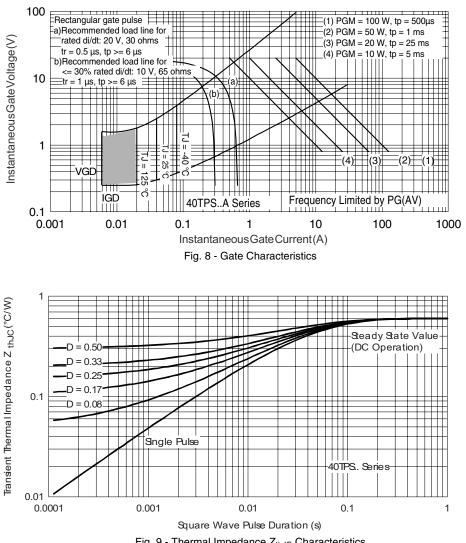


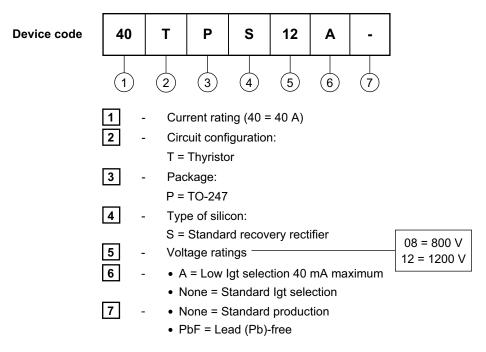
Fig. 9 - Thermal Impedance ZthJC Characteristics

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95223				
Part marking information	http://www.vishay.com/doc?95226			



Vishay

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