Preferred Device

General Purpose Transistors

PNP Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	-60	Vdc
Collector-Base Voltage	V _{CBO}	-60	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ι _C	-600	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

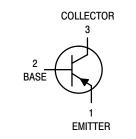
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

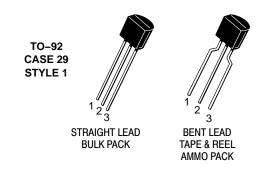
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



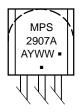
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MARKING DIAGRAM



A = Assembly Location Y = Year WW = Work Week = Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

© Semiconductor Components Industries, LLC, 2007 April, 2007 – Rev. 4 Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS			•		
Collector - Emitter Breakdown Voltage	V _{(BR)CEO}	-60	-	Vdc	
Collector - Base Breakdown Voltage (I	$_{\rm C} = -10 \ \mu {\rm Adc}, \ {\rm I}_{\rm E} = 0)$	V _{(BR)CBO}	-60	_	Vdc
Emitter-Base Breakdown Voltage (IE :	= –10 μAdc, I _C = 0)	V _{(BR)EBO}	-5.0	_	Vdc
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc)		I _{CEX}	-	-50	nAdc
Collector Cutoff Current $(V_{CB} = -50 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -50 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$		I _{CBO}		-0.01 -10	μAdc
Base Current (V _{CE} = -30 Vdc, V _{EB(off)}	= -0.5 Vdc)	IB	-	-50	nAdc
ON CHARACTERISTICS				•	•
$ \begin{array}{l} \mbox{DC Current Gain} \\ (I_C = -0.1 \mbox{ mAdc}, V_{CE} = -10 \mbox{ Vdc}) \\ (I_C = -1.0 \mbox{ mAdc}, V_{CE} = -10 \mbox{ Vdc}) \\ (I_C = -10 \mbox{ mAdc}, V_{CE} = -10 \mbox{ Vdc}) \\ (I_C = -150 \mbox{ mAdc}, V_{CE} = -10 \mbox{ Vdc}) \mbox{ (Note 1)} \\ (I_C = -500 \mbox{ mAdc}, V_{CE} = -10 \mbox{ Vdc}) \mbox{ (Note 1)} \\ \end{array} $		h _{FE}	75 100 100 100 50	- - 300 -	_
$\begin{array}{l} \mbox{Collector}-\mbox{Emitter Saturation Voltage (}\\ \mbox{(}I_C=-150\mbox{ mAdc},I_B=-15\mbox{ mAdc})\\ \mbox{(}I_C=-500\mbox{ mAdc},I_B=-50\mbox{ mAdc}) \end{array}$	Note 1)	V _{CE(sat)}		-0.4 -1.6	Vdc
Base – Emitter Saturation Voltage (Note 1) ($I_C = -150 \text{ mAdc}$, $I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$)		V _{BE(sat)}		-1.3 -2.6	Vdc
SMALL-SIGNAL CHARACTERISTIC	S		•	ł	
Current–Gain – Bandwidth Product (Notes 1 and 2), (I _C = –50 mAdc, V _{CE} = –20 Vdc, f = 100 MHz)		f _T	200	-	MHz
Output Capacitance ($V_{CB} = -10$ Vdc, $I_E = 0$, f = 1.0 MHz)		C _{obo}	-	8.0	pF
Input Capacitance ($V_{EB} = -2.0 \text{ Vdc}$, $I_C = 0$, f = 1.0 MHz)		C _{ibo}	-	30	pF
SWITCHING CHARACTERISTICS					•
Turn–On Time	$(V_{CC} = -30 \text{ Vdc}, I_C = -150 \text{ mAdc},$	t _{on}	-	45	ns
Delay Time	$I_{B1} = -15 \text{ mAdc}$ (Figures 1 and 5)	t _d	-	10	ns
Rise Time	1	tr	-	40	ns
Turn–Off Time	$(V_{CC} = -6.0 \text{ Vdc}, I_C = -150 \text{ mAdc},$	t _{off}	-	100	ns
Storage Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$ (Figure 2)	ts	-	80	ns
Fall Time	1	t _f	_	30	ns

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2%. 2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.

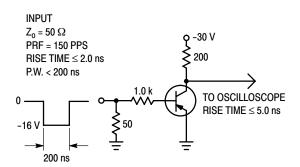
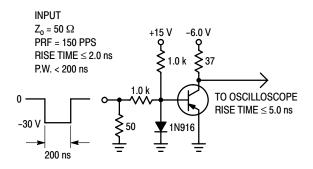
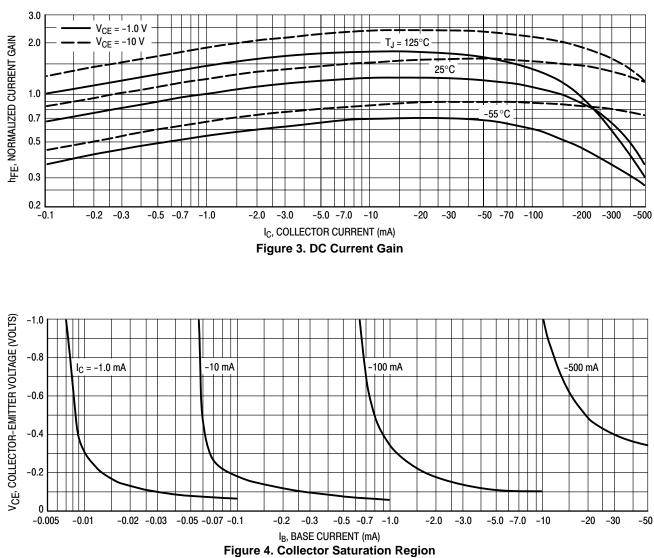


Figure 1. Delay and Rise Time Test Circuit





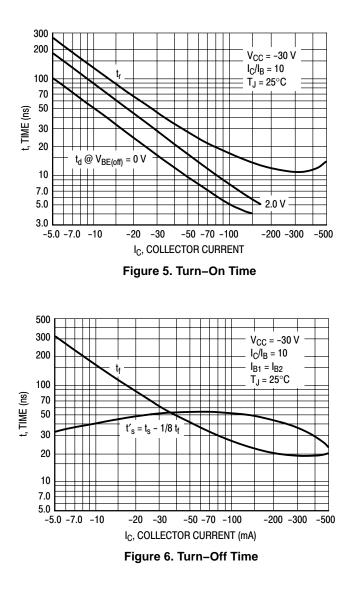


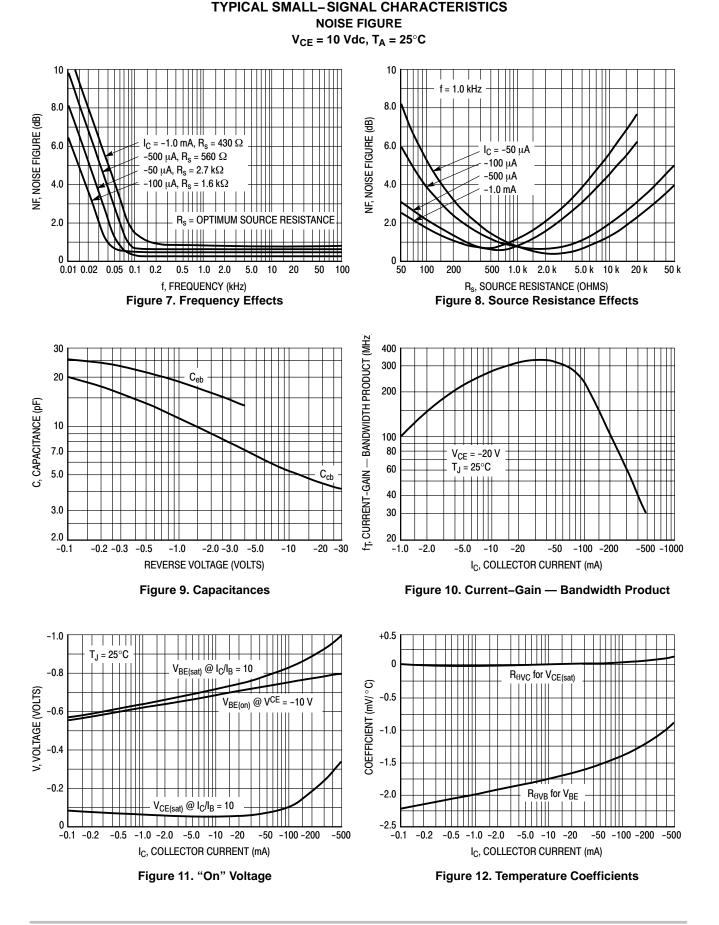
TYPICAL CHARACTERISTICS

ORDERING INFORMATION

Device	Package	Shipping [†]	
MPS2907A	TO-92	5000 Units / Bulk	
MPS2907AG	TO-92 (Pb-Free)		
MPS2907ARLG	TO-92 (Pb-Free)	2000 / Tape & Reel	
MPS2907ARLRA	TO-92		
MPS2907ARLRAG	TO-92 (Pb-Free)		
MPS2907ARLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

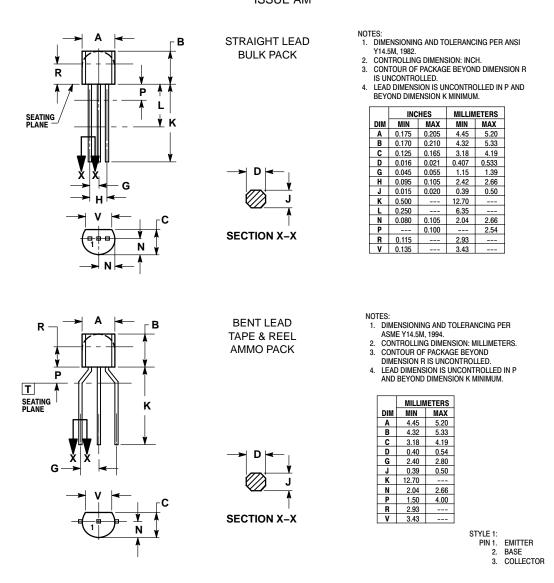




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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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