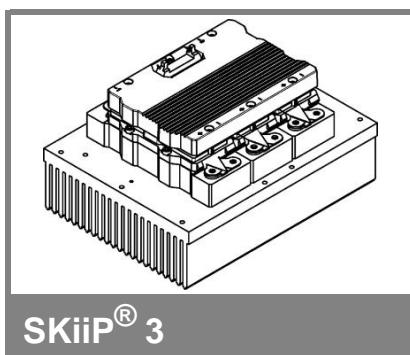




our personal.



## 6-pack-integrated intelligent Power System

### 6-pack integrated gate driver SKiiP 613GD123-3DUL

Data

#### Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformer
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

<b>Absolute Maximum Ratings</b>		$T_a = 25^\circ\text{C}$ unless otherwise specified	
<b>Symbol</b>	<b>Conditions</b>	<b>Values</b>	<b>Units</b>
$V_{S2}$	unstabilized 24 V power supply	30	V
$V_i$	input signal voltage (high)	15 + 0,3	V
$dv/dt$	secondary to primary side	75	kV/μs
$V_{isolIO}$	input / output (AC, rms, 2 s)	3000	V
$V_{isolPD}$	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$	1170	V
$V_{isol12}$	output 1 / output 2 (AC, rms, 2 s)	1500	V
$f_{sw}$	switching frequency	15	kHz
$f_{out}$	output frequency for $I_{peak(1)}=I_C$	15	kHz
$T_{op} (T_{stg})$	operating / storage temperature	- 40 ... + 85	°C

<b>Characteristics</b> ( $T_a = 25^\circ\text{C}$ )						
<b>Symbol</b>	<b>Conditions</b>	<b>min.</b>	<b>typ.</b>	<b>max.</b>	<b>Units</b>	
$V_{S2}$	supply voltage non stabilized	13	24	30	V	
$I_{S2}$	$V_{S2} = 24 \text{ V}$	$365+37*f/\text{kHz}+0,00111*(I_{AC}/\text{A})^2$			mA	
$V_{IT+}$	input threshold voltage (High)	12,3			V	
$V_{IT-}$	input threshold voltage (Low)	4,6			V	
$R_{IN}$	input resistance	10			kΩ	
$C_{IN}$	input capacitance	1			nF	
$t_{d(on)IO}$	input-output turn-on propagation time	1,3			μs	
$t_{d(off)IO}$	input-output turn-off propagation time	1,3			μs	
$t_{pERRRESET}$	error memory reset time	9			μs	
$t_{TD}$	top / bottom switch interlock time	3			μs	
$I_{analogOUT}$	max. 5 mA; 8 V corresponds to 15 V supply voltage for external components	600			A	
$I_{s1out}$	max. load current	50			mA	
$I_{TRIPSC}$	over current trip level ( $I_{analog OUT} = 10 \text{ V}$ )	750			A	
$T_{tp}$	over temperature protection	110	120			°C
$U_{DCTRIP}$	$U_{DC}$ -protection ( $U_{analog OUT} = 9 \text{ V}$ ); (option for GB types)	900			V	

For electrical and thermal design support please use SEMISEL.

Access to SEMISEL is via SEMIKRON website <http://www.semikron.com>.

\* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our personal.

