

# AP09N70R-A-HF

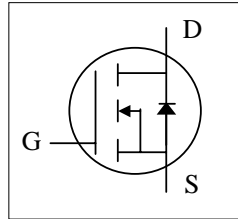
**Halogen-Free Product**



N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

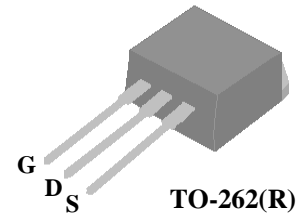
- ▼ 100% Avalanche Test
- ▼ Fast Switching
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant



$BV_{DSS}$	650V
$R_{DS(ON)}$	0.75 $\Omega$
$I_D$	9A

## Description

AP09N70 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications. TO-262 type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching, ruggedized design and cost-effectiveness.



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	9	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	5	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	40	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	156	W
	Linear Derating Factor	1.25	W/ $^\circ C$
$E_{AS}$	Single Pulse Avalanche Energy <sup>2</sup>	305	mJ
$I_{AR}$	Avalanche Current	9	A
$E_{AR}$	Repetitive Avalanche Energy	9	mJ
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	0.8	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	62	$^\circ C/W$

Data & specifications subject to change without notice

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**Electrical Characteristics @T<sub>j</sub>=25°C (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	650	-	-	V
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA	-	0.6	-	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>3</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A	-	-	0.75	Ω
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2	-	4	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =4.5A	-	4.5	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V	-	-	10	uA
	Drain-Source Leakage Current (T <sub>j</sub> =125°C)	V <sub>DS</sub> =520V, V <sub>GS</sub> =0V	-	-	500	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	-	-	±100	nA
Q <sub>g</sub>	Total Gate Charge <sup>3</sup>	I <sub>D</sub> =9A	-	44	-	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =480V	-	11	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =10V	-	12	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time <sup>3</sup>	V <sub>DD</sub> =300V	-	19	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =9A	-	21	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =10Ω, V <sub>GS</sub> =10V	-	56	-	ns
t <sub>f</sub>	Fall Time	R <sub>D</sub> =34Ω	-	24	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	2660	-	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V	-	170	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	10	-	pF

**Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I <sub>S</sub>	Continuous Source Current ( Body Diode )	V <sub>D</sub> =V <sub>G</sub> =0V, V <sub>S</sub> =1.5V	-	-	9	A
I <sub>SM</sub>	Pulsed Source Current ( Body Diode ) <sup>1</sup>		-	-	40	A
V <sub>SD</sub>	Forward On Voltage <sup>3</sup>	T <sub>j</sub> =25°C, I <sub>S</sub> =9A, V <sub>GS</sub> =0V	-	-	1.5	V

**Notes:**

1. Pulse width limited by Max. junction temperature
2. Starting T<sub>j</sub>=25°C, V<sub>DD</sub>=50V, L=6.8mH, R<sub>G</sub>=25Ω, I<sub>AS</sub>=9A.
3. Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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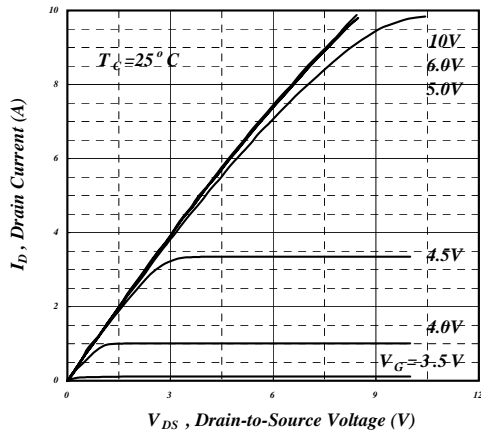


Fig 1. Typical Output Characteristics

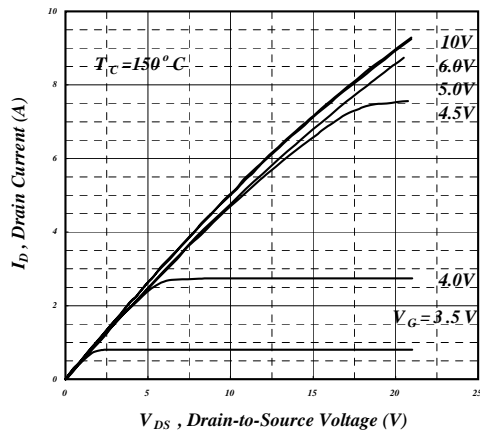


Fig 2. Typical Output Characteristics

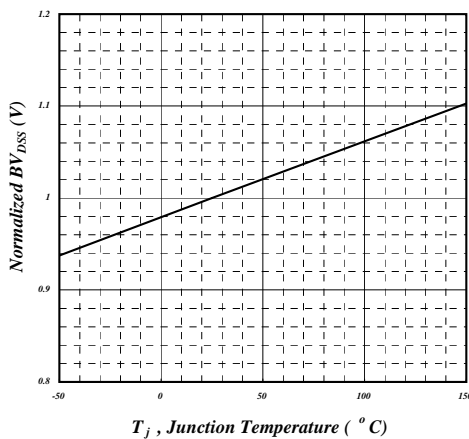


Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature

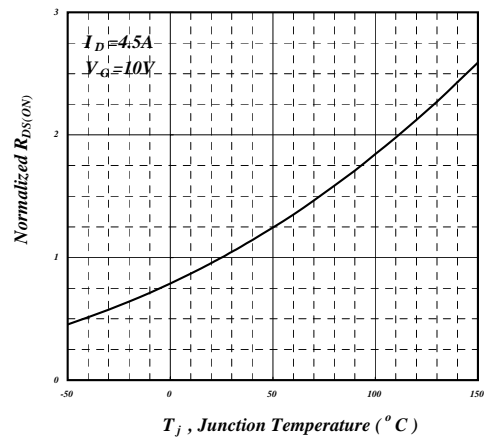


Fig 4. Normalized On-Resistance v.s. Junction Temperature

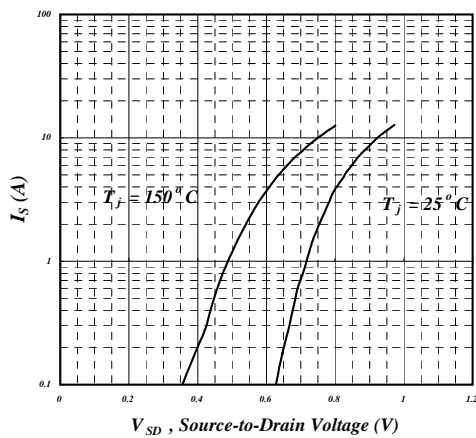


Fig 5. Forward Characteristic of Reverse Diode

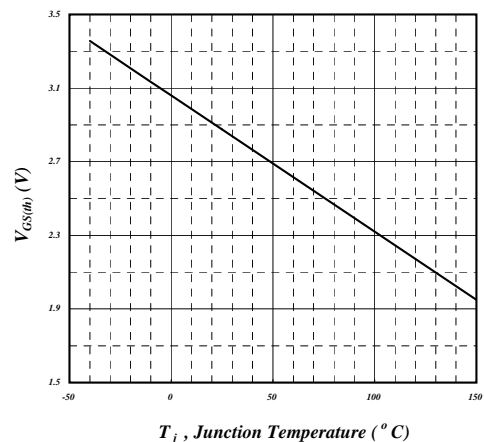


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



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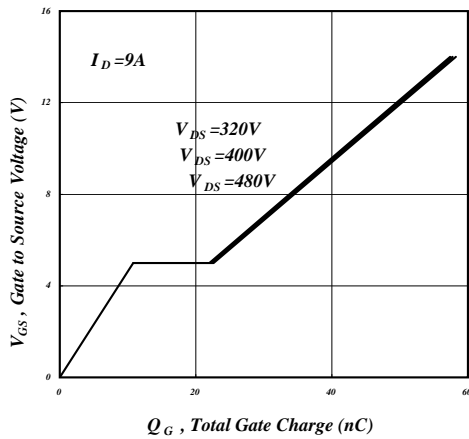


Fig 7. Gate Charge Characteristics

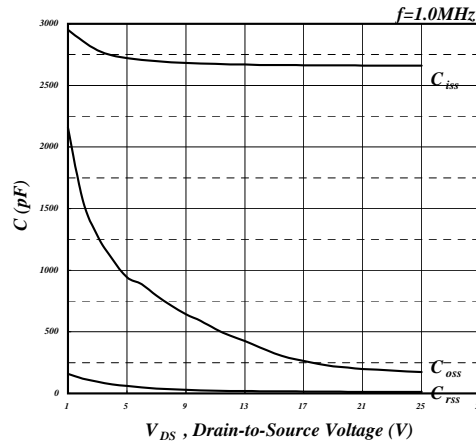


Fig 8. Typical Capacitance Characteristics

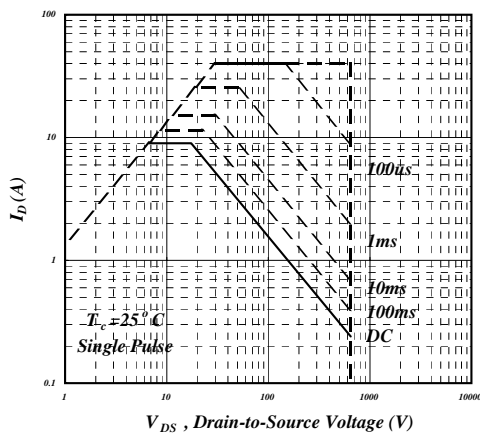


Fig 9. Maximum Safe Operating Area

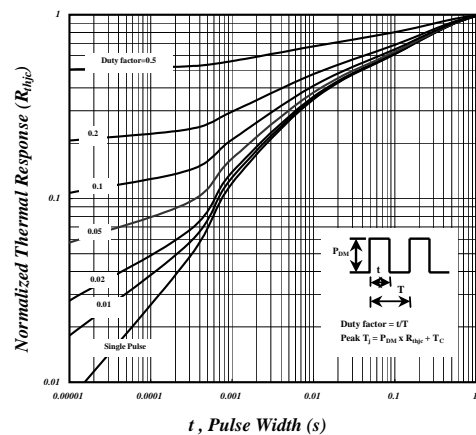


Fig 10. Effective Transient Thermal Impedance

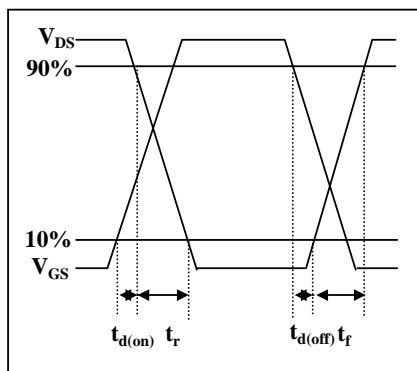


Fig 11. Switching Time Waveform

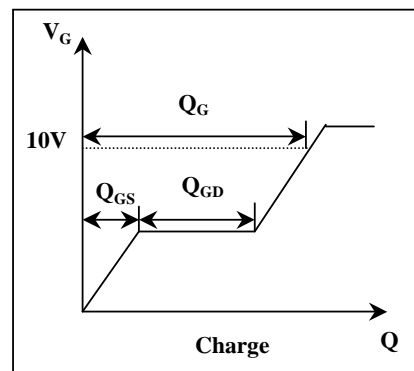


Fig 12. Gate Charge Waveform