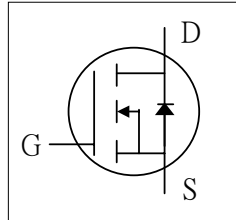
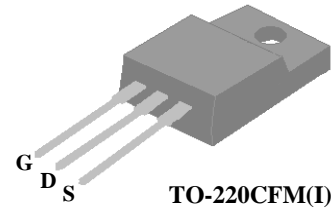




- ▼ 100% Avalanche Test
- ▼ Fast Switching
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant



$BV_{DSS}$	650V
$R_{DS(ON)}$	3.6 $\Omega$
$I_D$	3.3A



### Description

AP03N70 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications.

TO-220CFM type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching, ruggedized design and cost-effectiveness.

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	650	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	3.3	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	2.1	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	10	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	29	W
	Linear Derating Factor	0.23	W/ $^\circ C$
$E_{AS}$	Single Pulse Avalanche Energy <sup>2</sup>	67	mJ
$I_{AR}$	Avalanche Current	3	A
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	4.3	$^\circ C/W$
Rthj-a	Maximum Thermal Resistance, Junction-ambient	65	$^\circ C/W$



## Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=1mA$	650	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1mA$	-	0.6	-	$V/^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10V, I_D=1.6A$	-	-	3.6	$\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	-	4	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=1.6A$	-	2	-	S
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=600V, V_{GS}=0V$	-	-	10	$\mu A$
	Drain-Source Leakage Current ( $T_j=125^\circ\text{C}$ )	$V_{DS}=480V, V_{GS}=0V$	-	-	500	$\mu A$
$I_{GSS}$	Gate-Source Leakage	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge <sup>3</sup>	$I_D=3A$	-	12	20	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=480V$	-	3	-	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	5	-	nC
$t_{d(on)}$	Turn-on Delay Time <sup>3</sup>	$V_{DD}=300V$	-	9	-	ns
$t_r$	Rise Time	$I_D=3A$	-	5	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=10\Omega$	-	18	-	ns
$t_f$	Fall Time	$V_{GS}=10V$	-	6	-	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0V$	-	600	960	pF
$C_{oss}$	Output Capacitance	$V_{DS}=25V$	-	45	-	pF
$C_{rss}$	Reverse Transfer Capacitance	$f=1.0MHz$	-	4	-	pF

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{SD}$	Forward On Voltage <sup>3</sup>	$I_S=3A, V_{GS}=0V$	-	-	1.5	V
$t_{rr}$	Reverse Recovery Time <sup>3</sup>	$I_S=3A, V_{GS}=0V,$	-	422	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	2580	-	nC

### Notes:

1. Pulse width limited by Max. junction temperature
2. Starting  $T_j=25^\circ\text{C}$ ,  $V_{DD}=50V$ ,  $L=15mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=3A$ .
3. Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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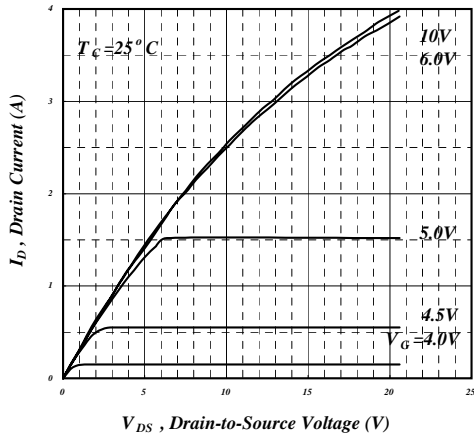


Fig 1. Typical Output Characteristics

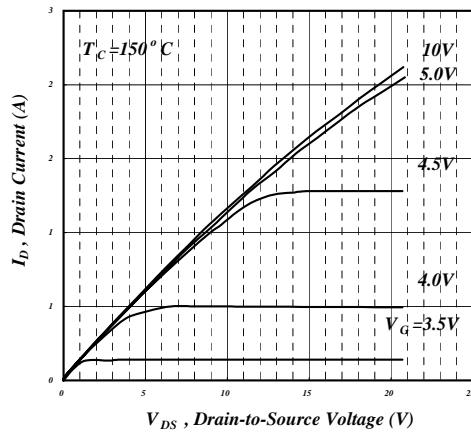


Fig 2. Typical Output Characteristics

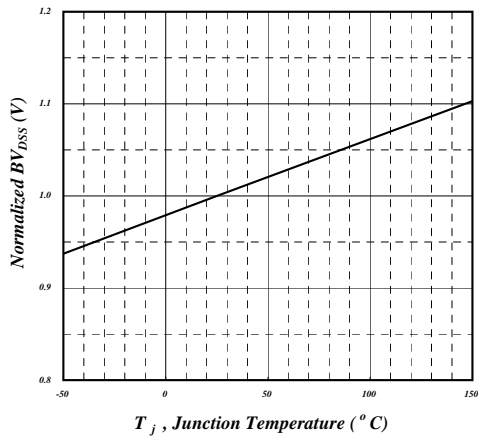


Fig 3. Normalized BV<sub>DSS</sub> v.s. Junction Temperature

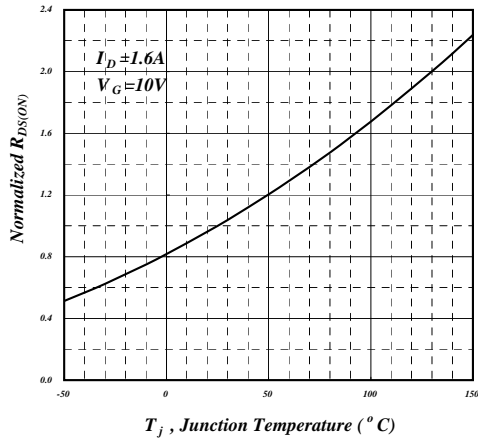


Fig 4. Normalized On-Resistance v.s. Junction Temperature

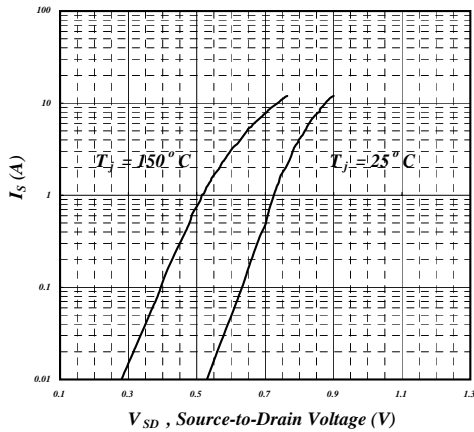


Fig 5. Forward Characteristic of Reverse Diode

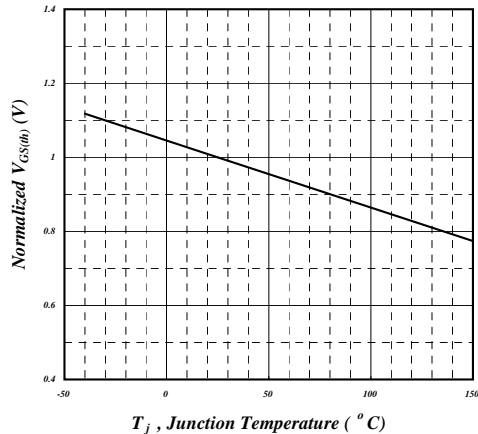


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



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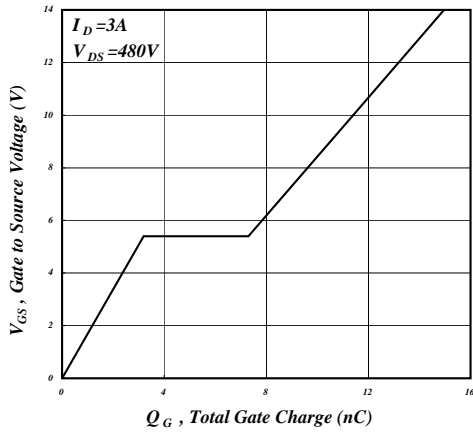


Fig 7. Gate Charge Characteristics

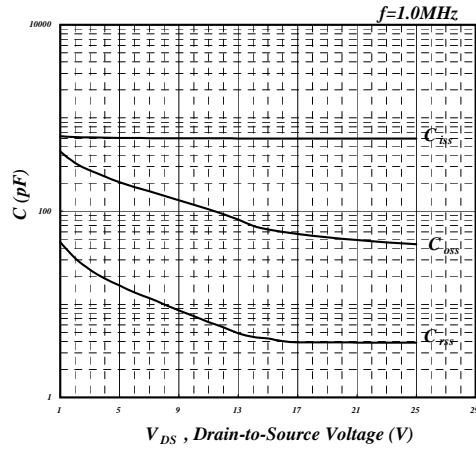


Fig 8. Typical Capacitance Characteristics

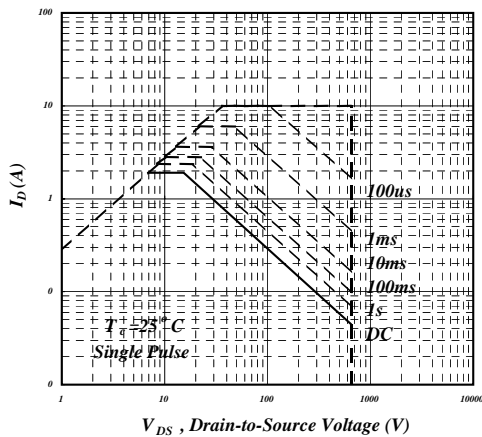


Fig 9. Maximum Safe Operating Area

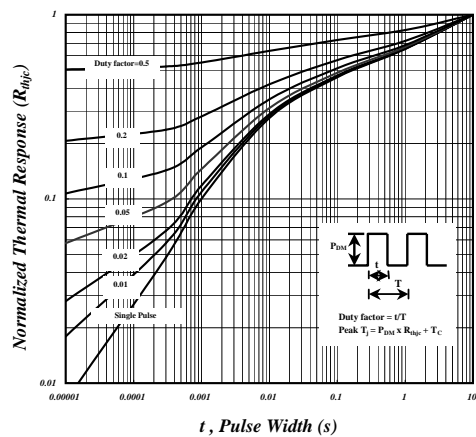


Fig 10. Effective Transient Thermal Impedance

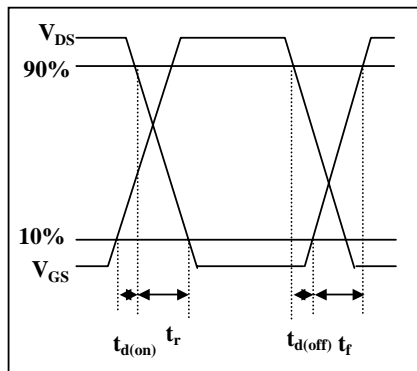


Fig 11. Switching Time Waveform

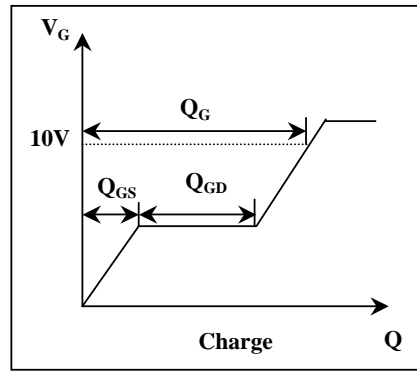


Fig 12. Gate Charge Waveform