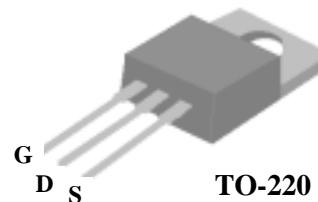




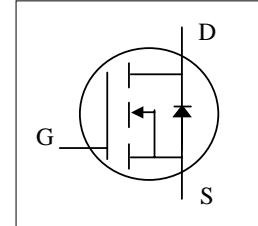
- ▼ Repetitive Avalanche Rated
- ▼ Fast Switching
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant



$BV_{DSS}$	600V
$R_{DS(ON)}$	8Ω
$I_D$	2A

## Description

The TO-220 package is universally preferred for all commercial-industrial applications. The device is suited for DC-DC ,DC-AC converters for telecom, industrial and consumer environment.



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	600	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	2	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	1.26	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	6	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	39	W
	Linear Derating Factor	0.31	W/°C
$E_{AS}$	Single Pulse Avalanche Energy <sup>2</sup>	130	mJ
$I_{AR}$	Avalanche Current	2	A
$E_{AR}$	Repetitive Avalanche Energy	2	mJ
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Units
$R_{thj-c}$	Thermal Resistance Junction-case	Max. 3.2	°C/W
$R_{thj-a}$	Thermal Resistance Junction-ambient	Max. 62	°C/W



### Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

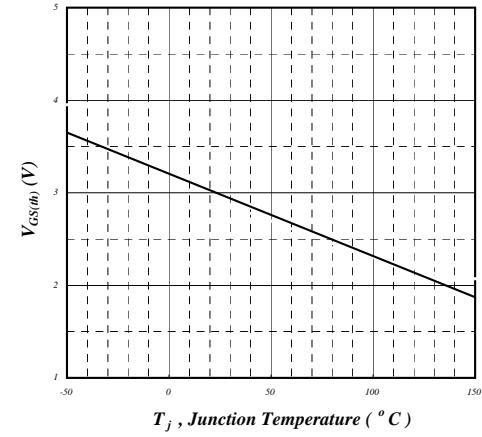
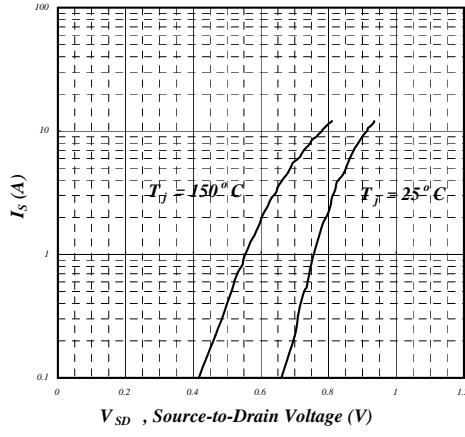
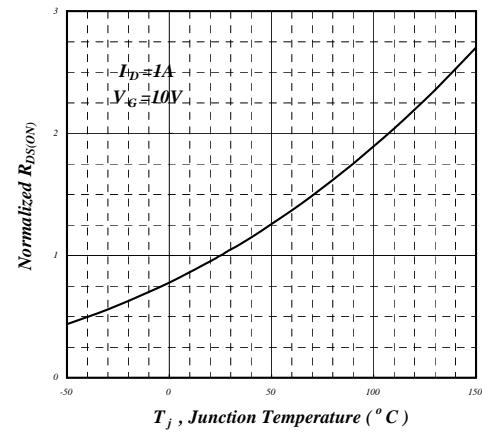
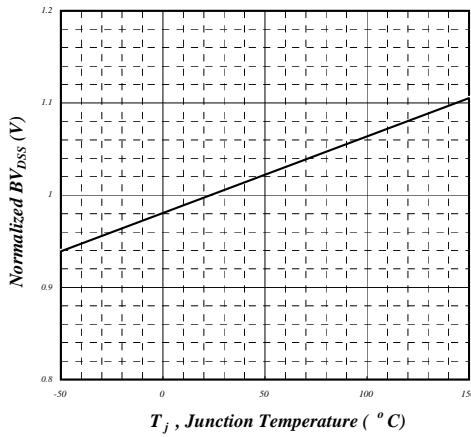
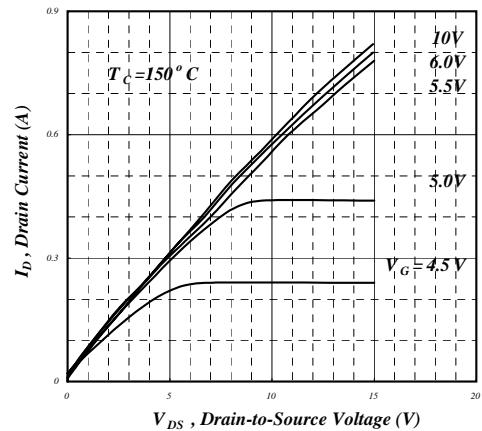
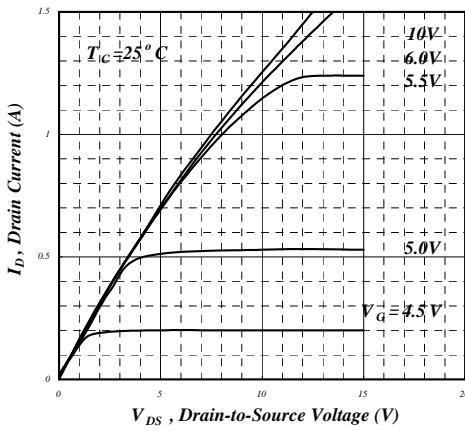
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	600	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	-	0.6	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=1\text{A}$	-	-	8	$\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_D=250\mu\text{A}$	2	-	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=1\text{A}$	-	0.2	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current ( $T_j=25^\circ\text{C}$ )	$V_{\text{DS}}=600\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	10	$\mu\text{A}$
	Drain-Source Leakage Current ( $T_j=150^\circ\text{C}$ )	$V_{\text{DS}}=480\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}=\pm 30\text{V}$	-	-	$\pm 100$	$\text{nA}$
$Q_g$	Total Gate Charge <sup>3</sup>	$I_D=2\text{A}$	-	14	20	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=480\text{V}$	-	2	-	$\text{nC}$
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	8.5	-	$\text{nC}$
$t_{\text{d(on)}}$	Turn-on Delay Time <sup>3</sup>	$V_{\text{DD}}=300\text{V}$	-	9.5	-	ns
$t_r$	Rise Time	$I_D=2\text{A}$	-	12	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_G=10\Omega$ , $V_{\text{GS}}=10\text{V}$	-	21	-	ns
$t_f$	Fall Time	$R_D=150\Omega$	-	9	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	155	240	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	27	-	$\text{pF}$
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	14	-	$\text{pF}$

### Source-Drain Diode

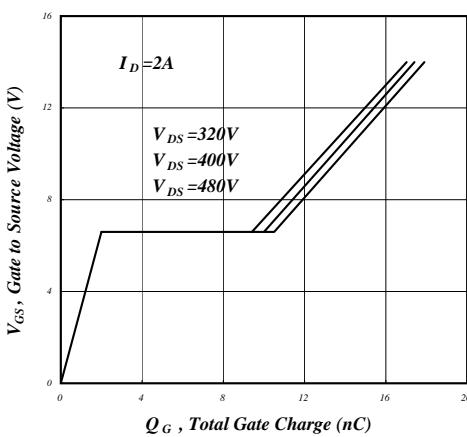
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$I_S$	Continuous Source Current ( Body Diode )	$V_D=V_G=0\text{V}$ , $V_S=1.5\text{V}$	-	-	2	A
$I_{\text{SM}}$	Pulsed Source Current ( Body Diode ) <sup>1</sup>		-	-	6	A
$V_{\text{SD}}$	Forward On Voltage <sup>3</sup>	$T_j=25^\circ\text{C}$ , $I_S=2\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	-	1.5	V

### Notes:

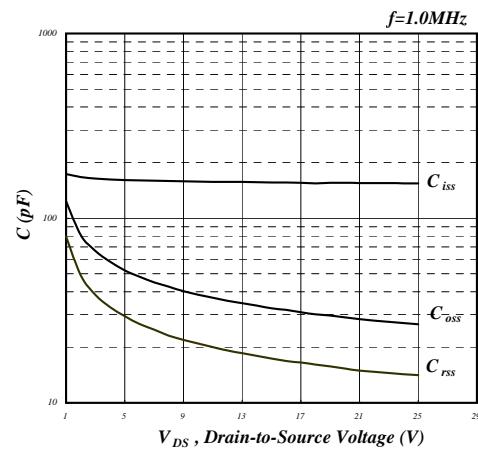
- 1.Pulse width limited by safe operating area.
- 2.Starting  $T_j=25^\circ\text{C}$  ,  $V_{\text{DD}}=50\text{V}$  ,  $L=50\text{mH}$  ,  $R_G=25\Omega$  ,  $I_{\text{AS}}=1.6\text{A}$ .
- 3.Pulse width  $<300\text{us}$  , duty cycle  $<2\%$ .



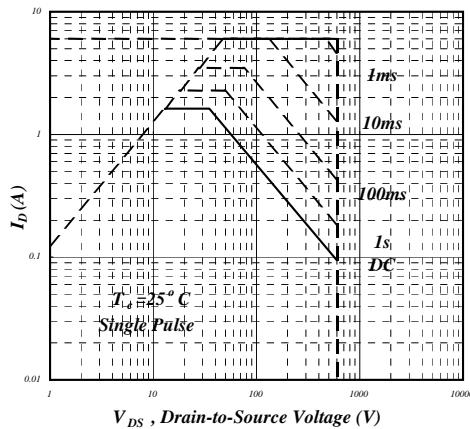
# AP02N60P



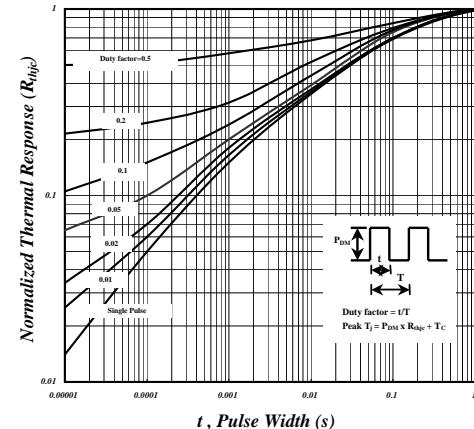
**Fig 7. Gate Charge Characteristics**



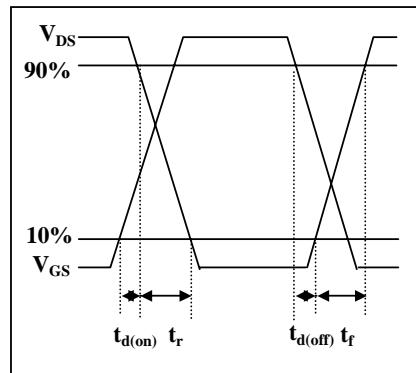
**Fig 8. Typical Capacitance Characteristics**



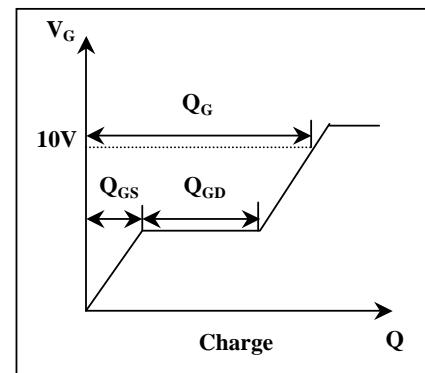
**Fig 9. Maximum Safe Operating Area**



**Fig 10. Effective Transient Thermal Impedance**



**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**