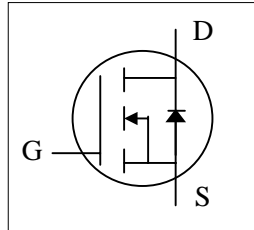




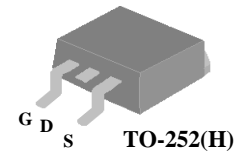
- ▼ Simple Drive Requirement
- ▼ Low On-resistance
- ▼ Low Gate Voltage Drive



BV_{DSS}	20V
$R_{DS(ON)}$	14m Ω
I_D	37A

Description

The Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.



The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	37	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	24	A
I_{DM}	Pulsed Drain Current ¹	140	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	25	W
	Linear Derating Factor	0.2	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case	Max. 5.0	$^\circ C/W$
Rthj-a	Thermal Resistance Junction-ambient	Max. 110	$^\circ C/W$



AP9U18GH

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =18A	-	-	14	mΩ
		V _{GS} =2.5V, I _D =9A	-	-	28	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.5	-	1.5	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =18A	-	18	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V, V _{GS} =0V	-	-	1	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±12V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =18A	-	21	34	nC
Q _{gs}	Gate-Source Charge	V _{DS} =16V	-	2.4	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	9	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =10V	-	8.3	-	ns
t _r	Rise Time	I _D =18A	-	102	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =5V	-	24	-	ns
t _f	Fall Time	R _D =0.56Ω	-	12	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1280	2050	pF
C _{oss}	Output Capacitance	V _{DS} =20V	-	175	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	170	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =18A, V _{GS} =0V	-	-	1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =10A, V _{GS} =0V,	-	27	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	17	-	nC

Notes:

- 1.Pulse width limited by max. junction temperature.
- 2.Pulse test

THIS PRODUCT IS AN ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR CONSUMER MARKET. APPLICATIONS OR USES AS CRITERIAL COMPONENT IN LIFE SUPPORT DEVICE OR SYSTEM ARE NOT AUTHORIZED.

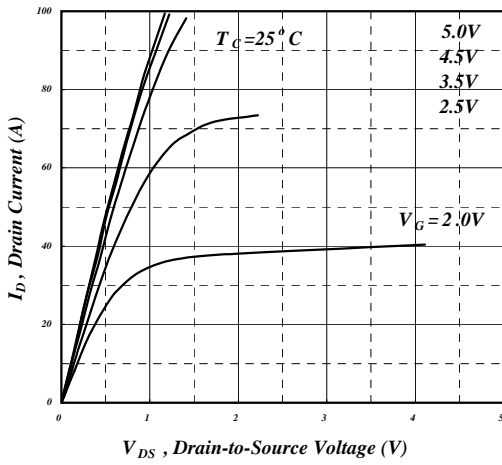


Fig 1. Typical Output Characteristics

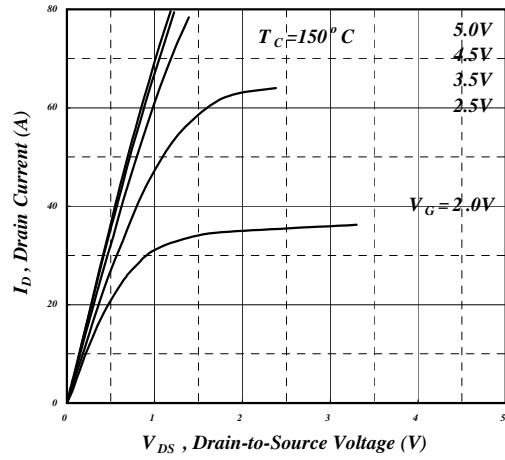


Fig 2. Typical Output Characteristics

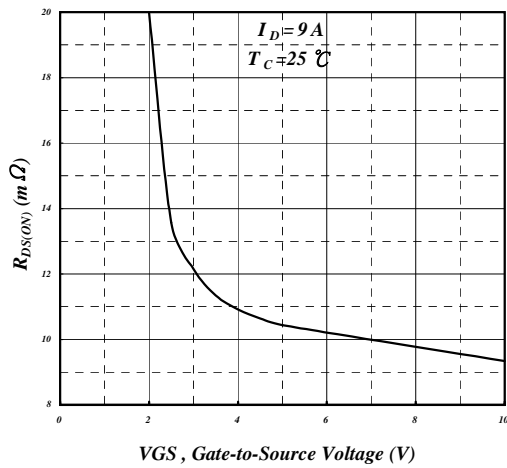


Fig 3. On-Resistance v.s. Gate Voltage

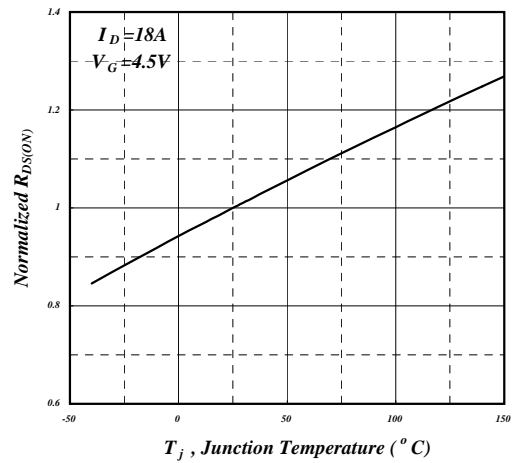


Fig 4. Normalized On-Resistance v.s. Junction Temperature

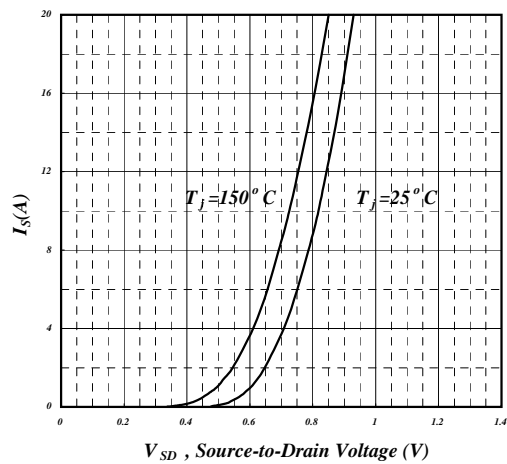


Fig 5. Forward Characteristic of Reverse Diode

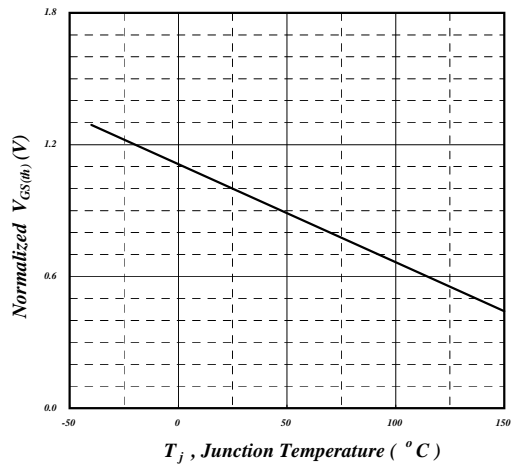


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

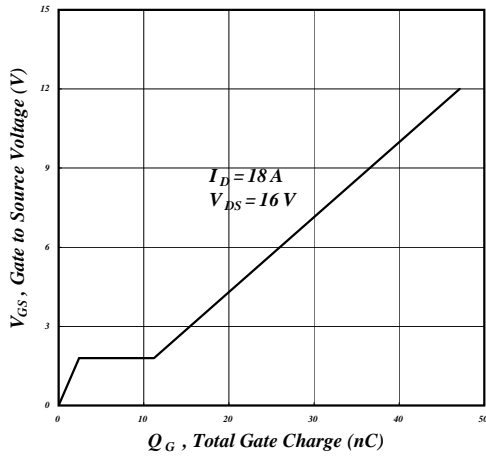


Fig 7. Gate Charge Characteristics

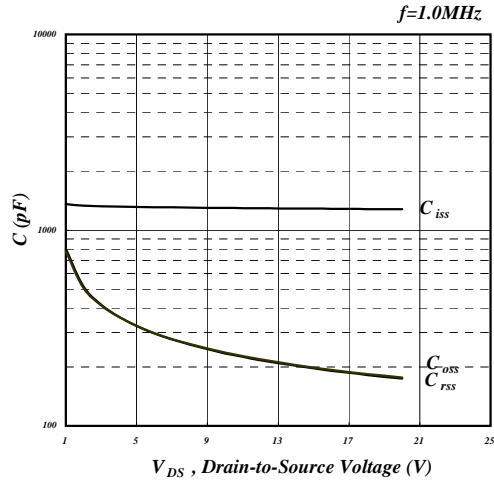


Fig 8. Typical Capacitance Characteristics

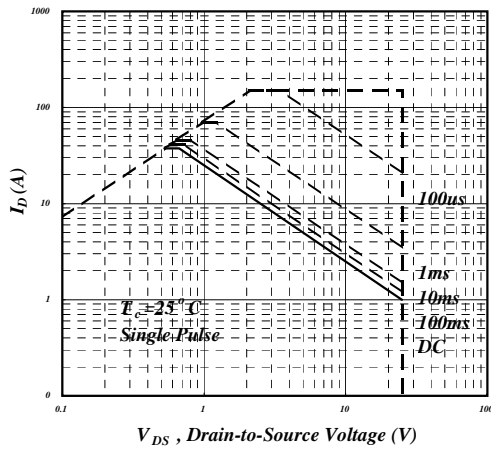


Fig 9. Maximum Safe Operating Area

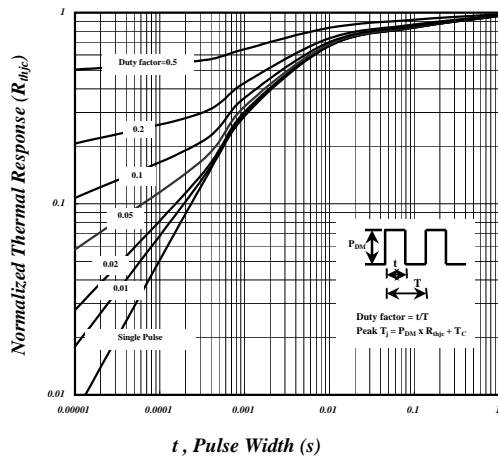


Fig 10. Effective Transient Thermal Impedance

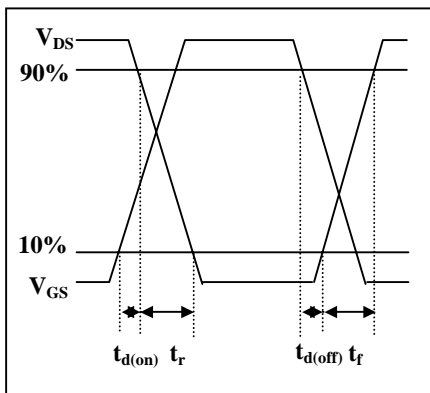


Fig 11. Switching Time Waveform

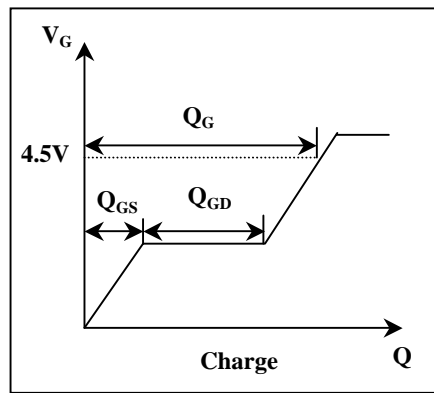
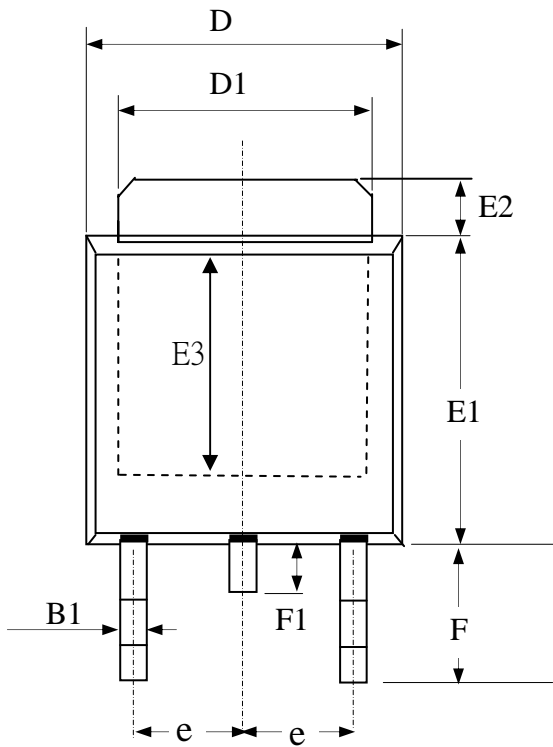


Fig 12. Gate Charge Waveform



Package Outline : TO-252

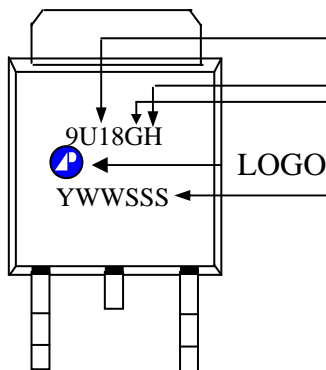


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.5	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



Part Marking Information & Packing : TO-252



Part Number

Package Code
meet Rohs requirement

LOGO

Date Code (YWWSSS)

Y : Last Digit Of The Year

WW : Week

SSS : Sequence