

STTH6002C

High efficiency ultrafast diode

Main product characteristics

I _{F(AV)}	2 x 30 A
V _{RRM}	200 V
T _j (max)	175° C
V _F (typ)	0.75 V
t _{rr} (typ)	22 ns

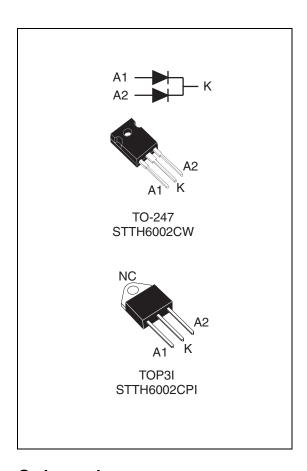
Features and benefits

- Suited for SMPS
- Low losses
- Low forward and reverse recovery times
- High surge current capability
- High junction temperature

Description

Dual center tab rectifier suited for switch mode power supplies and high frequency DC to DC converters.

Packaged in TO-247 and TOP3I, this device is intended for use in low voltage, high frequency inverters, free wheeling and polarity protection



Order codes

Part Number	Marking
STTH6002CW	STTH6002C
STTH6002CPI	STTH6002C

Characteristics STTH6002C

1 Characteristics

Table 1. Absolute ratings (limiting values at $T_i = 25^{\circ}$ C, unless otherwise specified)

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive peak reverse voltage			200	٧
I _{F(RMS)}	RMS forward current			50	Α
		TO-247	Per diode T _c = 140° C	30	
	$I_{F(AV)}$ Average forward current, $\delta = 0.5$		Per device T _c = 125° C	60	
^I F(AV)		TOROL	Per diode T _c = 120° C	30	Α
	TOP3I	Per device T _c = 105° C	60		
I _{FSM}	Surge non repetitive forward current	e non repetitive forward current t _p = 10 ms Sinusoidal		330	Α
T _{stg}	Storage temperature range			-65 to +175	° C
Tj	Maximum operating junction temperature			175	° C

Table 2. Thermal parameters

Symbol	Parameter			Value	Unit
		TO 047	Per diode	1.2	
	TO-247	Total	0.8		
H _{th(j-c)}	R _{th(j-c)} Junction to case	TOP3I	Per diode	1.8	° C/W
			Total	1.20	
R _{th(c)} Coupling	TO-247		0.4		
	TOP3I		0.6		

When the two diodes 1 and 2 are used simultaneously:

 $\Delta Tj(diode\ 1) = P\ (diode\ 1)\ X\ R_{th(j-c)}\ (Per\ diode) + P\ (diode\ 2)\ x\ R_{th(c)}$

STTH6002C Characteristics

Table 3. Static electrical characteristics

Symbol	Parameter	Test conditions		Тур	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25° C	$V_R = V_{RRM}$		30	μА
		T _j = 125° C		30	300	
V _F ⁽²⁾ Forward		T _i = 25° C	I _F = 30 A		1.05	
	Fanyoud valtage dues	1 _j = 25	I _F = 60 A		1.18	V
	Forward voltage drop	T 4500 O	I _F = 30 A	0.75	0.84] v
		T _j = 150° C	I _F = 60 A	0.9	0.99	

- 1. Pulse test: t_p = 5 ms, δ < 2 %
- 2. Pulse test: $t_{\rm D}$ = 380 μ s, δ < 2 %

To evaluate the conduction losses use the following equation:

 $P = 0.69 \text{ x } I_{F(AV)} + 0.005 I_{F}^{2}_{(RMS)}$

Table 4. Dynamic characteristics

Symbol	Parameter	Test conditions	Тур	Max.	Unit
t _{rr}	Reverse recovery time	$I_F = 1 \text{ A, } dI_F/dt = 200 \text{ A/}\mu\text{s,}$ $V_R = 30 \text{ V, } T_j = 25 \text{ °C}$	22	27	ns
I _{RM}	Reverse recovery current	$I_F = 30 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 160 \text{ V}, T_j = 125 ^{\circ}\text{C}$	7.6	9.5	Α
t _{fr}	Forward recovery time	$I_F = 30 \text{ A, } dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_{FR} = 1.1 \text{ x } V_{Fmax}, T_j = 25 \text{ °C}$		220	ns
V _{FP}	Forward recovery voltage	$I_F = 30 \text{ A, } dI_F/dt = 200 \text{ A/}\mu\text{s,}$ $T_j = 25 ^{\circ}\text{C}$	2.5		V

Figure 1. Peak current versus duty cycle (per diode)

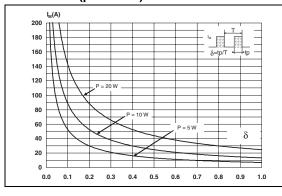
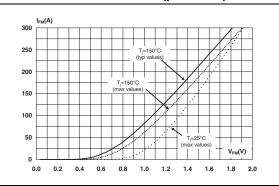


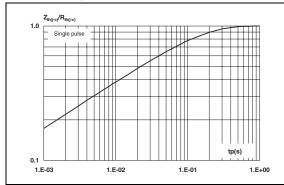
Figure 2. Forward voltage drop versus forward current (per diode)



Characteristics STTH6002C

Figure 3. Relative variation of thermal impedance junction to case versus pulse duration

Figure 4. Junction capacitance versus reverse applied voltage (typical values)



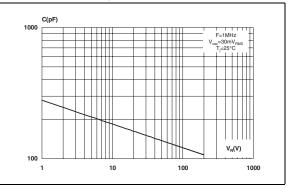


Figure 5. Reverse recovery charges versus dl_E/dt (typical values)

Q_{RR}(nC)
350

|_{j=30 A}
300

V_R=160V

250

150

100

T_{j=25 °C}

dl_s/dt(A/µs)
100
100

Figure 6. Reverse recovery time versus dl_F/dt (typical values)

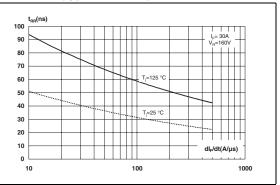


Figure 7. Peak reverse recovery current versus dl_F/dt (typical values)

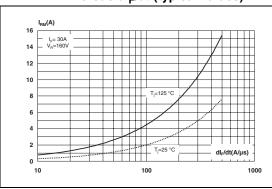
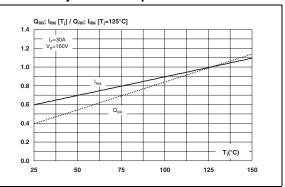
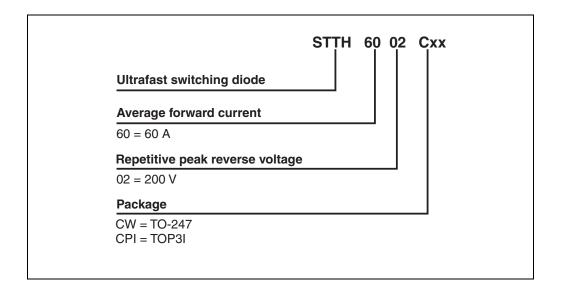


Figure 8. Dynamic parameters versus junction temperature



2 Ordering information scheme



477

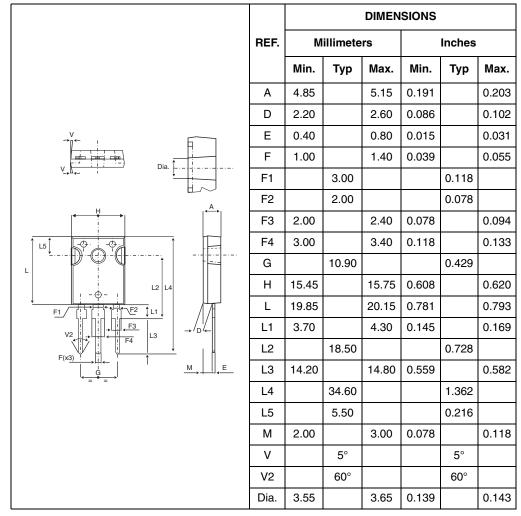
Package information STTH6002C

3 Package information

Epoxy meets UL94, V0

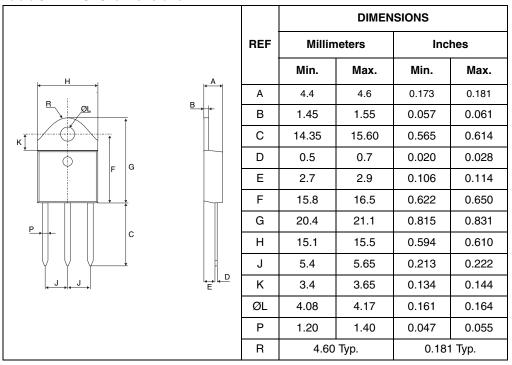
Cooling method: by conduction (C)
Recommended torque value: 0.8 Nm
Maximum torque value: 1.0 Nm

Table 5. TO-247 Dimensions



STTH6002C Package information

Table 6. TOP3I dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Ordering information STTH6002C

4 Ordering information

Part Number	Marking	Package	Weight	Base qty	Delivery mode
STTH6002CW	STTH6002C	TO-247	4.46 g	30	Tube
STTH6002CPI	STTH6002C	TOP3I	4.7 g	30	Tube

5 Revision history

Date	Revision	Description of Changes
Feb-2004	1	First issue
05-Apr-2006	2	Reformatted to current template. Package TOP3I added.

577

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577