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1. SCOPE

1.1 Introduction

This specification covers performance, test and quality requirements for the BOARD-TO-BOARD COAXIAL CONNECTOR. This coaxial connector is designed to handle severe mechanical misalignment between two printed circuit boards and is a surface mount device (SMD).

A single connector achieves the connection as the top board acts like the counterpart with its dedicated "target" layout.

Permitted radial misalignment is 0.8 mm; permitted axial misalignment from nominal stack height is ± 1.0 mm (for P/Ns 619127 and 619134), ± 0.4 mm (for P/Ns 619393 and 619134) and ± 0.3 mm (for P/N 1658260).

1.2 Qualification

When tests are performed on the subject product line, procedures specified in this Product Specification shall be used. All inspections shall be performed using applicable inspection plan and product drawing.

2. APPLICABLE DOCUMENTS

The following documents form a part of this specification to the extent specified herein. Unless otherwise specified, the latest edition of the document applies. In the event of conflict between requirements in this specification and the product drawing, the product drawing shall take precedence. In the event of conflict between the requirements in this specification and referenced documents, this specification shall take precedence.

2.1 Tyco Electronics Documents

- 619388: Drawing for connector with nominal PCB distance 32 mm
- 619127: Drawing for connector with nominal PCB distance 14 mm
- 619134: Drawing for connector with nominal PCB distance 10 mm
- 619135: Drawing for connector with nominal PCB distance 6.65 mm
- 619393 Drawing for connector with nominal PCB distance 7.00 mm
- 1658260: Drawing for connector with nominal PCB distance 4.00 mm
- 109-201: Component Heat Resistance to Lead-Free Reflow Soldering
- 501-90021: Qualification Test Report

2.2 Commercial Standards

- EIA-364: Electrical Connector/Socket Test Procedures Including Environmental Classifications
- IEC 60512: Electromechanical Components For Electronic Equipment; Basic Testing. Procedures and Measuring Methods Part 1: General
- IEC 60068: Basic Environmental Testing Procedures For Electric Components And Electronic Equipment
- J-STD-020: Moisture/Reflow Sensitivity Classification for Non Hermetic Solid State SMD



3. **REQUIREMENTS**

- 3.1 Design and Construction Product shall be of the design, construction and physical dimensions specified on the applicable product drawing.
- 3.2 Materials

Materials used in the construction of this product shall be as specified on the applicable product drawing.

- 3.3 Ratings
 - Voltage: 125 volts AC
 - DC Current: 4.8 A max.
 - Temperature: -40°C to +125°C
 - Characteristic Impedance: 50 ohms
 - Frequency Range: Up to 6 GHz

3.4 Characteristic Values

- Shielding Effectiveness: -60 dB max
- Parallel Stacking Height:

For PN 619388-1 :	32 mm	(nominal)
For PN 619127-1 :	14 mm	(nominal)
For PN 619134-1 :	10 mm	(nominal)
For PN 619393-1 :	7.00 mm	(nominal)
For PN 619135-1 :	6.65 mm	(nominal)
For PN 1658260-1 :	4.00 mm	(nominal)

- Radial Misalignment: ±0.8 mm max
- Axial Misalignment:

For PN 619388-1 :	±1.0 mm max
For PN 619127-1 :	±1.0 mm max
For PN 619134-1 :	±1.0 mm max
For PN 619393-1 :	±0.4 mm max
For PN 619135-1 :	±0.4 mm max
For PN 1658260-1 :	±0.3 mm max

- Angle between Boards: ±2° max
- 3.5 Performance and Test Description

The product is designed to meet the electrical, mechanical and environmental performance requirements specified in Figure 1. Unless otherwise specified, all tests shall be performed at ambient environmental conditions per EIA-364.



3.6 Test Requirements and Procedures Summary

Ref.	Test Descriptio	n	Performance, Requirements	Procedure			
	VISUAL						
C1	Initial examination of product.		Meets requirements of product and customer drawings.	IEC 60512-2 test 1a. Magnification 5x. Visual, dimensional and functional. Inspection per product and customer drawings.			
C2	Final examination of pro	auct.	Meets visual requirements.	IEC 60512-2 test 1a. Magnification 5x. Visual inspection.			
			ELECTRICAL				
C3	Contact resistance.	• Ou ≤ 1 ≤ 2 ≤ 5 • Ce ≤ 2 ≤ 5 ≤ 1 With tin • Ou ≤ 2 • Ce ≤ 1	old/Silver-plated target board: uter conductor: 0 mΩ after 30 Cycle 0 mΩ after 5000 Cycle** 0 mΩ after 8000 Cycle** enter conductor: 0 mΩ after 30 Cycle 0 mΩ after 5000 Cycle** 00 mΩ after 8000 Cycle** h-plated target board: uter conductor: 0 mΩ after 30 Cycle enter conductor: 0 mΩ after 30 Cycle enter conductor: 00 mΩ after 30 Cycle enter conductor: 00 mΩ after 30 Cycle	IEC 60512-2-2a. Subject specimens to 30mA maximum and 20 mV maximum open circuit voltage. Measure with target board at maximum stack height. See Figure 2.			
C4	VSWR	<1.2 up to 2.2 GHz <1.3 up to 3 GHz Keasure return loss up to 3 0 MHz at nominal and maximum misaligned positions, radially axially. See Figure 3.					
C5	Insertion loss.	<0.2 dB up to 2.2 GHz < 0.25 dB up to 3 GHz Another the second structure in the second struc					
C6	Shielding effectiveness60 dB maximum for f ≤ 2'500 MHzAbsorbing Clamp Method in 160096-1 Amendment 2.						

Figure 1 (cont)



	Test Description	Performance, Requirements	Procedure
		MECHANICAL	
C10	Vibration, sinus.	No discontinuities of 1 microsecond or longer duration. See Note.	IEC 60512-4-6d. Cycles: 10Hz-2000Hz-10Hz (1 cycle). 10-58Hz: Const. amplitude of 0.75 mm, 58-2000Hz: Const. acceleration of 100 m/s ² . Sweep rate: 1 octave / minute. Duration: 2 hours in each of 3 mutually perpendicular directions.
C11	Vibration, sinus.	See Note.	IEC 60512-4-6d. Cycles: 10Hz-500Hz-10Hz (1 cycle). 10-58Hz: Const. amplitude of 1.5 mm, 58-500Hz: Const. acceleration of 200 m/s ² . Sweep rate: 1 octave / minute. Duration: 2 hours in each of 3 mutually perpendicular directions.
C12	Mating force.	 Outer conductor: 2.5 to 10.0 N Center conductor: 0.8 to 4.0 N 	Measure at maximum and minimum stack heights.
		ENVIRONMENTAL	
C13	Rapid change of temperature.	See Note.	IEC 60512-6-11d. Subject specimens to 10 cycles between –40 and 105°C. Exposure time: 30 minutes
C15	Temperature life.	See Note.	IEC 60512-6-11i. Subject mated specimens to 125°C for 1000 hours.
C16	Mixed flowing gas. (SO ₂ /H ₂ S/Cl ₂ /NO ₂)	See Note.	IEC 60068-2-60, Test Ke, Method 4. Subject mated specimens for 10 days.

Figure 1 (end)

NOTE Shall meet visual requirements, show no physical damage, and meet requirements of additional tests as specified in the Product Qualification and Requalification Test Sequence shown in Figure 6.



- 3.7 Additional Test and Measuring Details
- 3.7.1 Contact Resistance

Outer Conductor

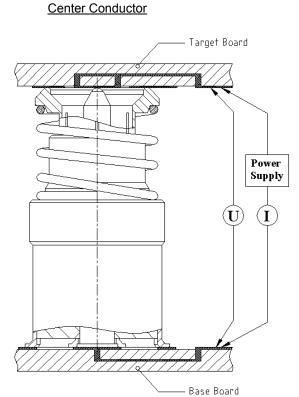


Figure 2

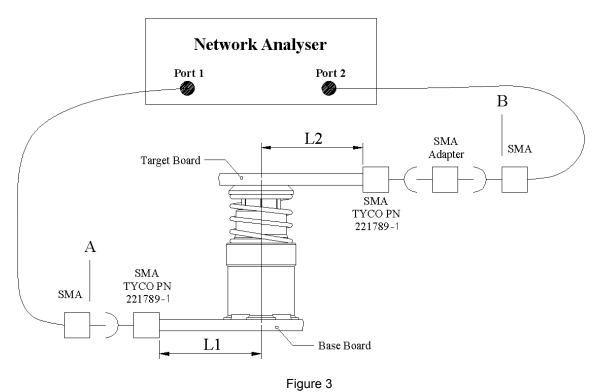


3.7.2 Return Loss and Insertion Loss

Calibration method:

- Actual frequency range on Network Analyzer : 45 to 20.5 GHz
- Power: 10 dBm
- Number of measurement points: 401
- Calibration planes: A, B.
- Full 2 port calibration : open, short, load, thru, (isolation).

Measurement method:



- Return loss:
 Measurement without gating.
- Insertion loss: Subtract 50 ohms line of reference board as shown in Figure 4 to isolate the share of the device under test.

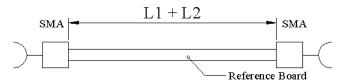


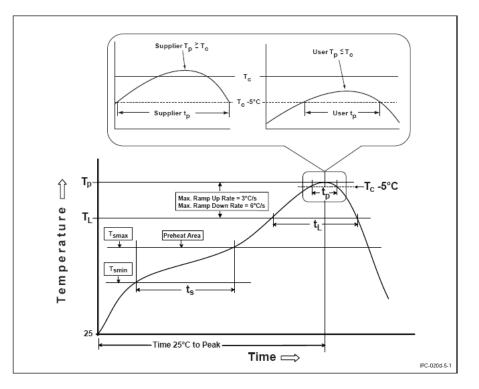
Figure 4



3.7.3 Resistance to Soldering Heat

This connector is suitable for reflow soldering onto a PCB within the limits of the following typical oven temperature profile in conjunction with lead free solder paste.

Temperature profile:



The process parameters are described by the Standard Spec. JEDEC J-STD-020 (Pb-Free Process) and Tyco Spec. TEC-109-201 (Condition B).



3.8 Product Qualification and Requalification Test Sequence

			TEST-GROUP (c)						
Test or Examination	Ref.	1a*	1b*	2a*	2b*	3a*	3b*	4	5
	TEST SEQUENCE (d)								
Initial examination of product	C1	1	1	1	1	1	1	1	1
Contact resistance	C3	2,6	2,6	3,6	3,6	2,4	2,4		
Return loss	C4							2	
Insertion loss	C5							3	
Shielding effectiveness	C6								2
Vibration, sinus (10g)	C10	5	5						
Vibration, sinus (20g)	C11	4	4						
Mating force	C12			2,5	2,5				
Rapid change of temperature	C13	3	3						
Temperature life	C15			4	4				
Mixed flowing gas	C16					3	3		
Final examination of product	C2	7	7	7	7	5	5		

NOTES

With tin-plated (SnPb) target board a*

b* With gold-plated (Au) target board

- (c) See paragraph 4.1.A.
 (d) Numbers indicate sequence in which tests are performed.

Figure 6

All test-groups are performed with specimens soldered on PCB's.

Stack height (PCB setting):

Test-group 1:	Max
Test-groups 2 & 3:	Nominal during environmental test,
	Max for contact resistance measurements
Test-group 4:	Nominal, Min, and Max
Test-group 5:	Nominal



4. QUALIFICATION TEST RESULTS

All requirements were met. Refer to 501-90021, 501-90024 and 501-71011 for detailed results.

5. QUALITY ASSURANCE PROVISIONS

- 5.1 Qualification Testing
 - A. Specimen Selection

Specimen shall be prepared in accordance with applicable Instruction Sheets and shall be selected at random from current production. All test groups shall each consist of a minimum of 10 specimens P/N 619127-1 except test-group 4 for which a minimum of 5 specimens is required, and test-group 5 for which a minimum of 2 specimens is required.

B. Test Sequence

Qualification inspection shall be verified by testing specimens as specified in Figure 6.

5.2 Requalification Testing

If changes significantly affecting form, fit or function are made to product or manufacturing process, product assurance shall coordinate requalification testing, consisting of all or part of original testing sequence as determined by development/product, quality and reliability engineering.

5.3 Acceptance

Acceptance is based on verification that product meets requirements of Figure 1. Failures attributed to equipment, test setup, customer supplied components or operator deficiencies shall not disqualify the product. If product failure occurs, corrective action shall be taken and samples resubmitted for qualification. Testing to confirm corrective action is required before resubmitting.

5.4 Quality Conformance Inspection

The applicable quality inspection plan shall specify the sampling acceptable quality level to be used.

Dimensional and functional requirements shall be in accordance with applicable product drawing and this specification.