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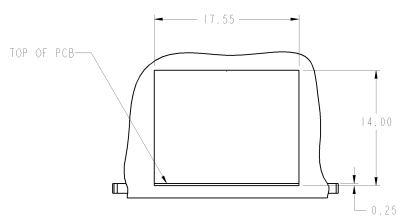
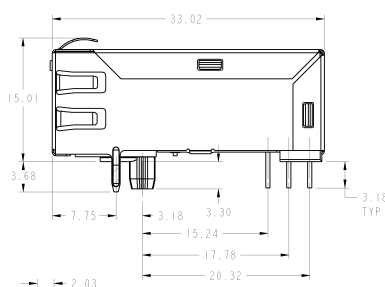
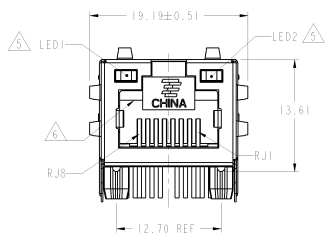
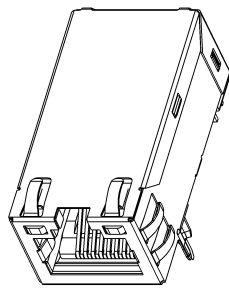
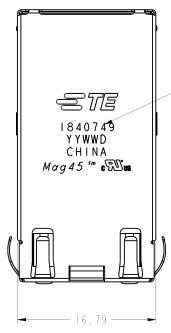
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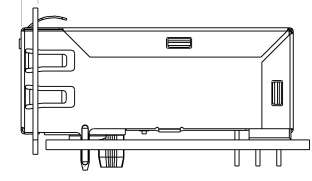
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LOC	QTY	REV	DESCRIPTION	DATE	BY	APPD
AA	00	1				
		3	ECO-11-014530	01022011	PP	LJ



SUGGESTED PANEL CUTOUT VS CUSTOMER BOARD



CONNECTOR ASSEMBLED TO PANEL AND PCB

- MATERIALS**
 - HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0
 - SHIELD: BRASS, 0.20mm THICK
 - PREPLATED WITH MIN 0.76um SEMI-BRIGHT NICKEL
 - POST DIPPED WITH 2.54um MIN SAC SOLDER AT GROUND PINS.
 - CONTACT: PHOSPHOR BRONZE, 0.46mmx0.25mm WITH 1.27um MIN OVERALL NICKEL UNDERPLATE AND SELECTIVE 1.27um MIN GOLD PLATING AT MATING INTERFACE
 - SOLDER TAIL: COPPER CLADDED STEEL, 0.50mm SQUARE
 - LIGHT PIPE: POLYCARBONATE, TRANSPARENT, RATING UL 94V-0
 - LED: DIFFUSED EPOXY LENS, 0.50mm SQUARE CARBON STEEL WIREFRAME LEADS
 - PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL OVER 1.02um MIN COPPER UNDERPLATE
 - POST PLATED WITH 2.54um MIN TIN
- MAGNETICS**
 - APPLICATION: 10/100/1000 BASE-T
 - IMPEDANCE: 100 OHMS
 - TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS
 - OPEN CIRCUIT INDUCTANCE (OCL): 350nH MIN @100kHz, 0.1VRMS, 8mA DC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
 - ALL FOUR PAIRS ARE BI-DIRECTIONAL
 - PERFORMANCE @ 25°C:
 - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 - 12-20LOG(F/80)dB MIN FROM 40.1MHz TO 100MHz
 - 35dB MIN FROM 0.5MHz TO 40MHz
 - 33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz
 - COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 - ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM b.

- 4. OPERATING TEMPERATURE: 0°C ~ 70°C
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA.
- LED COLOR:
 - DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. at IF=20mA
 - FORWARD VOLTAGE (VF): GREEN 2.2V TYP. at IF=20mA
 - DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP. at IF=20mA
 - FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. at IF=20mA
 - DOMINANT WAVELENGTH (λD): ORANGE 605 nm TYP. at IF=20mA
 - FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. at IF=20mA
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- TE CONNECTIVITY LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN, AGENCY APPROVAL MARKING LOGO LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK WITH SUNDAYS=1
- 8. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK WAVE SOLDERING TEMPERATURE IS 265°C MAX, 10 SECONDS MAX.

THIS PRINT IS
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 UNQUALIFIED PRODUCT
 CONTACT PRODUCT ENGINEERING
 BEFORE USING THIS PRINT

LED1	LED2	PART NUMBER
GREEN/ORANGE	YELLOW	7-1840749-3
GREEN	GREEN	6-1840749-7
YELLOW/GREEN	YELLOW/GREEN	6-1840749-6
YELLOW/GREEN	GREEN	6-1840749-5
YELLOW	GREEN	1840749-9

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DIMENSIONS	UNLESS OTHERWISE SPECIFIED	UNIT	mm
MATERIAL	SEE NOTE 1	FINISH	SEE NOTE

PRELIMINARY
 1G61 GIGABIT SCHEMATIC SHIELDED WITH LEDS

DATE CODE: A2100778
 DRAWING NO: 1840749

SCALE: NTS SHEET 1 OF 2 REV 3

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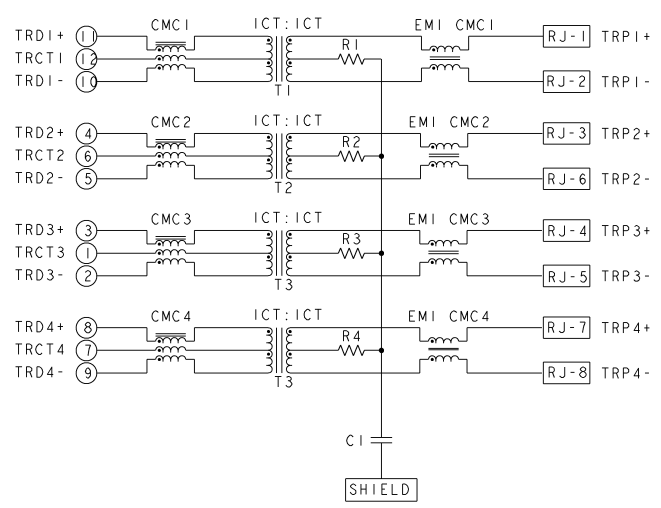
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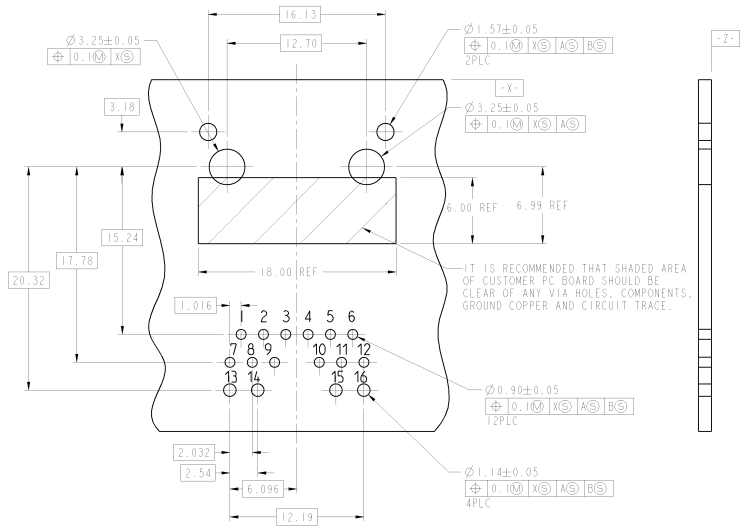
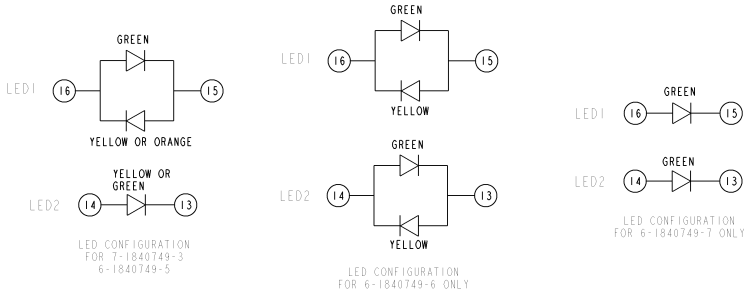
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LOC	QTY	REV	DESCRIPTION	DATE	BY	APPD
AA	00		SEE SHEET 1			

IG61 GIGABIT CIRCUIT



R1 - R4 = 75 Ω , 1/16 W, 5% RESISTORS
 C1 = 1000 pF, 3KV CAPACITOR



SUGGESTED CUSTOMER PCB COMPONENT SIDE
 SCALE 2:1

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THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 24-Mar-08	TE TE Connectivity
DRAWN BY: LINDA DING		REV: 1	
CHECKED BY: PRELTH ZHU		DATE: 24-Mar-08	IX1 MAG(45) MODULAR JACK IG61 GIGABIT SCHEMATIC SHIELDED WITH LEADS
OPERATED BY: JEFFREY XIANG		DATE: 24-Mar-08	
DIMENSIONS: mm	OPERATED BY: JEFFREY XIANG	PRODUCT SPEC: 108-2100	SIZE: A2
MATERIAL: SEE NOTE 1	FINISH: SEE NOTE	WEIGHT: -	CASE CODE: 1840749
CUSTOMER DRAWING		SCALE: NTS	SHEET 2 OF 2