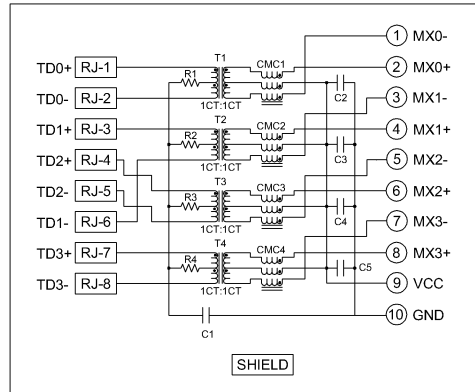


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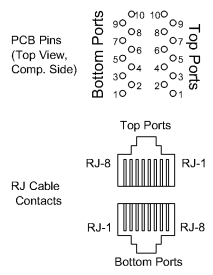
- 1 MATERIALS:
- HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.
  - SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.
  - CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
  - LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME LEADS, PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL OVER 1.02um MIN COPPER UNDERPLATE, POST PLATED WITH 2.54um MIN MATTE TIN AND/OR 2.54um MIN SAC SOLDER DIP OR PURE TIN SOLDER DIP.
- ⚠ MAGNETICS
- APPLICATION: 10/100/1000 BASE-T
  - IMPEDANCE: 100 OHMS AT
  - TURNS RATIO (CHIP-CABLE): 1:1 ALL FOUR PAIRS
  - OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS
  - ALL FOUR PAIRS BI-DIRECTIONAL
  - PERFORMANCE @ 25 °C
  - INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
  - RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
  - 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
  - CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
  - 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
  - COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
  - ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM B.
- ⚠ PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
- ⚠ TYCO ELECTRONICS LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.
- ⚠ RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.
- ⚠ LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20mA. DOMINANT WAVELENGTH (nD)
- GREEN, 568nm TYP AT IF=20mA
  - YELLOW, 588nm TYP AT IF=20mA
- FORWARD VOLTAGE(VF)
- GREEN, 2.2V TYP AT IF=20mA
  - YELLOW, 2.1V TYP AT IF=20mA
- ⚠ INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
- 8 OPERATING TEMPERATURE: FROM 0 °C TO 70 °C
- 9 THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260 °C MAX, 10 SECONDS MAX.
- ⚠ CONTACT AREA LUBRICATED WITH BELLCORE APPROVED LUBRICANT TECHNICAL REFERENCE GR-1217-CORE, ISSUE 1, NOVEMBER 1995.

**S8G67 GIGABIT CIRCUIT** ⚠ ⚠  
 Top and Bottom Ports



C1=1000pF, 2kV  
 C2-C5=470pF, 10%, 50V  
 R1-R4=75 ohm, 1/16W

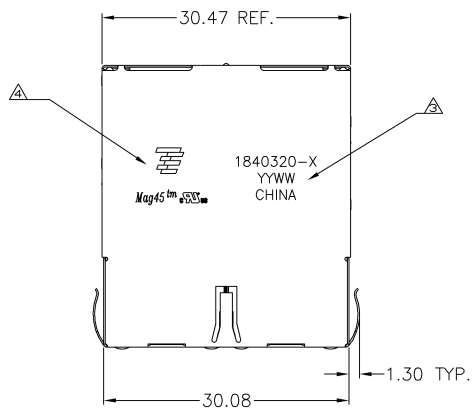
**Pin Designations**  
 (Repeat for each vertical pair of ports.)



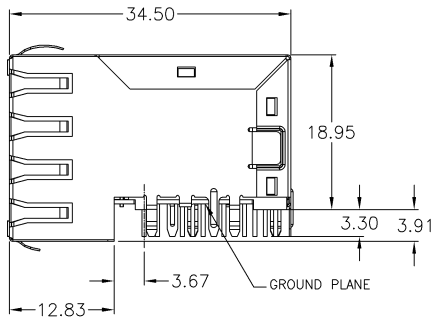
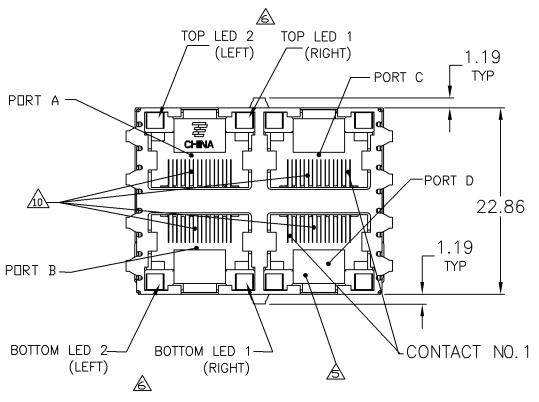
**PRELIMINARY**

GREEN	GREEN	GREEN	GREEN	1	1840320-4
GREEN	YELLOW	GREEN	YELLOW	1	1840320-3
YELLOW	GREEN	GREEN	YELLOW	1	1840320-2
GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	1	1840320-1

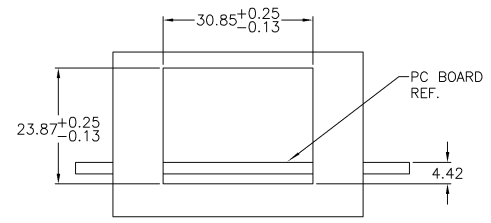
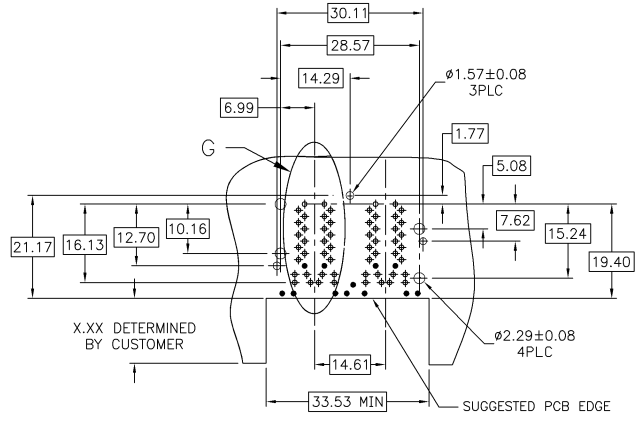
BOTTOM LED 2 (LEFT)	BOTTOM LED 1 (RIGHT)	TOP LED 2 (LEFT)	TOP LED 1 (RIGHT)	NEAR PCB GND TABS	PART NUMBER
THIS DRAWING IS A CONTROLLED DOCUMENT. DATE: 10/19/2002					
DESIGNER: DATE: 10/19/2002					
DRAWN: DATE: 10/19/2002					
CHECKED: DATE: 10/19/2002					
APPROVED: DATE: 10/19/2002					
TITLE: 2X MAGNETIC MODULAR JACK, OFFSET S8G67 GIGABIT CIRCUIT, W/ LED'S SHIELDED, WAVE PANEL, GROUND TABS					
PART NUMBER: A100779 (C)-1840320					
CUSTOMER DRAWING					



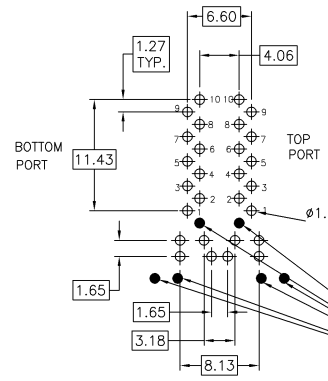
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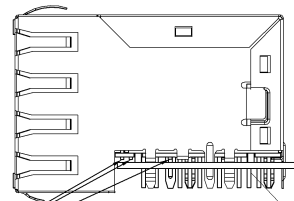
THIS DRAWING IS A CONTROLLED DOCUMENT		DRAWING NO.		REVISED		DATE		BY		APP	
1840320-X	YYWW	1840320-X	YYWW	1840320-X	YYWW	1840320-X	YYWW	1840320-X	YYWW	1840320-X	YYWW
CUSTOMER DRAWING		REVISED		DATE		BY		APP		REVISIONS	
1840320-X		YYWW		1840320-X		YYWW		1840320-X		YYWW	



RECOMMENDED  
PC BOARD LAYOUT VIEW FROM COMPONENT SIDE



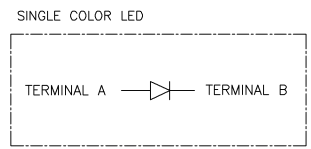
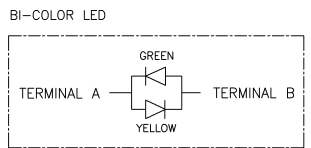
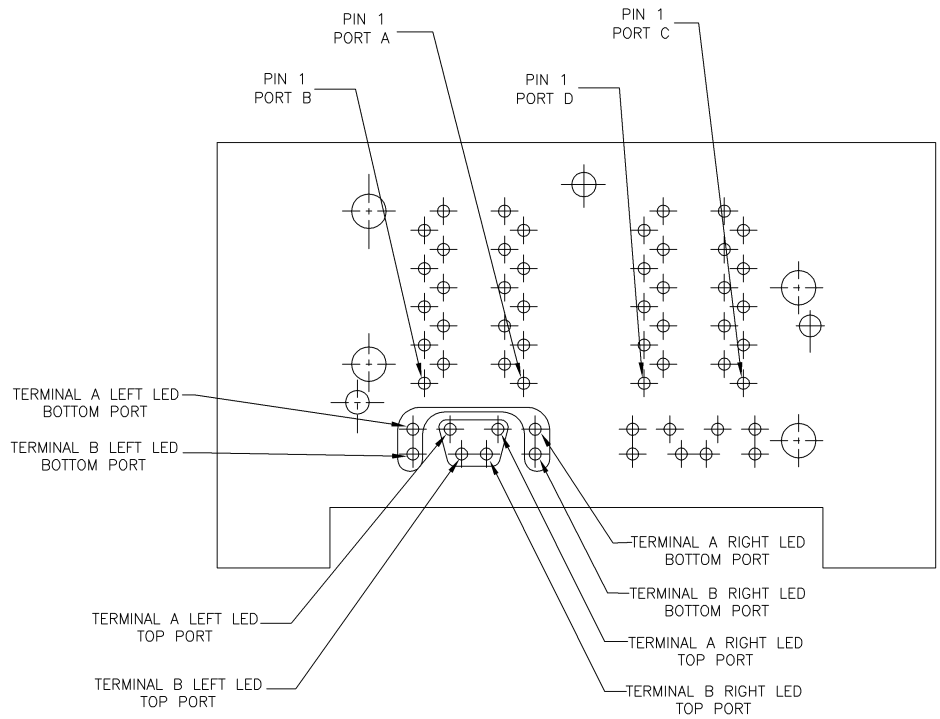
PIN #0, BOTTOM LED'S PIN ON MOD JACK AND BOTTOM PCB ARE STUB PINS WITH LENGTH SIMILAR TO PCB STANDOFF HEIGHT. PRECAUTIONS SHOULD BE TAKEN IN PCB DESIGN TO GUARD AGAINST SHORTING TRACES TO STUB PINS



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DETAIL G  
SCALE: 2:1

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DATE	BY	APP	REV		
DRAWN BY: [Signature]		DATE: 08/11/10	BY: [Signature]	APP: [Signature]	REV: 1
CHECKED BY: [Signature]		DATE: 08/11/10	BY: [Signature]	APP: [Signature]	REV: 1
DESIGNED BY: [Signature]		DATE: 08/11/10	BY: [Signature]	APP: [Signature]	REV: 1
APPROVED BY: [Signature]		DATE: 08/11/10	BY: [Signature]	APP: [Signature]	REV: 1
TITLE: 2X2 MAC45/50 MODULAR JACK, OFFSET 38067 GIGABIT CIRCUIT, W/ LED'S SHIELDED, WAVE PANEL GROUND TABS		REV: A1	DATE: 08/11/10	BY: [Signature]	APP: [Signature]
CUSTOMER DRAWING		REV: 1,1	REV: 3,4	REV: 4	



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LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE

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DRAWING		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
MATERIAL		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
SPECIFICATION		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
WEIGHT		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
PARTIAL		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
CUSTOMER DRAWING		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D
A1		00779	1840320					
CUSTOMER DRAWING		DATE	ISSUED	BY	REVISION	DATE	BY	APP'D