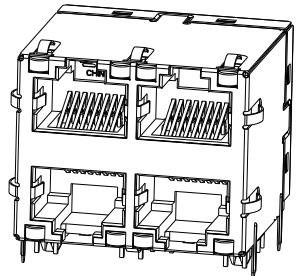
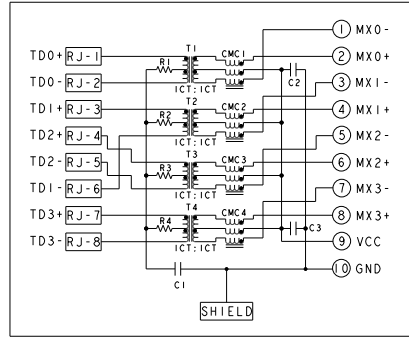


REV	DATE	DESCRIPTION	BY	CHK	APP
A	ECO-11-022912				
B	ECO-11-023440				



S8G56 GIGABIT CIRCUIT
TOP AND BOTTOM PORTS



C1 = 1000pF, 2kV CAPACITOR
C2 - C3 = 10nF, 50V CAPACITORS
R1-R4 = 75 Ohms, 1/16 W, RESISTORS

- MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
UNDERPLATE WITH SELECTED 1.27um MIN GOLD AT MATING INTERFACE
AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE, POST-PLATED WITH
2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
- MAGNETICS
APPLICATION: 10/100/1000 BASE-T
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS
OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
ALL FOUR PAIRS BI-DIRECTIONAL
PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(F/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
500V/SEC AND WITH ALL PORTS CONNECTED.
- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED
IN APPROXIMATE AREA SHOWN. DATE CODE: "YY" IS YEAR, "WW" IS
WORK WEEK. "D" IS DAY OF WEEK, WITH SUNDAY=1
- TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE
LOCATED IN APPROXIMATE AREA SHOWN.
- OPERATING TEMP: FROM 0°C TO +70°C.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND
SUPPORT AUTO-MDI/MDIX.
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE
GREATER THAN 5.08mm.
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
- THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS.
PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

3.04	GREEN	YELLOW	GREEN	YELLOW	1840868-3
3.04	YELLOW	GREEN	GREEN	YELLOW	1840868-2
3.04	GREEN	GREEN	GREEN	GREEN	1840868-1
DIM A	BOTTOM LED 2	BOTTOM LED 1	TOP LED 2	TOP LED 1	PART NUMBER

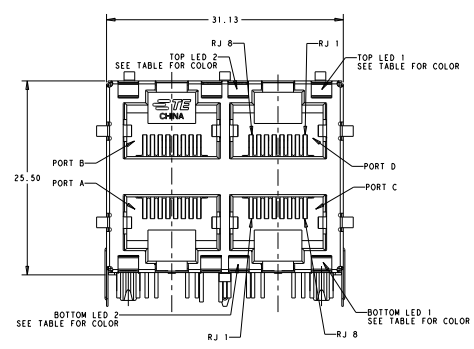
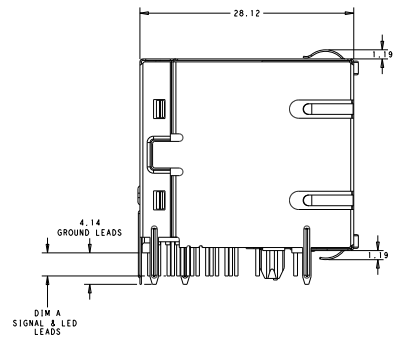
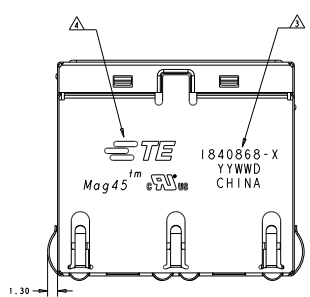
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DATE: 11/02/2012
REV: 00
DRAWN BY: J. J. J.
CHECKED BY: J. J. J.
APPROVED BY: J. J. J.
MATERIAL: 1840868-1
REVISION: A | 00779 | 1840868
CUSTOMER DRAWING

TE CONNECTIVITY

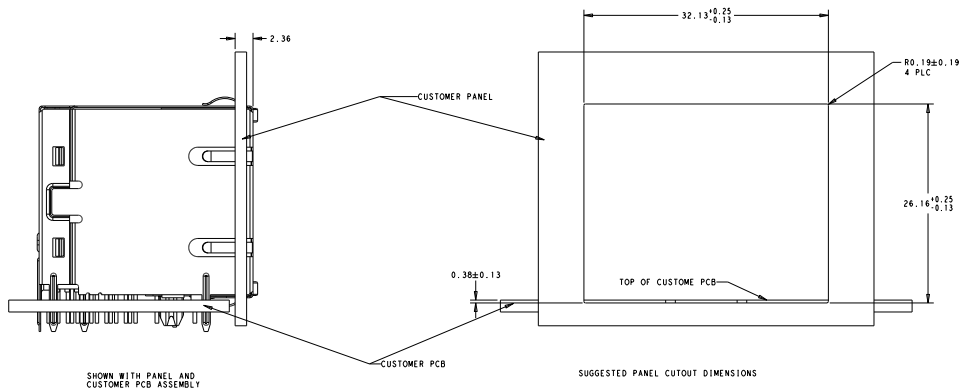
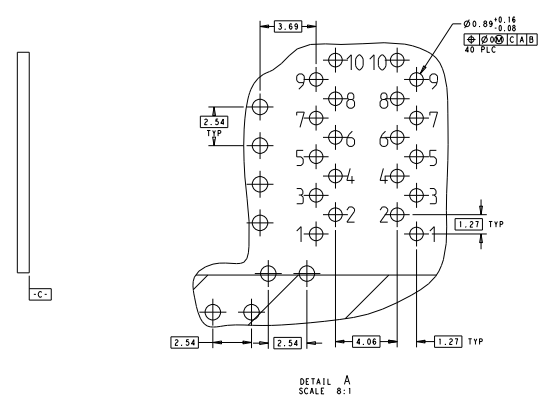
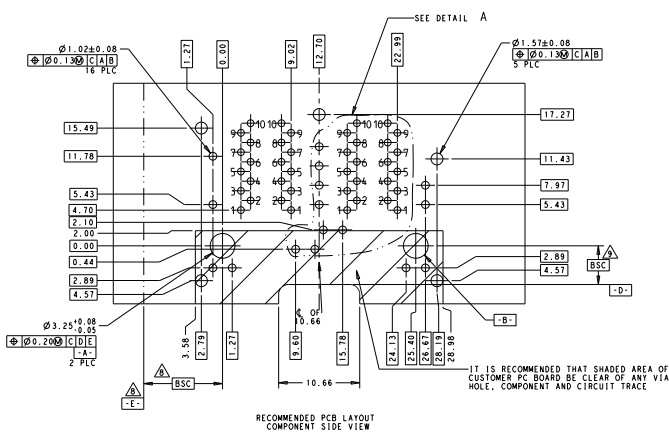
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REVOLUTIONS		DATE	BY	APP
1	SEE SHEET 1	-	-	-



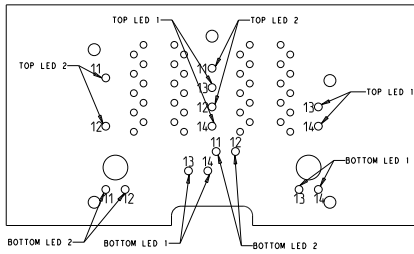
THIS DRAWING IS A CONTROLLED DOCUMENT.		REV: 00	DATE: 18/07/2018	ST logo	TE Connectivity
DESIGNED BY: J. WILSON	DATE: 18/07/2018	REV: 00	DATE: 18/07/2018	TITLE: 2X2 MAG45(TM), GIGABIT S8656 CIRCUIT WAVE PANEL, GROUND SHIELD, WITH LEADS	
DRAWN BY: J. WILSON	DATE: 18/07/2018	REV: 00	DATE: 18/07/2018	PART NUMBER: 1840868	
CHECKED BY: J. WILSON	DATE: 18/07/2018	REV: 00	DATE: 18/07/2018	MATERIAL: A100779	
APPROVED BY: J. WILSON	DATE: 18/07/2018	REV: 00	DATE: 18/07/2018	SCALE: 2:1	
CUSTOMER DRAWING		SHEET 2 OF 4		REV D	

REV	DATE	REVISIONS	BY	CHK	APP
AA	00	SEE SHEET 1	-	-	-

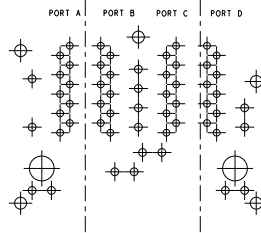
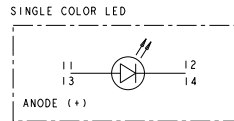


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DRAWN BY: J. WILSON		CHECKED BY: J. WILSON		APPROVED BY: J. WILSON		
TITLE: CUSTOMER DRAWING		PROJECT: 2X2 MAG45(TM), GIGABIT S8G56 CIRCUIT WAVE PANEL, GROUND SHIELD, WITH LEADS		PART: 100779		
MATERIAL: CUSTOMER DRAWING		REV: 00		DATE: 00/00/00		

REV	DATE	DESCRIPTION	BY	CHK	APP
00		SEE SHEET 1			



LED HOLE DESIGNATIONS
VIEWED FROM COMPONENT SIDE



PORT ASSIGNMENT
COMPONENT SIDE VIEW

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DRAWN BY: J. WILSON		DATE: 11/01/2011	REV: 0000010		
CHECKED BY: J. WILSON		DATE: 11/01/2011	REV: 0000010		
APPROVED BY: J. WILSON		DATE: 11/01/2011	REV: 0000010		
PART NAME: 2X2 MAG45(TM), GIGABIT S8G56 CIRCUIT					
PART NUMBER: 1840868					
MATERIAL: 1840868					
REVISION: A		00779	1840868		
CUSTOMER DRAWING		SCALE: 4:1	SHEET: 4	OF: 4	REV: D