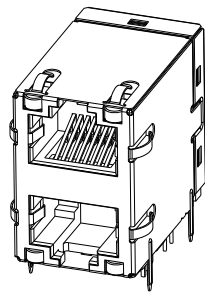
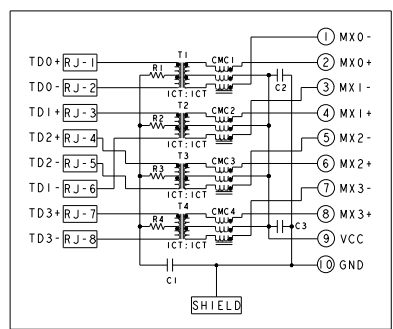


REVISONS		DATE	BY	CHK
A	ECO-11-024592			



S8G56 GIGABIT CIRCUIT  
TOP AND BOTTOM PORTS



C1 = 1000pF, 2kV CAPACITOR  
 C2 - C3 = 10nF, 50V CAPACITORS  
 R1-R4 = 75 Ohms, 1/16 W, RESISTORS

- MATERIALS:  
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0  
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL.  
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.  
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL  
 UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE  
 AND 2.54um MIN MATTE TIN ON SOLDER TAILS.  
 ALL PC BOARDS: HIGH TEMPERATURE PCB, TG>170°C
- MAGNETICS  
 APPLICATION: 10/100/1000 BASE-T  
 IMPEDANCE: 100 OHMS  
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS  
 OPEN CIRCUIT INDUCTANCE (OCL): 350nH MIN @100kHz, 0.1VRMS,  
 8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS  
 ALL FOUR PAIRS BI-DIRECTIONAL  
 PERFORMANCE @ 25°C:  
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz  
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz  
 12-20LOG(F/80)dB MIN FROM 40.1MHz TO 100MHz  
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz  
 33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz  
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz  
 ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND  
 WITH ALL PORTS CONNECTED

△ PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN. DATE CODE: "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1

△ TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

△ INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

△ DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

△ BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.

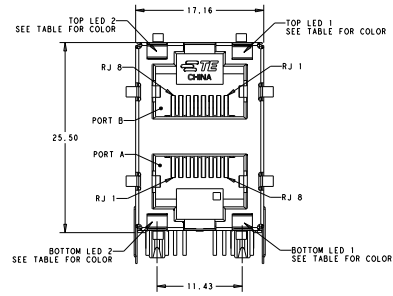
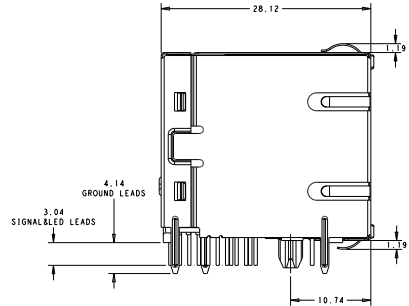
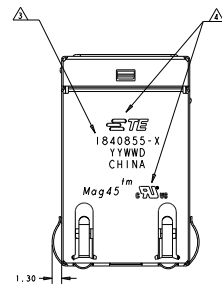
△ LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA  
 LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA  
 DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA  
 DOMINANT WAVELENGTH (λD): ORANGE 605 nm TYP. @ IF=20 mA  
 FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. @ IF=20 mA

11. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

GREEN/YELLOW	GREEN	GREEN/YELLOW	GREEN	1840855-5
GREEN/ORANGE	GREEN/ORANGE	GREEN/ORANGE	GREEN/ORANGE	1840855-7
GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	1840855-4
GREEN	YELLOW	GREEN	YELLOW	1840855-3
GREEN	GREEN	GREEN	GREEN	1840855-1

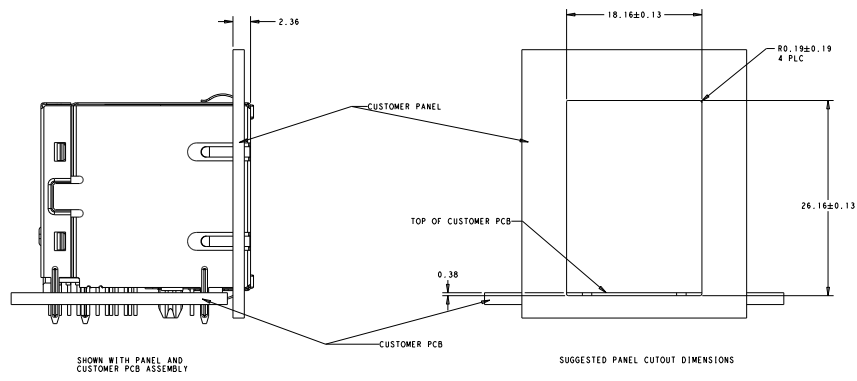
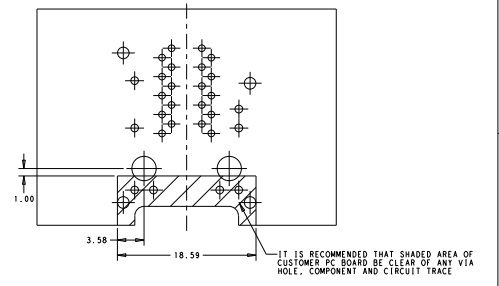
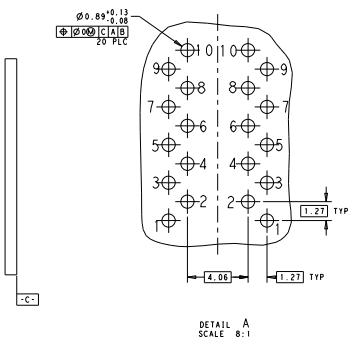
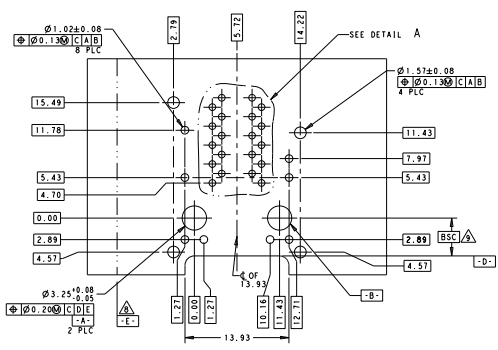
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CUSTOMER DRAWING		DATE	BY
		10/27/09	1840855

REVOLUTIONS		DATE	BY	CHK	APP
1	DESCRIPTION				
2	SEE SHEET 1				



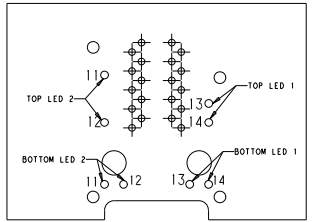
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CUSTOMER DRAWING		A100773		1840855	

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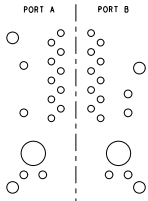


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MATERIAL		DRAWN BY	
A100773		1840855	
CUSTOMER DRAWING		SCALE	
		1:1	

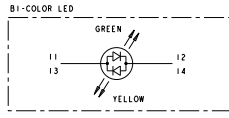
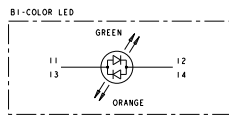
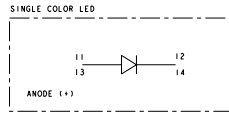
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REV. NO.	DATE	DESCRIPTION	BY	CHK
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LED HOLE DESIGNATIONS  
VIEWED FROM COMPONENT SIDE



PORT ASSIGNMENT  
COMPONENT SIDE VIEW



THIS DRAWING IS A CONTROLLED DOCUMENT		THIS DRAWING IS CONTROLLED BY THE FOLLOWING: <table border="1"> <tr> <td>DATE</td> <td>BY</td> </tr> <tr> <td> </td> <td> </td> </tr> </table>	DATE	BY			2X1 MAG45(17M) GIGABIT S8G56 CIRCUIT GROUND SHIELD, WITH LEADS																	
DATE	BY																							
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