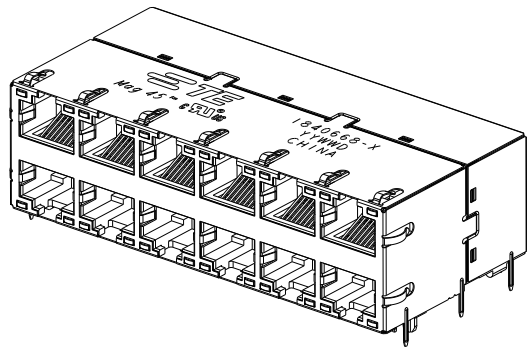
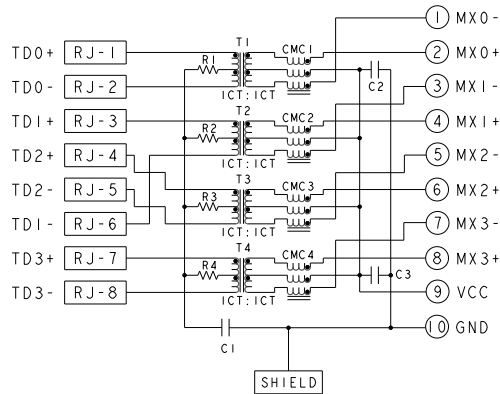


REV	DATE	DESCRIPTION	BY	CHK	APP
1	10-01-05	ECO-11-05092	AMORINO	RL	KZ
2	10-01-05	SEE FINAL SHEET	AMORINO	RL	KZ
3	10-01-05	ECO-12-05029	AMORINO	RL	KZ



### S8G56 GIGABIT CIRCUIT

TOP AND BOTTOM PORTS



C1 = 1000pF, 2kV DECOUPLING CAPACITOR  
 C2 - C3 = 10nF, 50V CAPACITOR  
 R1-R4 = 75 Ω, 1/16 W, RESISTORS

- MATERIALS:**  
 PLASTIC HOUSING: BLACK, THERMOPLASTIC, FLAMMABILITY RATING UL 94V-0  
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,  
 POST-DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.  
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL UNDERPLATE  
 WITH 1.27um MIN GOLD OR WITH SELECT 0.5um MIN GOLD OVER 0.76um  
 MIN PALLADIUM-NICKEL AT MATING INTERFACE AND 2.54um MIN MATTE  
 TIN ON SOLDER TAILS.  
 LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED  
 ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL  
 UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH  
 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
- MAGNETICS:**  
 APPLICATION: 10/100/1000 BASE-T,  
 IMPEDANCE: 100 OHMS  
 TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS  
 OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100KHz, 0.1VRMS,  
 8mA DC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS.  
 ALL FOUR PAIRS BI-DIRECTIONAL  
 PERFORMANCE @25°C:  
 INSERTION LOSS (IL): 1.1dB MAX FROM 1.0MHz TO 100MHz  
 RETURN LOSS (RL): 18dB MIN FROM 1.0MHz TO 40MHz  
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz  
 CROSSTALK ATTENUATION: 35dB MIN FROM 1.0MHz TO 40MHz  
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz  
 COMMON MODE REJECTION RATIO (CMRR):  
 30dB MIN FROM 1.0MHz TO 100MHz  
 ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC  
 AND WITH ALL PORTS CONNECTED.

- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN LOCATED IN APPROXIMATE AREA SHOWN  
 DATE CODE: YYWW WHERE "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK,  
 WITH SUNDAY = 1
- TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE LOCATED  
 IN APPROXIMATE AREA SHOWN.
- OPERATING TEMP: FROM 0°C TO 70°C.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MD1/MDIX.
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE  
 GREATER THAN 5.08mm.
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA  
 LED COLOR: DOMINANT WAVELENGTH (LD): GREEN 568 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA  
 DOMINANT WAVELENGTH (LD): YELLOW 588 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
- THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C  
 FOR 5 SECONDS

3.04	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	1840668-1
DIM A	BOTTOM LED NO. 2	BOTTOM LED NO. 2	TOP LED NO. 2	TOP LED NO. 1	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT. DATE: 10/01/05. DRAWING NO: 1840668-1. REV: 1.0. CUSTOMER DRAWING.

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2x8 MAGNETIC GIGABIT S8G56 CIRCUIT, WAVE PANEL, GROUND SHIELD, W/LEDS, AUTO-WELDING

TE CONNECTIVITY

1840668-1

