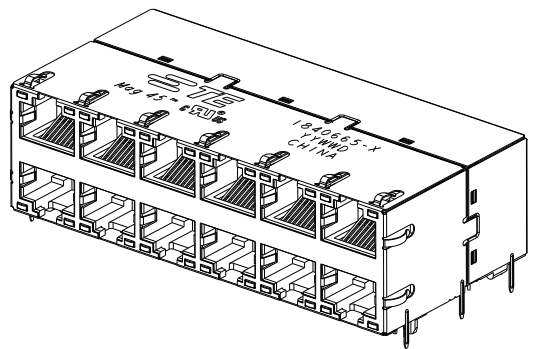
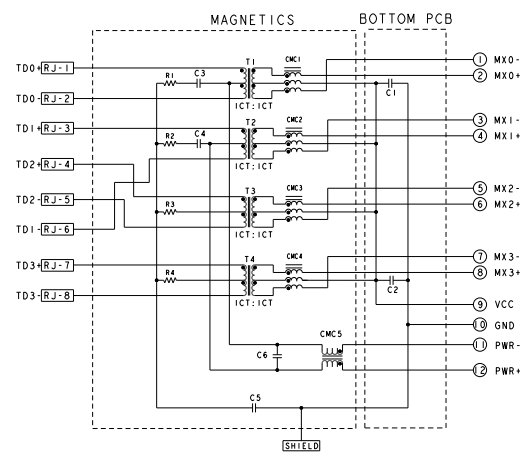


REV	DATE	DESCRIPTION	BY	CHK	APP
1	10-01-08	REVISED	TE	TE	TE
2	10-01-08	REVISED	TE	TE	TE
3	10-01-08	REVISED	TE	TE	TE
4	10-01-08	REVISED	TE	TE	TE
5	10-01-08	REVISED	TE	TE	TE
6	10-01-08	REVISED	TE	TE	TE
7	10-01-08	REVISED	TE	TE	TE
8	10-01-08	REVISED	TE	TE	TE
9	10-01-08	REVISED	TE	TE	TE
10	10-01-08	REVISED	TE	TE	TE



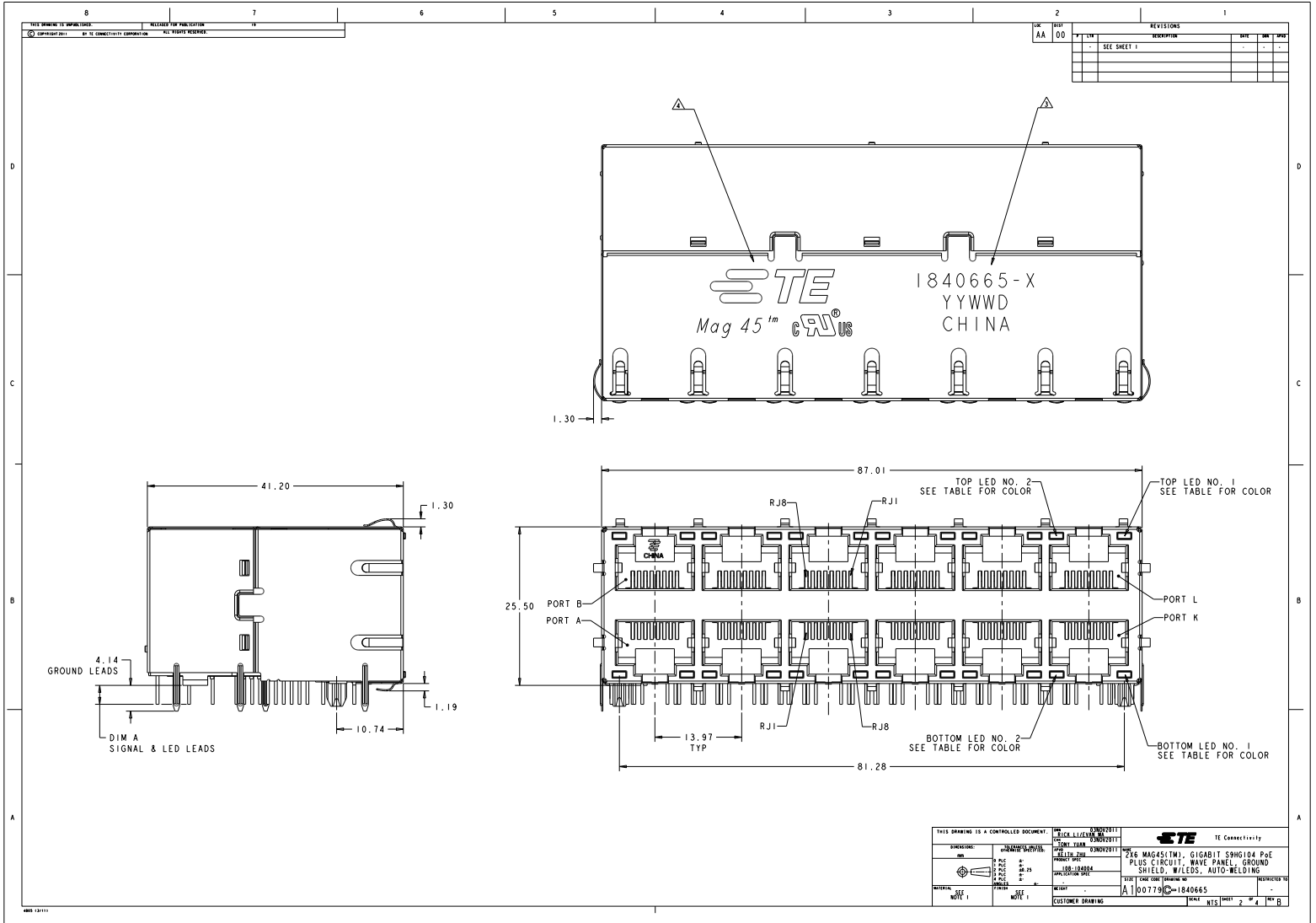
S9HG104 GIGABIT PoE PLUS CIRCUIT  
 TOP AND BOTTOM PORTS



C1-C2=10nF, 50V, CAPACITORS  
 C3-C4=22nF, 100V, CAPACITORS  
 C5=1000pF, 2kV, CAPACITOR  
 C6=68nF, 100V, CAPACITOR  
 R1-R4=75 OHMS, 1/16W, RESISTORS

- MATERIALS:  
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0  
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,  
 POST-DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,  
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL UNDERPLATE  
 WITH 1.27um MIN GOLD OR WITH SELECT 0.05um MIN GOLD OVER 0.76um  
 MIN PALLADIUM-NICKEL AT MATING INTERFACE AND 2.54um MIN MATTE  
 TIN ON SOLDER TAILS.  
 LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED  
 ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL  
 UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH  
 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
- MAGNETICS:  
 APPLICATION: 10/100/1000 BASE-T, PoE PLUS  
 IMPEDANCE: 100ohms  
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS  
 OPEN CIRCUIT INDUCTANCE (OCL):  
 ALL CHANNELS 350 uH MIN @ 100kHz, 0.1 VRMS WITH 8mA DC BIAS FROM 0°C TO 70°C,  
 120 uH min @ 100kHz, 0.1 VRMS WITH 19 mA DC BIAS FROM 0°C TO 70°C, ACROSS  
 RJ1-RJ2 & RJ3-RJ6  
 ALL FOUR PAIRS BI-DIRECTIONAL  
 POE CURRENT: 600mA DC MAX  
 PERFORMANCE @25°C:  
 INSERTION LOSS (IL): 1.1dB MAX FROM 1.0MHz TO 100MHz  
 RETURN LOSS (RL): 18dB MIN FROM 1.0MHz TO 40MHz  
 12-20LOG(F/80)dB MIN FROM 40.1MHz TO 100MHz  
 CROSSTALK ATTENUATION: 35dB MIN FROM 1.0MHz TO 40MHz  
 33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz  
 COMMON MODE REJECTION RATIO (CMRR):  
 30dB MIN FROM 1.0MHz TO 100MHz  
 ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC  
 AND WITH ALL PORTS CONNECTED.
- PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN LOCATED IN APPROXIMATE AREA SHOWN  
 DATE CODE: YYWW WHERE "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK,  
 WITH SUNDAY =1
- TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE LOCATED  
 IN APPROXIMATE AREA SHOWN.
- OPERATING TEMP: FROM 0°C TO 70°C.
- RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
- DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE  
 GREATER THAN 5.08mm.
- LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA  
 LED COLOR: DOMINANT WAVELENGTH (LD): GREEN 568 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA  
 DOMINANT WAVELENGTH (LD): YELLOW 588 nm TYP. @ IF=20mA  
 FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
- THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C  
 FOR 5 SECONDS

DIM A	3.04	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	TOP LED	TOP LED	1840665-1
	BOTTOM LED	BOTTOM LED	BOTTOM LED	BOTTOM LED	NO. 1	NO. 1	PART NO.	
THIS DRAWING IS A CONTROLLED DOCUMENT. THE DATE LISTED IS 10/01/08.								
216 MAGNETICS: GIGABIT S9HG104 PoE PLUS CIRCUIT; WAVE PANEL; GROUND SHIELD; W/LEDS; AUTO-WELDING								
PART NO. 1840665-1								
CUSTOMER DRAWING								



THIS DRAWING IS A CONTROLLED DOCUMENT		REV. 00	DATE 11/11/2011	BY	SYNDZ011
DRAWN BY		DATE	DATE	DATE	DATE
CHECKED BY		DATE	DATE	DATE	DATE
APPROVED BY		DATE	DATE	DATE	DATE
MATERIAL		QTY	UNIT	PRICE	TOTAL
SEE NOTE 1	SEE NOTE 1				
CUSTOMER DRAWING					

