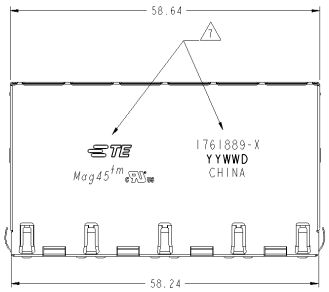
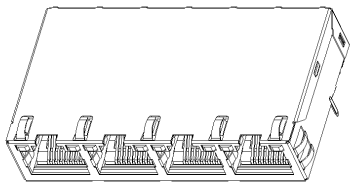
LOC	DIST
AA	00

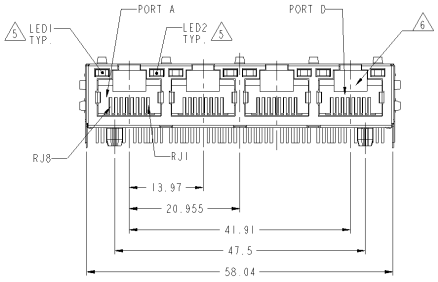
REVISIONS

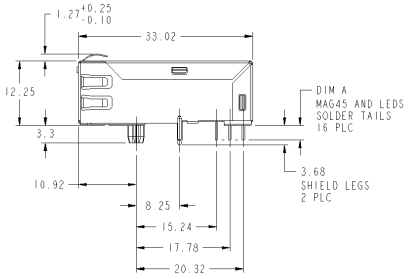
P	LTR	DESCRIPTION	DATE	DWN	APVD
	F	ECO-11-014530	01JUL2011	PP	LJ

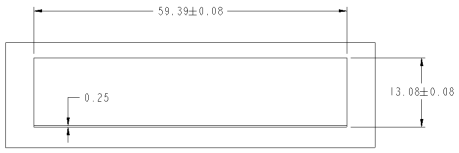




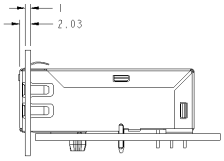
SCALE 2:1







SUGGESTED PANEL CUTOUT
SCALE 2:1



CONNECTOR ASSEMBLED
TO PANEL AND PCB
SCALE 2:1

1 MATERIALS
HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, 0.20mm THICK,PREPLATED WITH MIN 0.76µm SEMI-BRIGHT NICKEL
POST DIPPED WITH 2.54µm MIN SAC SOLDER AT GROUND PINS.
CONTACT: PHOSPHOR BRONZE, 0.46mm X0.25mm
WITH 1.27µm MIN OVERALL NICKEL UNDERPLATE AND
SELECTIVE 1.27µm MIN GOLD PLATING AT MATING INTERFACE
SOLDER TAIL: COPPER CLADDED STEEL, 0.50mm SQUARE
PLATED WITH 2.54µm MIN MATTE TIN
LIGHT PIPE: POLYCARBON, TRANSPARENT, RATING UL 94V-0
LED: DIFFUSED EPOXY LENS, 0.50mm SQUARE CARBON STEEL WIREFRAME LEADS
PREPLATED WITH 2.03µm MIN SILVER OVER 1.02µm MIN NICKEL UNDERPLATE
OVER 1.02µm MIN COPPER UNDERPLATE;POST PLATED WITH 2.54µm MIN MATTE TIN.

2 MAGNETICS
-APPLICATION: 10/100/1000 BASE-T
-IMPEDANCE: 100 OHMS
-TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mA DC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
-ALL FOUR PAIRS BI-DIRECTIONAL
-PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
-ISOLATION VOLTAGE:
I761889-1/-2/-3,2-I761889-1/-2/-3 COMPLY WITH IEEE802.3 2002,
PARA 40.6.1.1, ITEM b.
I761889-8,6-I761889-8,8-I761889-8 COMPLY WITH IEEE802.3 2002,
PARA 40.6.1.1, ITEM a AND b.

3 THE MAGNETIC CIRCUIT ON ALL FOUR CHANNELS IS SYMMETRICAL AND SUPPORTS AUTO-MDIX OPERATION.

4. OPERATING TEMPERATURE: 0°C ~ 70°C

5 LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA.
LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. at IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. at IF=20mA
DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP. at IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. at IF=20mA
DOMINANT WAVELENGTH (λD): ORANGE 610 nm TYP. at IF=20mA
FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. at IF=20mA

6 RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

7 **TE CONNECTIVITY LOGO,PART NUMBER,DATE CODE,COUNTRY OF ORIGIN,AGENCY APPROVAL MARKING LOGO LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR,WW IS WORK WEEK,D IS DAY OF WEEK,WITH SUNDAY=1**

8. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK WAVE SOLDERING TEMPERATURE IS 265°C, 10sec MAX.

3.18	GREEN	YELLOW	8-1761889-8
3.18	GREEN	GREEN/YELLOW	2-1761889-3
3.18	GREEN/ORANGE	YELLOW	2-1761889-2
3.18	GREEN	GREEN	2-1761889-1
2.74	GREEN	YELLOW	6-1761889-8
3.18	GREEN/YELLOW	GREEN	1761889-8
2.74	GREEN	GREEN/YELLOW	1761889-3
2.74	GREEN/ORANGE	YELLOW	1761889-2
2.74	GREEN	GREEN	1761889-1
DIM A	LED1	LED2	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm
TOLERANCES, UNLESS OTHERWISE SPECIFIED:
0 PLC ±0.25
1 PLC ±0.25
2 PLC ±0.25
3 PLC ±0.25
4 PLC ±0.25
ANGLES ±
FINISH
MATERIAL
WEIGHT -

DWN KEITH ZHU 21-Jun-05
CHK TEDDY XIONG 21-Jun-05
APVD TEDDY XIONG 21-Jun-05
PRODUCT SPEC 108-2100
APPLICATION SPEC -

TE Connectivity

NAME 1X4 MAG(45) MODULAR JACK LOW PROFILE, 1G27 GIGABIT CIRCUIT SHIELDED, WITH LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-1761889	-

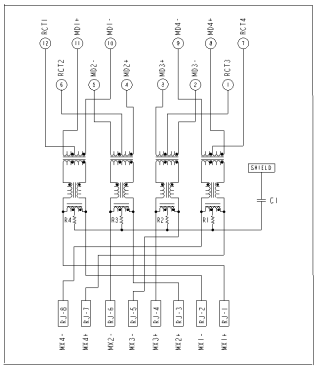
CUSTOMER DRAWING | SCALE NTS | SHEET 1 OF 2 | REV F

LOC	DIST
AA	00

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	-	SEE SHEET 1	-	-	-

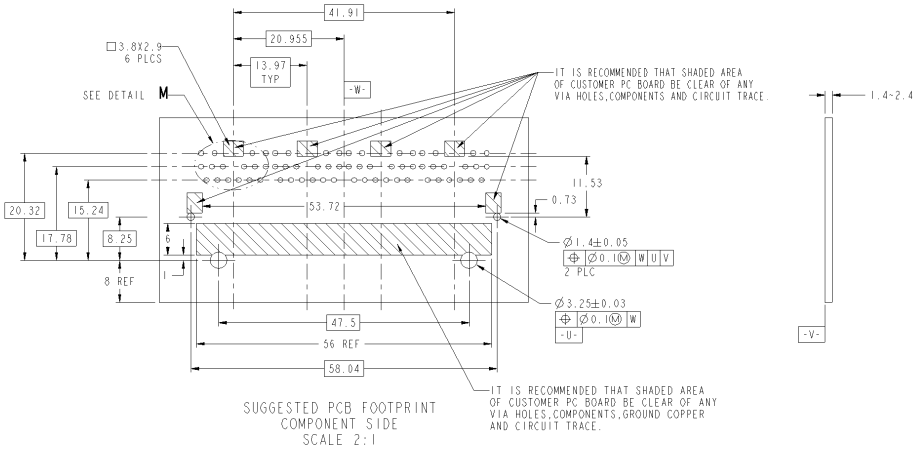
IG27 GIGABIT CIRCUIT 2 3



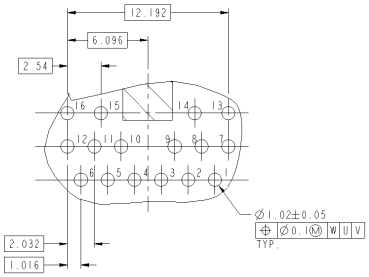
R1-R4=75 Ohms, 5%, 1/16W RESISTORS

C1=1000pF, 3kV CAPACITOR FOR 1761889-8,6-1761889-8,8-1761889-8 ONLY.

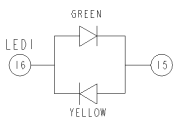
C1=1000pF, 2kV CAPACITOR FOR 1761889-1/-2/-3 & 2-1761889-1/-2/-3 ONLY.



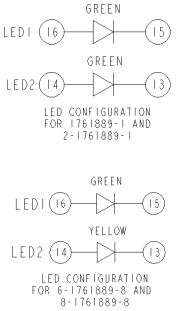
SUGGESTED PCB FOOTPRINT
COMPONENT SIDE
SCALE 2:1



DETAIL M
SCALE 5:1
TYP

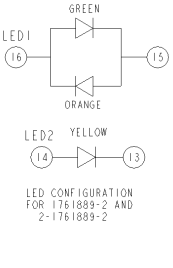


LED CONFIGURATION
FOR 1761889-8

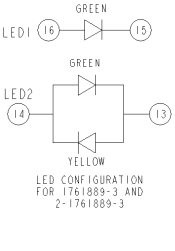


LED CONFIGURATION
FOR 6-1761889-8 AND
8-1761889-8

LED CONFIGURATION
FOR 1761889-1 AND
2-1761889-1



LED CONFIGURATION
FOR 1761889-2 AND
2-1761889-2



LED CONFIGURATION
FOR 1761889-3 AND
2-1761889-3

THIS DRAWING IS A CONTROLLED DOCUMENT.

DWN	KEITH ZHU	21-Jun-05
CHK	TEDDY XIONG	21-Jun-05
APVD	TEDDY XIONG	21-Jun-05
PRODUCT SPEC	108-2100	
APPLICATION SPEC	-	
WEIGHT	-	

DIMENSIONS: mm
TOLERANCES, UNLESS OTHERWISE SPECIFIED:
0 PLC ±0.25
1 PLC ±0.25
2 PLC ±0.25
3 PLC ±0.25
4 PLC ±0.25
ANGLES ±
MATERIAL -
FINISH -

TE Connectivity

NAME: 1X4 MAG(45) MODULAR JACK
LOW PROFILE, IG27 GIGABIT CIRCUIT
SHIELDED, WITH LEDS

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-1761889	-

CUSTOMER DRAWING | SCALE NTS | SHEET 2 OF 2 | REV E