

4

3

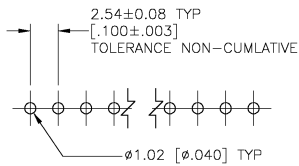
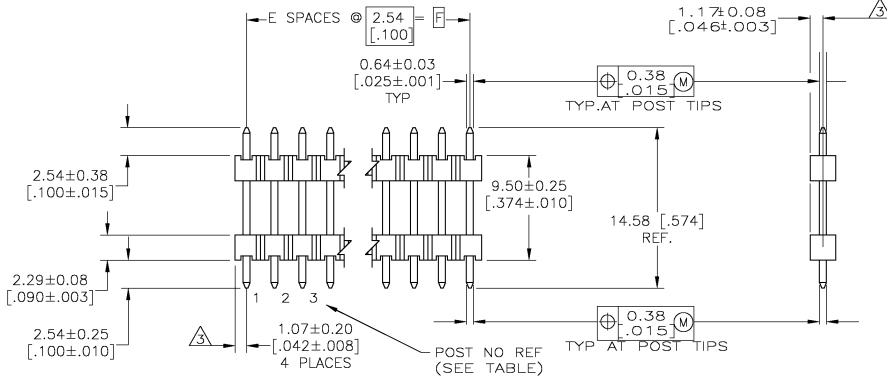
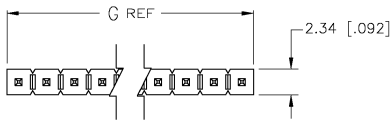
2

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LOC	DIST	REVISIONS			
P	LTR	DESCRIPTION	DATE	OWN	APPD
AD	00				
	D	REV PER 0G3C-0058	06APR04	MB	JG

- ASSEMBLY MAY BE BROKEN TO THE DESIRED NUMBER OF POSITIONS
 - TRUE POSITION TOLERANCE OF THE POST TIPS APPLIES WHEN THE HEADER IS HELD FLAT AGAINST THE PRINTED CIRCUIT BOARD
- △ THE NOTED DIMENSIONS APPLY AT THE INTERSECTION OF THE POST AND HOUSING
- △ HOUSING: LCP, COLOR-BLACK.
- △ FINISH: 0.000254-0.00508 [.000100-.00200] MIN MATTE TIN-LEAD OVER 0.00127 [.000050] MIN NICKEL ENTIRE POST.



2	19.91 [.784]	17.78 [.700]	7	-	7	8	147286-2
2	19.91 [.784]	17.78 [.700]	7	1,3,7,8	7	8	147286-1
POST OMITTED	G	F	E	HOLDDOWN POST CONFIG LOCATION	NO. OF ACTIVE POSN	NO. OF POSITIONS	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 26JUL00	TYCO ELECTRONICS CORPORATION
DRAWN BY: S. HAYMAKER	CHKD BY: J. GEFSEORD	23JUN99	Harrisburg, PA 17105
APPD BY: J. GEFSEORD	23JUN99	NAME:	HEADER ASSEMBLY, MOD II, STACKING, SINGLE ROW, .025 SQ. POST, UNSHROUDED, SELECT LOAD, HIGH TEMP, RET LEGS
MATERIAL: -	FINISH: -	SEE DATE CODE DRAWING NO:	A2 00779 ©=147286
CUSTOMER DRAWING		SCALE: 4:1	SHEET 1 OF 1 REV D

AMP 1471-9 REV 31MAY2000

26JUL00 10:13am AMP33743

147286

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