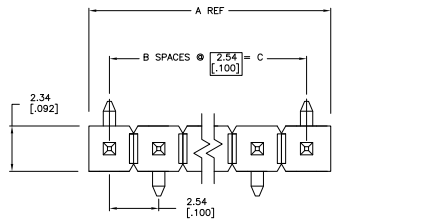
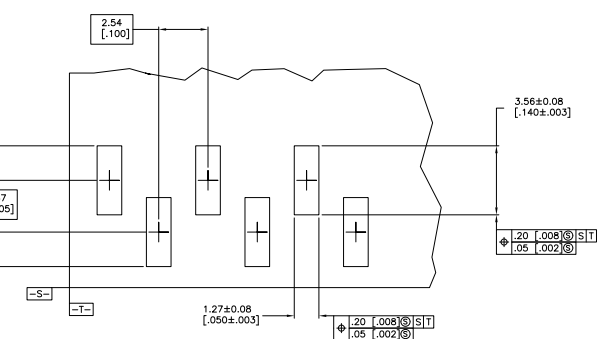
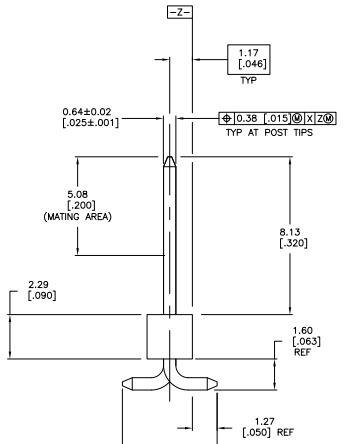
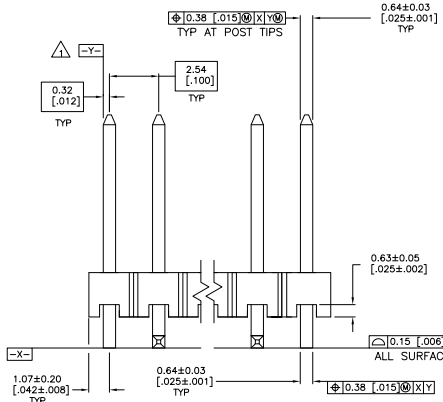


REV	DATE	DESCRIPTION	BY	APP
J	02 02 03 0826 04		1MFBMS	BSV/JLG
J1	REVISED PER ECO-09-020932		0789P9	KKK/JAG



- △ THE NOTED DIMENSIONS APPLY AT THE INTERSECTION OF THE POST AND THE HOUSING.
- △ 0.000762[.000030] GOLD ON CONTACT AREA, 0.00254[.000100]-0.00508[.000200] MATTE TIN-LEAD ON SOLDER TAIL, ALL OVER 0.00127[.000050] NICKEL.
- △ HOUSING: LCP, COLOR-BLACK. POSTS: PHOSPHOR BRONZE.
- △ 0.000762[.000030] GOLD ON CONTACT AREA, 0.00254[.000100]-0.00508[.000200] MATTE TIN ON SOLDER TAIL, ALL OVER 0.00127[.000050] NICKEL.
- △ OBSOLETE PARTS: OBSOLETE CIS STREAMLINING PER D.RENAUD/D.SINISI



RECOMMENDED PCB LAYOUT FOR USE WITH .008 THICK STENCIL

PLATING	C	B	A	NO OF POSN	PART NUMBER
△ OBSOLETE	△	△	△	35.56 [1.400]	14 35.69 [1.484]
△ OBSOLETE	△	△	△	33.02 [1.300]	13 33.15 [1.384]
△ OBSOLETE	△	△	△	30.48 [1.200]	12 30.61 [1.284]
△ OBSOLETE	△	△	△	27.94 [1.100]	11 28.07 [1.184]
△ OBSOLETE	△	△	△	25.40 [1.000]	10 27.53 [1.084]
△ OBSOLETE	△	△	△	22.86 [0.900]	9 22.45 [0.884]
△ OBSOLETE	△	△	△	20.32 [0.800]	8 22.45 [0.884]
△ OBSOLETE	△	△	△	17.78 [0.700]	7 19.91 [0.784]
△ OBSOLETE	△	△	△	15.24 [0.600]	6 17.37 [0.684]
△ OBSOLETE	△	△	△	12.70 [0.500]	5 14.83 [0.584]
△ OBSOLETE	△	△	△	10.16 [0.400]	4 12.29 [0.484]
△ OBSOLETE	△	△	△	7.62 [0.300]	3 9.75 [0.384]
△ OBSOLETE	△	△	△	5.08 [0.200]	2 7.21 [0.284]
△ OBSOLETE	△	△	△	35.56 [1.400]	14 35.69 [1.484]
△ OBSOLETE	△	△	△	33.02 [1.300]	13 33.15 [1.384]
△ SUPERSEDED	△	△	△	30.48 [1.200]	12 30.61 [1.284]
△ SUPERSEDED	△	△	△	27.94 [1.100]	11 28.07 [1.184]
△ OBSOLETE	△	△	△	25.40 [1.000]	10 27.53 [1.084]
△ OBSOLETE	△	△	△	22.86 [0.900]	9 22.45 [0.884]
△ SUPERSEDED	△	△	△	20.32 [0.800]	8 22.45 [0.884]
△ SUPERSEDED	△	△	△	17.78 [0.700]	7 19.91 [0.784]
△ OBSOLETE	△	△	△	15.24 [0.600]	6 17.37 [0.684]
△ SUPERSEDED	△	△	△	12.70 [0.500]	5 14.83 [0.584]
△ SUPERSEDED	△	△	△	10.16 [0.400]	4 12.29 [0.484]
△ SUPERSEDED	△	△	△	7.62 [0.300]	3 9.75 [0.384]
△ SUPERSEDED	△	△	△	5.08 [0.200]	2 7.21 [0.284]

THIS DRAWING IS A CONTROLLED DOCUMENT. SEE TABLE

DATE: 03-31-04
 DESIGNED BY: JAG
 CHECKED BY: JAG
 DRAWN BY: JAG
 DATE: 03-31-04
 DESIGNED BY: JAG
 CHECKED BY: JAG
 DRAWN BY: JAG

TI Electronics Corporation
 Harrisburg, Pa 17105-3008

SURFACE MOUNT HEADER ASSEMBLY,
 MOD II, BREAKAWAY, VERTICAL, SINGLE ROW,
 100 X 100 Q/L, 025 SOLDER POST

DATE: 03-31-04
 DRAWING NO: A100779
 CUSTOMER DRAWING: 146129