

## Active Errata List

- During UART Reception, Clearing REN may Generate Unexpected IT
- 32 kHz Oscillator Needs External Feedback Resistor
- Timer 2 – Baud Rate Generator – Long Start Time
- Strech MOVX Does Not Work

## Errata History

Lot Number	Errata List
All	1, 2, 3, 4

## Errata Description

### 1. During UART Reception, Clearing REN May Generate Unexpected IT

During UART reception, if the REN bit is cleared between a start bit detection and the end of reception, the UART will not discard the data (RI is set).

#### Workaround

Test REN at the beginning of Interrupt routine just after CLR RI, and run the Interrupt routine code only if REN is set.

### 2. 32 kHz Oscillator Needs External Feedback Resistor

Internal feedback resistor is not connected, to start up, the 32 kHz Oscillator needs an external resistor.

#### Workaround

Connect XTALB1 and XTALB2 between a 2 M $\Omega$  resistor.

### 3. Timer 2 – Baud Rate Generator – Long Start Time

When Timer 2 is used as baud rate generator, TH2 is not loaded with RCAP2H at the beginning, then UART is not operational before 10000 machine cycles.

#### Workaround

Add the initialisation of TH2 and TL2 in the initialisation of Timer 2.

### 4. Strech MOVX Does not Work

When setting M0 bit in AUXR sfr (08Eh), the RD# or WR # pulse on a MOVX instruction on external memory is not 30 XTAL length but always standart 6 XTAL length. Thus slow external peripherals mapped in the XDATA space could not work properly.

#### Workaround

None.



80C51 MCUs

AT89C51IC2

Errata Sheet

Rev. 4107A-8051-06/02





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