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## Active Errata List

- PCA – Incorrect Behavior with CPU X2 Mode Bit of HSB
- Timer0/1 – Extra Interrupt

## Errata History

| Lot Number | Errata List |
|------------|-------------|
| All lots   | 1, 2        |

## Errata Description

### 1. PCA – Incorrect Behavior with CPU X2 Mode Bit of HSB

When starting the microcontroller in X2 mode upon reset with the X2 fuse bit of the HSB, the PCA may not work properly when configured with Timer0 in X1 mode as clock input.

#### Workaround

Set the CPU in X2 mode by software by writing CKCON register at the begin of the application.

### 2. Timer0/1 – Extra Interrupt

When Timer0 is in X1 mode and Timer1 in X2 mode and vice versa, extra interrupt may randomly occur for Timer0 or Timer1.

#### Workaround

Use the same mode for the two timers.



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## C51 Microcontrollers

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## AT89C51AC3 Errata Sheet

4387A-8051-10/04





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