

AS1581

5A Ultra Low Dropout Voltage Regulator Fast Response, Adjustable & Fixed

FEATURES

- Low Dropout Voltage 500mV at 5A Full Load Current
- Adjustable Output Down to 1.2V from ATX Power Supply
- Fixed Output Voltages of 3.3V, 2.8V & 2.5V
- Extremely Tight Voltage and Line Regulation
- Standard 5-Terminal Low Cost TO-220 & TO-263

APPLICATIONS

- 3.3V to 2.8V ATX Power Supplies
- 3.3V to 2.9V for Portable PENTIUM™ Processor
- 5V to 3.5V VRE Supply
- High Efficiency "Green" Computer Systems

PRODUCT DESCRIPTION

The Alpha Semiconductor AS1581 is a 5A Low Dropout Regulator with extremely low dropout voltage. The adjustable version requires only two external resistors to set the output voltage. The fixed version has a preset output of 3.3V, 2.8V or 2.5V and does not require any external resistors. The AS1581 features a low dropout of less than 400mV(typ.) and offers fast transient response. This device is suitable for Pentium applications requiring 2.8V or 2.5V from 3.3V ATX power supplies, where a low current input voltage 1V greater than the output voltage is needed. With an external sense pin the load regulation is less than 1mV. This device is an excellent choice for the use in powering low voltage microprocessors that require a lower dropout, fast transient response to regulate from 3.3V and 5V supplies. The AS1581 is also an excellent choice as a post regulator for switching supplies applications.

The AS1581 offers full protection against over-current faults, reversed input polarity, over temperature operation and positive and negative transient voltage.

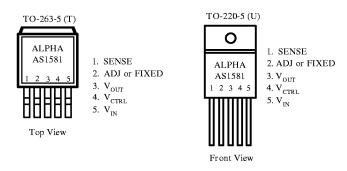
The AS1581 is offered in a 5 pin TO-220 and TO-263 compatible with industry standard 5-terminal regulators. For 7A, 3A and 1.5A ultra low dropout versions refer to AS1580, AS1582 and AS1583 data sheets respectively.

ORDERING INFORMATION

TO-22O	TO-263	OUTPUT		
5-PIN	5-PIN			
AS1581U	AS1581T	Adjustable		
AS1581U-X	AS1581T-X	Fixed		

Consult with factory for other fixed output voltages. X= Output Voltage (3.3V, 2.8V & 2.5V)

PIN CONNECTIONS



Alpha Semiconductor Inc. 1031 Serpentine Lane. Pleasanton, CA 94566 Tel: (925) 417-1390 Fax: (925) 417-1391

ABSOLUTE MAXIMUM RATINGS

Power Dissipation	Internally Limited	Input Supply Voltage69
Lead Temp (soldering, 10 seconds)	300°C	V _{CTRL} Input Voltage
Storage Temperature Range	65°C to +150°C	
Operating Junction Temperature Range		
AS1581 Control Section	0°C to +125°C	
AS1581 Power Transistor	0°C to +150°C	

ELECTRICAL CHARACTERISTICS at V_s =14V, Ta=25°C, Io=10mA, C2=100 μ F, unless otherwise specified. (Note 1)

 $(\underline{Boldface\ applies\ over\ full\ temperature\ range}).$

Parameters	Conditions	M:	AS1581	М	Units	
2.5V Version			Min	Тур	Max	<u> </u>
Output Voltage	V _{CTRL} =6.0V to 12V, V	V _{IN} =3.0V to 5.0V, I _O =10mA	2.450	2.500	2.550	V
2.8V Version		I ₀ =10mA to 5A	2.400		2.600	1
Output Voltage	V_{CTRL} =6.3V to 12V, V_{IN} =3.3V to 12V, I_{O} =10mA			2.800	2.856	V
2 27/7/		2.688		2.912		
3.3V Version						
Output Voltage	V _{CTB1} = 6.3V to 12V V	V _{CIRL} =6.3V to 12V, V _{IN} =3.3V to 12V, I _O =10mA		3.300	3.366	l v
ouput vollage	CIRL OIS CO 12 C,	3.234 3.168	3.500	3.462		
All Voltage Options				l	ı	<u> </u>
Reference Voltage	V_{CTRL} =2.75V, V_{IN} =2. V_{CTRL} =2.7V to 12V, V	1.238	1.250	1.263	V	
Line Regulation		V _{CTRL} =2.5V to 12V, V _{IN} =1.75V to 5.5V, I _O =10mA				
	V _{ADJ} =0V		1.0	3.0	mV	
Load Regulation (Note1)	V_{CTRL} =2.75V, V_{IN} =2.		1.0	5.0	mV	
Dropout Voltage Minimum V _{CTRL} (Note2)	V _{ADJ} =0V	V _{ADI} =0V			1.25	V
$(V_{CTRL} - V_{OUT})$	$V_{IN}=2.05V, I_O=1A$					
Dropout Voltage Minimum V _{IN} (Note2)	V _{ADJ} =0V		0.40	0.50	V	
$(V_{IN} - V_{OUT})$ Current Limit	V_{IN} =2.75V, I_O =5A V_{CTRL} =2.75V, V_{IN} =2.0	5.1			A	
Minimum Load Current	$V_{\text{CTRL}}=5V, V_{\text{IN}}=3.3V,$		5	10	mA	
Thermal Regulation	30ms Pulse		0.002	0.02	%W	
Ripple Rejection	V _{CIRL} =3.75V V _{IN} =3.75V, I _O =2.5A, V _{ADJ} =0V		60	80		dB
Control Pin Current	$T_J=25$, $V_{RIPPLE}=1$ Vpp at 120Hz $V_{ADI}=0$ V			60	120	
Control i in Current	$V_{\text{ADJ}} = 0.00$ $V_{\text{CTRL}} = 2.75$ V, $V_{\text{IN}} = 2.05$ V, $I_{\text{O}} = 5$ A				120	mA
Adjustable Pin Current	V _{CTRL} =2.75V, V _{IN} =2.0		50	120	μΑ	
Thermal Resistance	TO-220-5	Junction to Case (θ _{JC})			3	°C/W
		Junction to Ambient (θ_{JA})			50	°C/W
	TO-263-5	Junction to Case (θ _{JC})			3	°C/W
		Junction to Ambient (θ_{JA})			60	°C/W

The Bold specifications applying to the over full operating temperature range.

Note 1: Low duty cycle pulse testing with Kelvin connections are required to order to maintain accurate data.

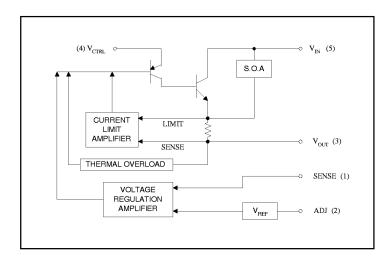
Note 2: Dropout voltage is defined as the minimum differential between V_{IN} and V_{OUT} or V_{CTRL} and V_{OUT} required to maintain regulation at V_{OUT} 99% Nominal V_{OUT} .

Note 3: V_{REF} is measured across the Adjust pin to Sense pin.

PIN DESCRIPTION

- Sense = Allows Kelvin sense of V_{OUT} at the load. (Positive side of the reference voltage of the device).
- 1. ADJ = Negative side of the reference voltage for the device. Adding a small bypass capacitor from the ADJ pin to ground will improve the transient response.
- 1. V_{OUT} = Power output of the device.
- 4. V_{CTRL} = Supply pin for the control circuitry of the device. The current flow into this pin will be about 1% of the output current. V_{CTRL} must be between 1.0V and 1.3V greater than the output voltage for the device to regulate.
- 4. V_{IN} = Output load current is supplied through this pin. V_{IN} must be between 0.1V and 0.8V greater than the output voltage for the device to regulate.

BLOCK DIAGRAM



APPLICATIONS NOTES

The AS1581 is designed as a high performance and low cost solution for application requiring a lower dropout than traditional NPN regulators.

The AS1581 uses a separate input voltage V_{CTRL} ($V_{CTRL} \ge V_{OUT} + 1.3V$) to minimize the dropout voltage. This allows the 2.5V power for the load to come from a 3.3V system supply. As added benefit this will reduce the heat dissipation, and lower heatsink and cooling fan cost. A typical application would use 5V for Vin and 3.3V for V_{CTRL} from a motherboard power supply to provide a nominal 2.5V output. Using the sense pin allows to Kelvin measure the output, reducing resistive-associated errors.

The AS1581 can power the 2.5V core voltage for microprocessors such as PentiumTM, P55CTM, AMD5k86TM and K6TM and the IBM PowerPCTM 603EV and 604EV processors.

Adjustable Regulator Design

1.25V reference voltage is being developed between the SENSE pin and the ADJ pin of the AS1581. Adding two external resistors (see fig 1.) will allow setting the output voltage from 1.25V to 6V. $R_{\rm 1}$ is chosen so that this current is specified minimum load current of 10mA. $R_{\rm 2}$ is given by the formula: $V_{\rm OUT} = V_{\rm REF} \, (1 + R_{\rm 2}/R_{\rm 1}) + I_{\rm ADJ} \, (R_{\rm 2})$. The current flowing from the ADJ pin is typically 50 μ A. This ADJ pin contributes to the final VOUT but is usually neglected. Connecting the sense pin to the top of the resistor divider will improve load regulation.

Lowering Noise

Using the SENSE pin to Kelvin the load will increase accuracy of the output voltage during load regulation. For the fixed voltage devices, adding a capacitor at the GND pin will improve transient response. This capacitor is chosen in the range of $1\mu F$ to $0.1\mu F$ and will depend on the amount of output capacitance in the system.

^{*}The reduction of heat dissipation is a result of the increase of the regulator efficiency (efficiency = $V_{\rm OUT}$ / $V_{\rm IN}$).

TYPICAL APPLICATION

Fig. 1 Adjustable Regulator

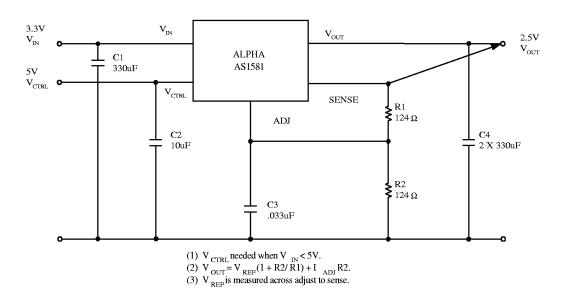


Fig.2 Typical Fixed Regulator

