B-8

2N3994, 2N3994A

P-Channel Silicon Junction Field-Effect Transistor

- Choppers
- High Speed Commutators

Absolute maximum ratings at $T_A = 25 \,^{\circ}C$

Reverse Gate Source Voltage 25 V
Reverse Gate Drain Voltage 25 V
Continuous Forward Gate Current - 10 mA
Continuous Device Power Dissipation 300 mW
Power Derating 2.4 mW/°C

At 25°C free air temperature:		2N3994		2N3994A		Process PJ99			
Static Electrical Characteristics		Min	Max	Min	Max	Unit	Test Conditions		
Gate Source Breakdown Voltage	V _{(BR)GSS}	25		25		V	$I_G = 1 \mu A, V_{DS} = \emptyset V$		
Gate Source Cutoff Voltage	V _{GS(OFF)}	1	5.5	1	5.5	V	V _{DS} = - 10 V, I _D = - 1 μA		
Drain Saturation Current (Pulsed)	I _{DSS}	- 2		- 2		mA	V _{DS} = - 10 V, V _{GS} = Ø V		
Drain Reverse Current	I _{DGO}		- 1.2		- 1.2	nA	V _{DG} = - 15 V, I _S = ØA		
			- 1.2		- 1.2	μΑ	V _{DG} = - 15 V, I _S = ØA	T _A = 150°C	
Drain Cutoff Current	I _{D(OFF)}		- 1.2		- 1.2	nA	V _{DS} = - 10 V, V _{GS} = 10 V		
			- 1		- 1	μΑ	$V_{DS} = -10 \text{V}, V_{GS} = 10 \text{V}$	T _A = 150°C	

Dynamic Electrical Characteristics

Drain Source ON Resistance	r _{ds(on)}		300		300	Ω	$V_{GS} = \emptyset V$, $I_D = \emptyset A$	f = 1 kHz
Common Source Forward Transmittance	Y _{fs}	4	10	5	10	mS	V _{DS} = -10 V, V _{GS} = Ø V	f = 1 kHz
Common Source Input Capacitance	C _{iss}		16		12	pF	$V_{DS} = -10 V$, $V_{GS} = \emptyset V$	f = 1 MHz
Common Source Reverse Transfer Capacitance	C _{rss}		5		3.5	pF	V _{DS} = Ø, V _{GS} = 10 V	f = 1 MHz

TO-72 Package Dimensions in Inches (mm) **Pin Configuration** 1 Source, 2 Gate, 3 Drain, 4 Case

