



复旦微电子

FM7843

Touch Screen Controller

Specification

May. 2008

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Product Overview

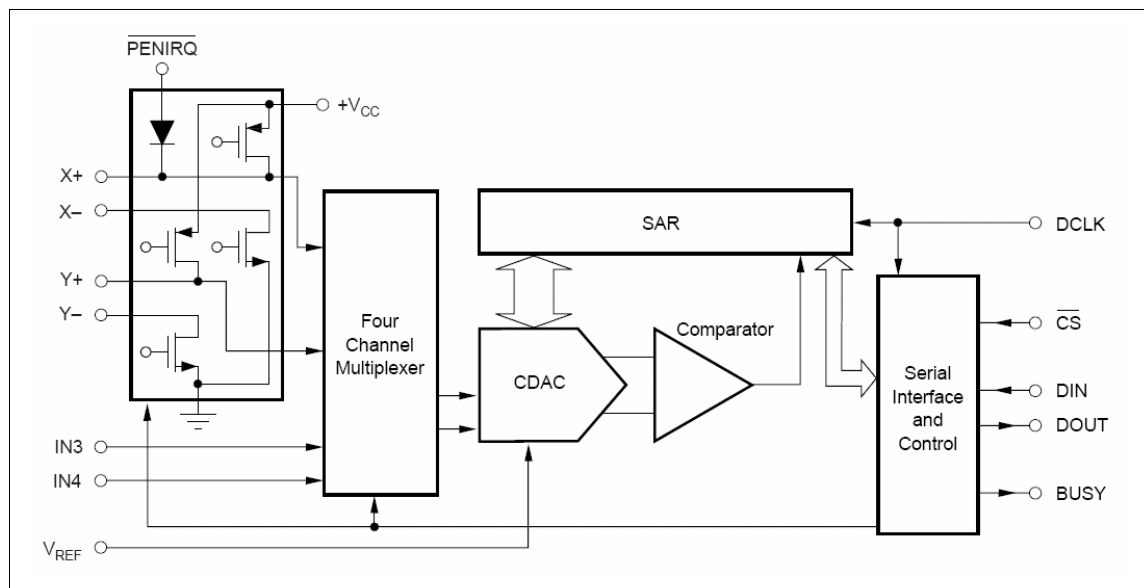
Instruction

FM7843 is a 4-wire resistive touch screen input controller integrated circuit. The device is a 12-bit analog-to-digital converter with a synchronous serial interface and touch screen driving circuit. It has a shutdown mode, in which the power dissipation of the device is as low as $0.5\mu\text{W}$. Fully compatible with Burr-Brown' ADS7843, this device is ideal to be used in personal digital assistant (PDA) and portable instruments applications.

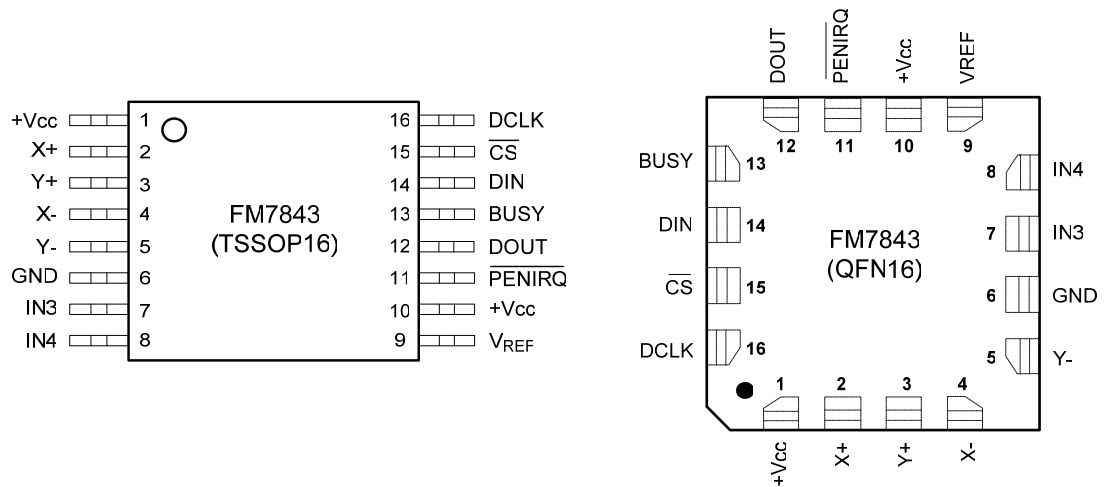
Features

- ◆ Realizing the driver selection of touch screen
- ◆ Analog-to-digital conversion on input voltage or auxiliary voltage
- ◆ Synchronous serial interface
- ◆ Up to 125kHz conversion rate
- ◆ Programmable 8-bit or 12-bit resolution
- ◆ Single supply: 2.7V to 5V
- ◆ 2 auxiliary analog inputs

Block Diagram



Pin Assignment



Pin Description

Symbol	Pin	Function
Vcc	1	Power supply 2.7v to 5v
X+	2	X+ Position input. ADC input Channel 1
Y+	3	Y+ Position input. ADC input Channel 2
X-	4	X- Position input
Y-	5	Y- Position input
GND	6	Ground
IN3	7	Auxiliary input 1. ADC input Channel 3
IN4	8	Auxiliary input 2. ADC input Channel 4
Vref	9	Voltage Reference input
Vcc	10	Power supply, 2.7v to 5v
PENIRQ	11	Pen interrupt. Open anode output (requires 10kΩ pull-up resistor externally).
DOUT	12	Serial Data Output. Data is shifted on the falling edge of, CS DCLK. This output is high impedance when CS is HIGH
BUSY	13	Busy Output. This output is high impedance when CS is HIGH
DIN	14	Serial Data input. If CS is LOW, data is latched on rising edge of DCLK.
CS	15	Chip Select Input, Conversion timing and enables the serial input/output register.
DCLK	16	External Clock Input. This clock runs the SAR conversion process and synchronizes serial data I/O.

Characteristics

Absolute Maximum Ratings

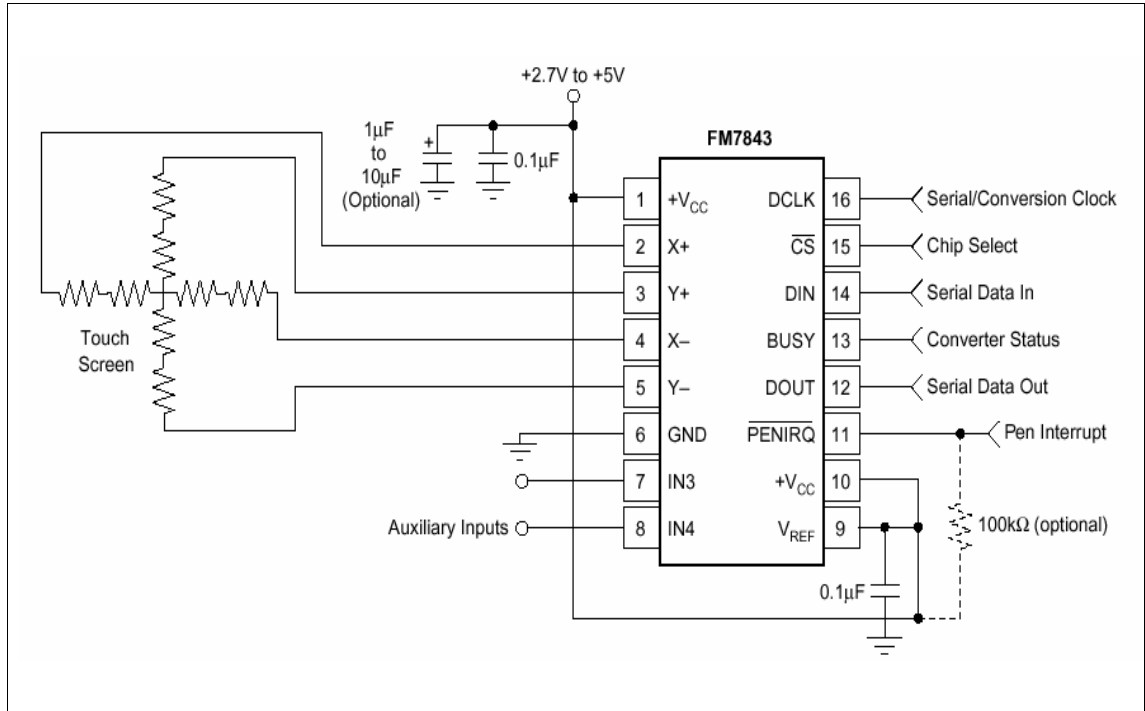
Symbol	Parameter	Min	Max	Unit
V_{CC}	Maximum Operation Voltage	-0.3	6	V
V_{IA}	Analog input voltage	-0.3	$V_{CC}+0.3$	V
V_{ID}	Digital input voltage	-0.3	$V_{CC}+0.3$	V
P_{TOT}	Total power dissipation		250	mW
T_{STG}	Storage temperature	-55	+150	°C
T_{OPR}	Operation temperature	-40	+85	°C

Electrical Characteristic

($V_{CC}=2.7V$, $V_{ref}=2.5V$, $f_{sample}=125KHz$, $f_{clk}=2MHz$)

	Parameter	Min	Typ	Max	Unit
Analog Input	Absolute input range	-0.2		$V_{CC} + 0.2$	V
	Capacitance		2.5		pF
	Leakage Current		0.1		μA
System Performance	Resolution		12		Bits
	No Missing Codes	11			Bits
	Integral Linearity Error			± 2	LSB
	Offset Error			± 6	LSB
	Gain Error			± 4	LSB
	Power Supply Rejection		70		dB
Sampling Dynamics	Conversion Time			12	Clk Cycles
	Acquisition Time	3			Clk Cycles
	Throughput Rate			125	KHz
Switch Drivers	On-Resistance		5		Ω
Power Supply Requirements	Quiescent Current		280	650	μA
	Shut Down Mode			3	μA
	Power Dissipation			1.8	mW

Application Example

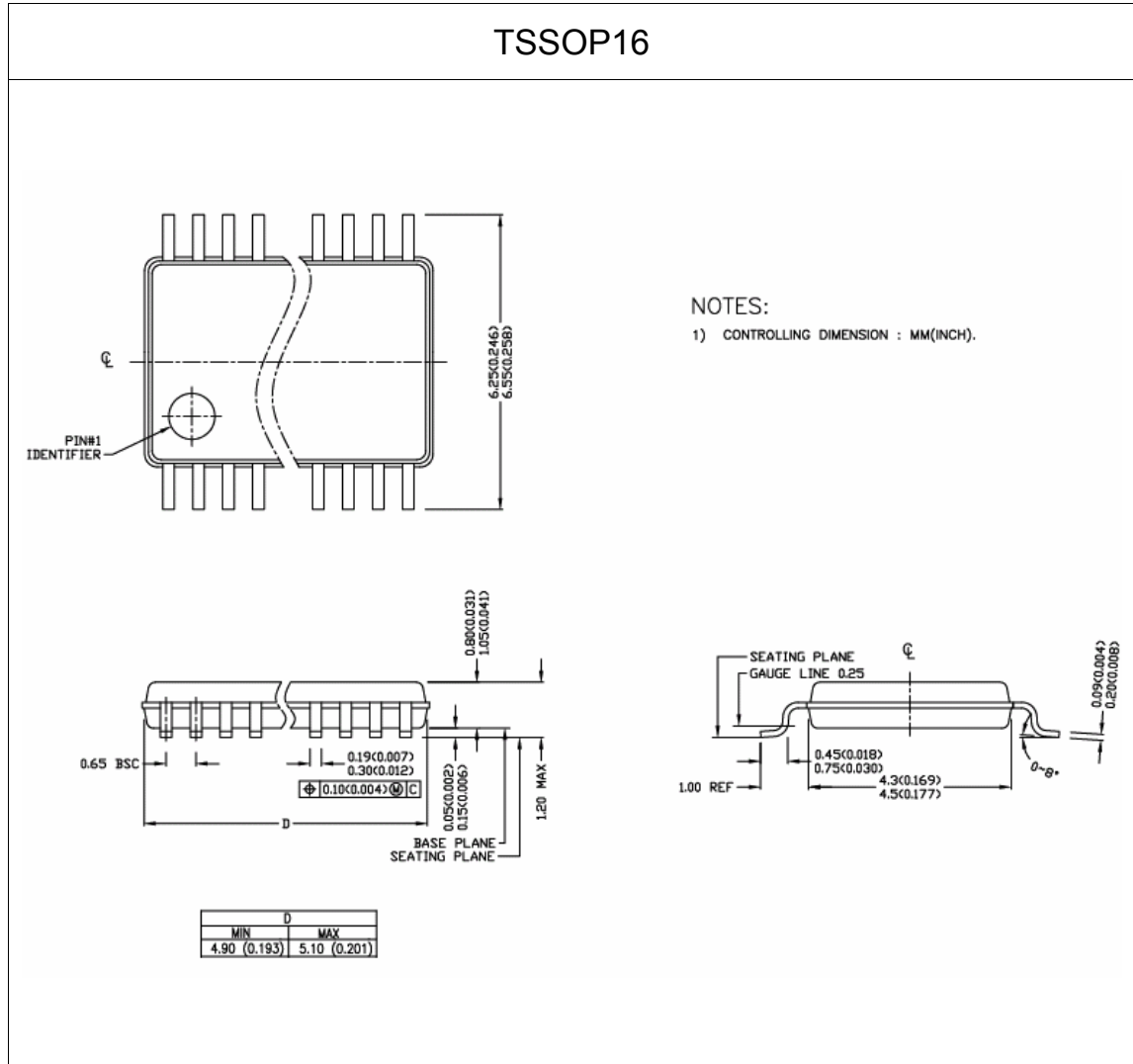




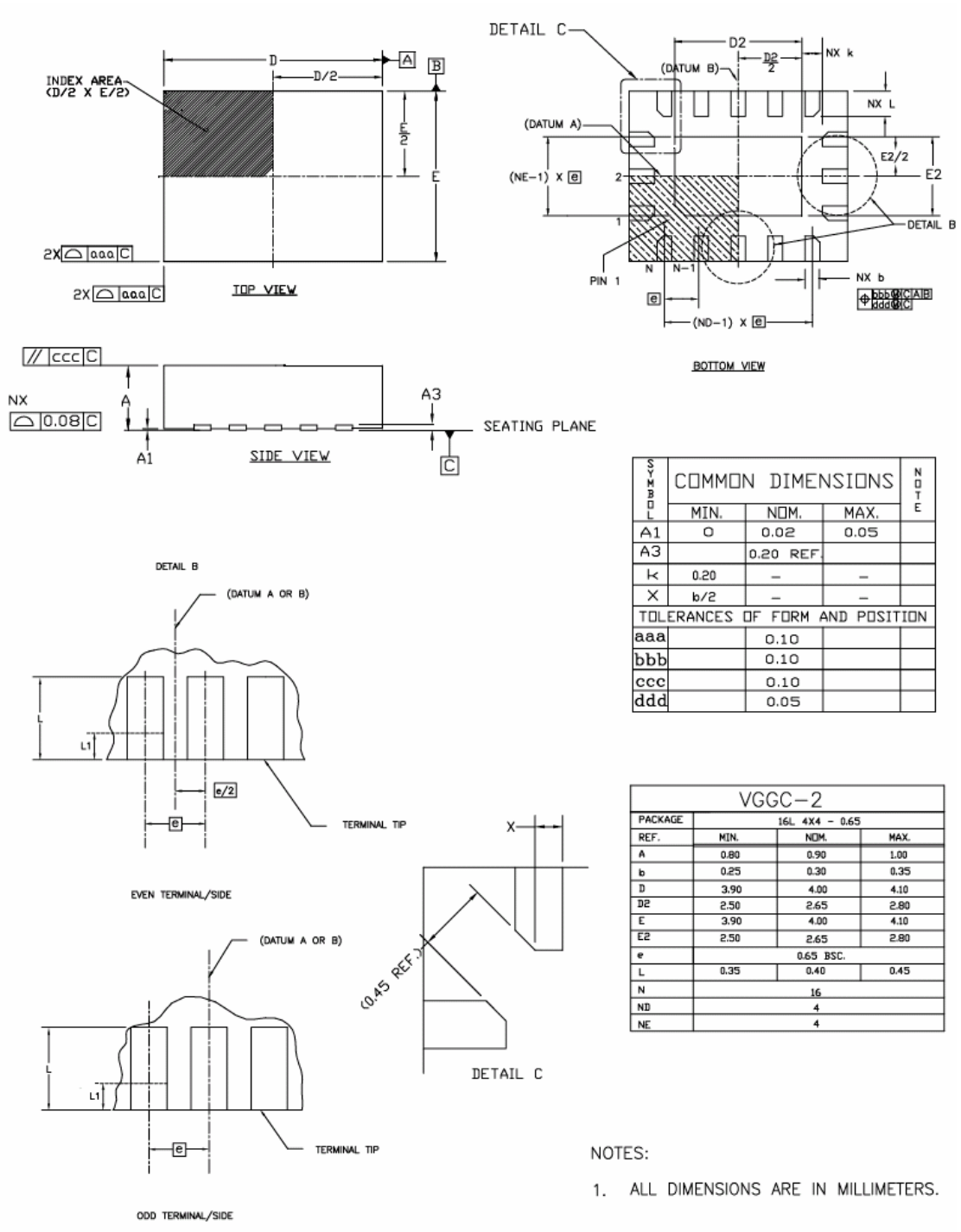
Ordering Information

Ordering Code	Package	Operation Temperature
FM7843-TS	TSSOP16	Industrial Temperature (-40°C ~ +85°C)
FM7843-QF	QFN16	

Package Dimensions



QFN16





Revision History

Version	Publication date	Pages	Paragraph or Illustration	Revise Description
1.0	Oct. 2007	11		Initial Release.
1.1	May. 2008	11	Sales and service	Updated the address of HK office.



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