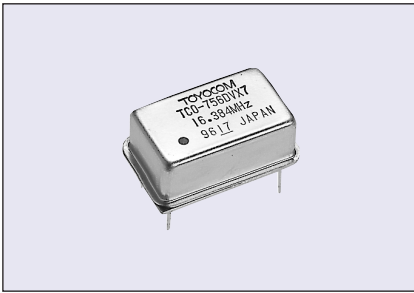


Crystal Clock Oscillator



TCO-756DVX7

FULL DIP Double-Sealed VCXO

Features

- CMOS logic output
- DIP-14 pin package compatible
- Hermetically double-sealed metal package
- Voltage controlled oscillator

Absolute Maximum Ratings

Parameter	Symbol	Rating
Supply voltage	V _{cc}	-0.5 to +7.0 V
Input voltage	V _{IN}	-0.5 to V _{cc} +0.5 V
Output voltage	V _O	-0.5 to V _{cc} +0.5 V
Output current	I _O	±25 mA
Storage temperature	T _{stg}	-40 to +85 °C

Specifications

Parameter		TCO-756DVX7	Conditions
Frequency	f _o	1.5 to 40 MHz	
Frequency Stability	Δf/f _o	±50 ppm max.	(*1)
Pullability		±150 ppm min.	at V _{cont} =0 to +5.0V, Ref=+2.5 V
Control Voltage Range	V _{cont}	+2.5 V ±2.5V	DC, Pin #1
Operating Temperature	T _{opr}	0 °C to +70 °C	
Supply Voltage	V _{cc}	+5.0 V ±5%	DC, Pin #14
Supply Current	I _{cc}	See Table A (max.)	V _{cc} =+5.25V
Output Voltage	V _{OH} V _{OL}	V _{OH} =V _{cc} -0.4 V min. / V _{OL} =+0.4 V max.	I _{OH} =-4mA Pin #8 I _{OL} =+4mA
Symmetry	SYM	40 to 60 %	at 50% V _{cc}
Rise/Fall time	t _r /t _f	See Table A (max.)	at 10% to 90% V _{cc}
Load Capacitance	C _L	30 pF max.	
Start-up time	t _{st}	4 ms max. 10 ms max.	1.5 ≤ f _o ≤ 26 MHz (*2) 26 < f _o ≤ 40 MHz (*2)

*1 Inclusive of calibration tolerance at +25°C, operating temperature, operating voltage range.

*2 Rise time (0 to +4.75V) of V_{cc} > 150μs

Package Outlines [Dimensions in mm]

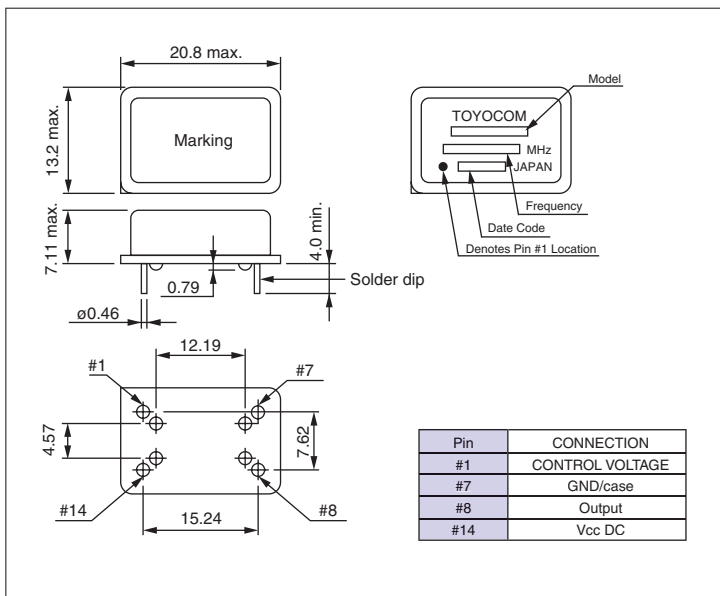


Table A

Freq. (MHz)	1.5 ≤ f _o ≤ 10	10 < f _o ≤ 26	26 < f _o ≤ 34	34 < f _o ≤ 40
I _{cc} (mA)	15	20	35	40
t _r , t _f (ns)	15	15	10	10

Test Circuit

See Test Circuit page TEST-7